1.Time: Current simulation time.

2. clk: Clock signal state (0 or 1).

3. WE (Write Enable): Determines whether data is written (1) or read (0).

4. Addr (Address): RAM address being accessed.

5. Din (Data Input): Data being written during a write operation.

6. Dout (Data Output): Data read from the RAM; shows xx if undefined (no read operation has occurred).