Class: CS1/CS2/CS3/CS4/CS5

Subject: Digital Logic Design Time Allowed: Till Next lab start

Name: Suvonkulov Abdulaziz

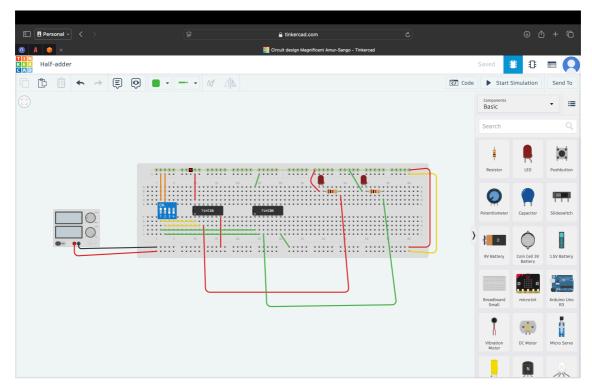
Task 1:

Date: Oct 28th- Nov 1st , 2024

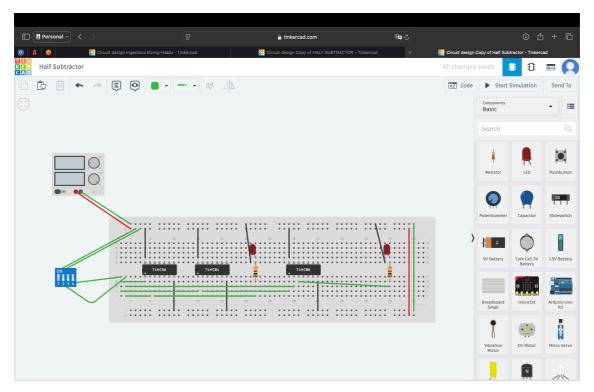
Instructor: Dr. M. Bilal Qureshi

Max Marks: 10

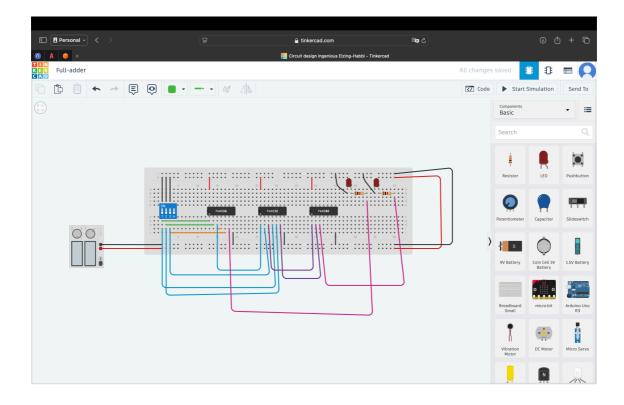
Student ID # 220456



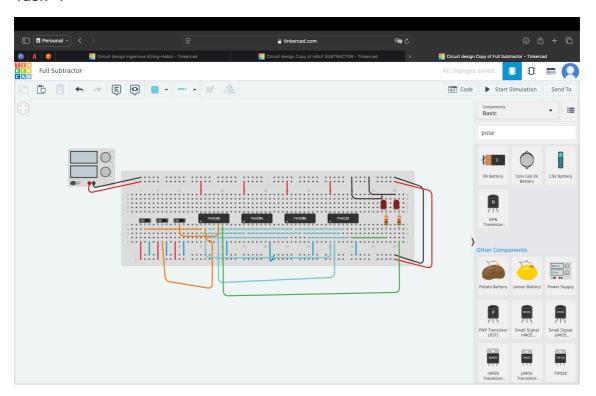
Task-2



Task-3



Task-4



Task-5

