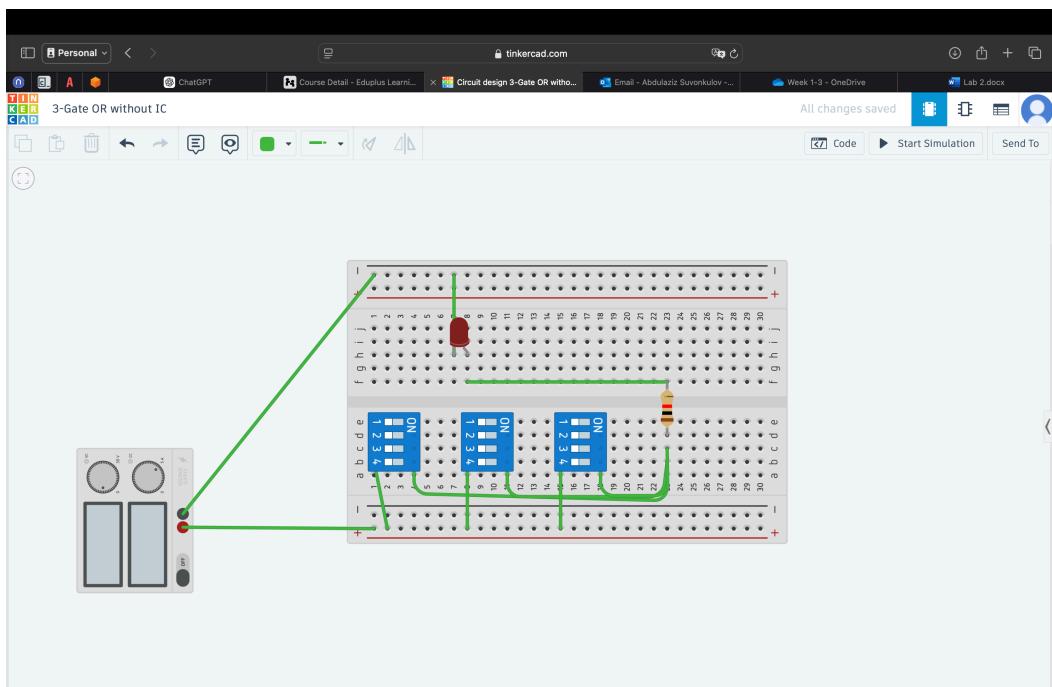
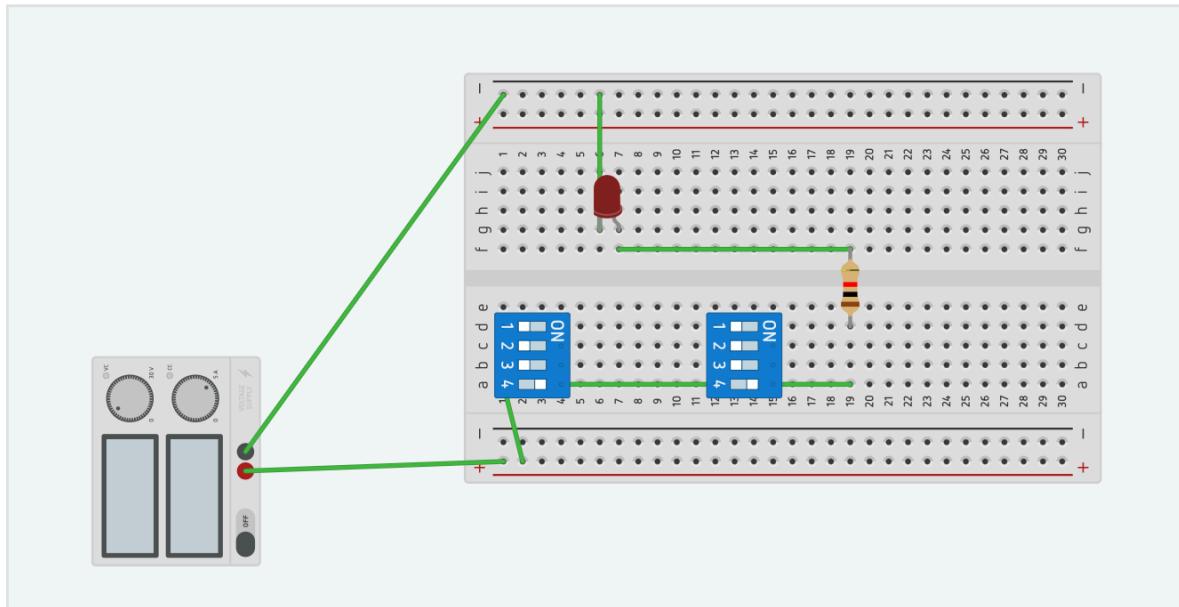
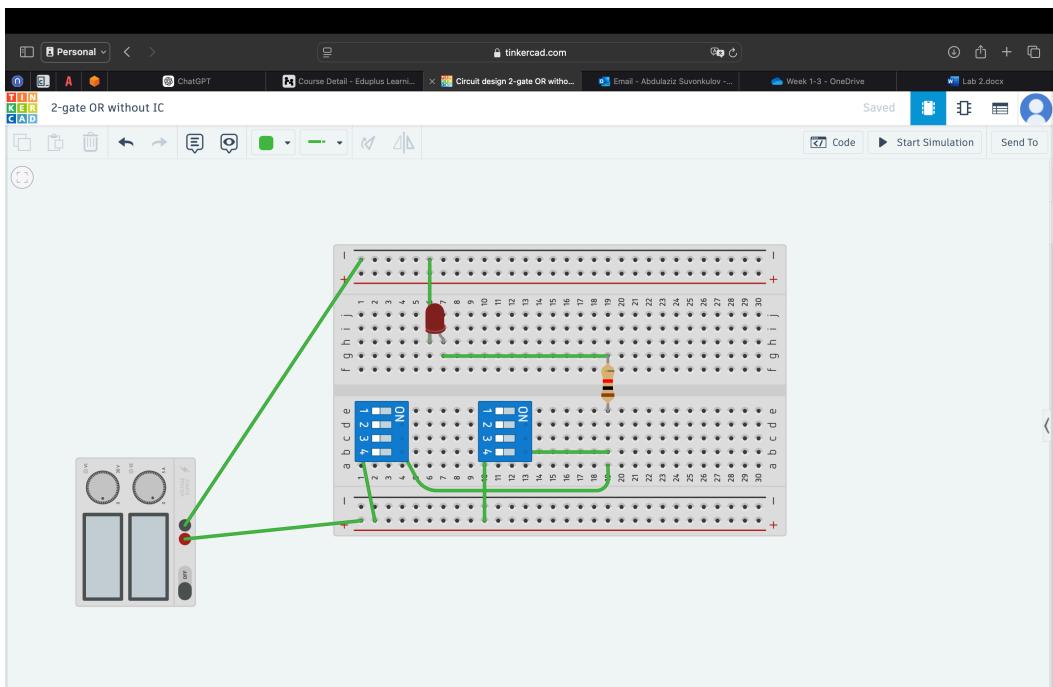
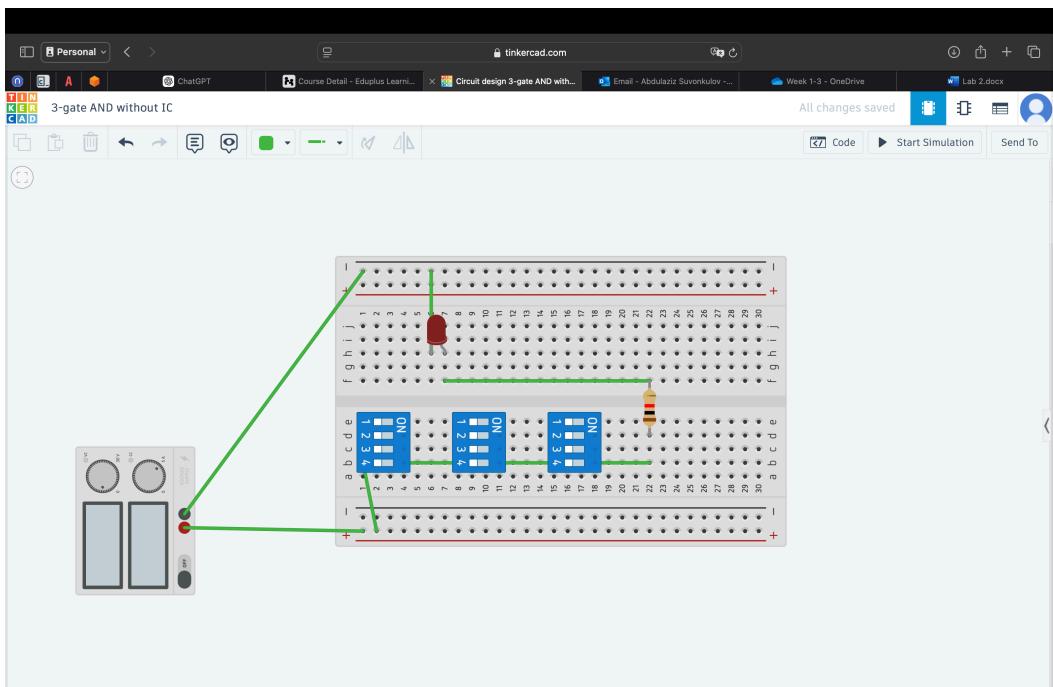


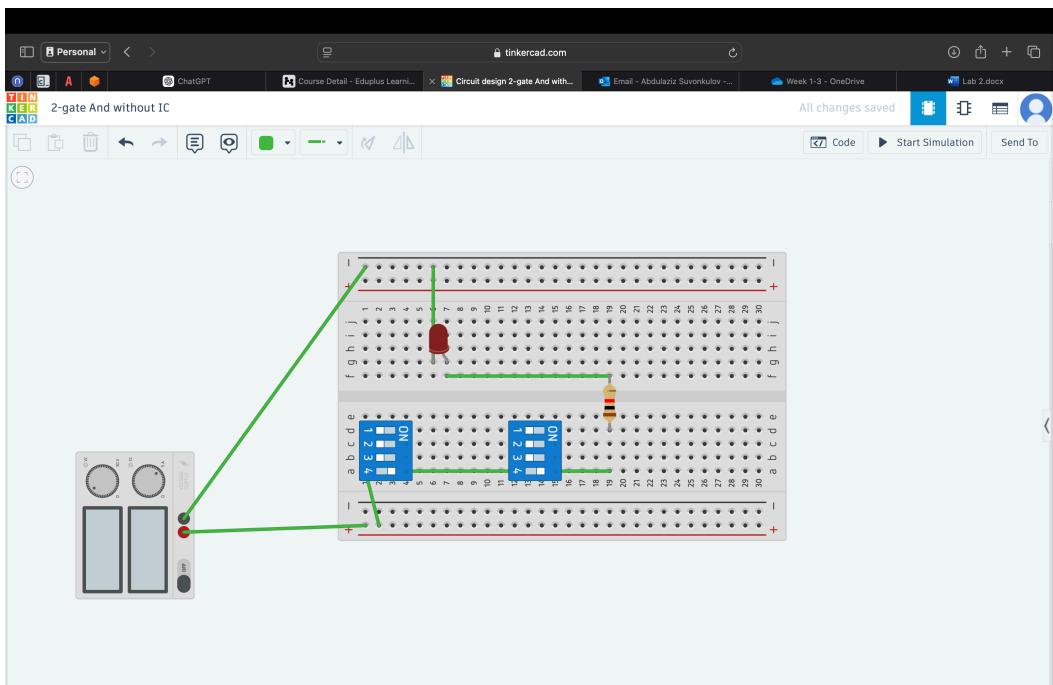
# LAB-1-2 DLD

HomeWork for lab 1 and 2 for ID 220456, Name: Suvonkulov Abdulaziz

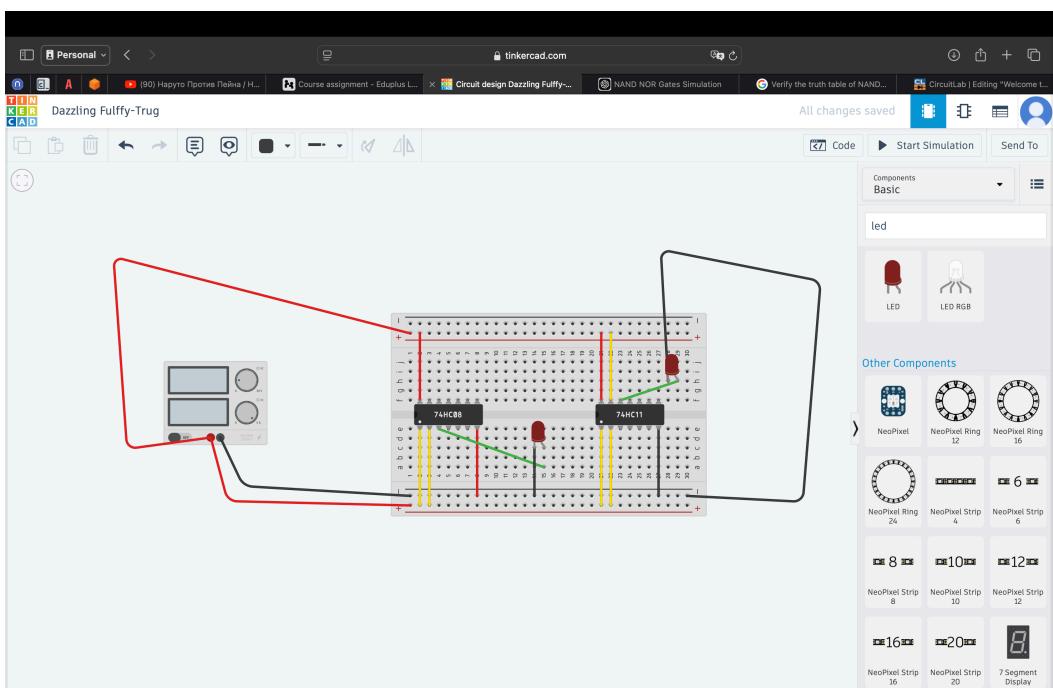
## TASK-1



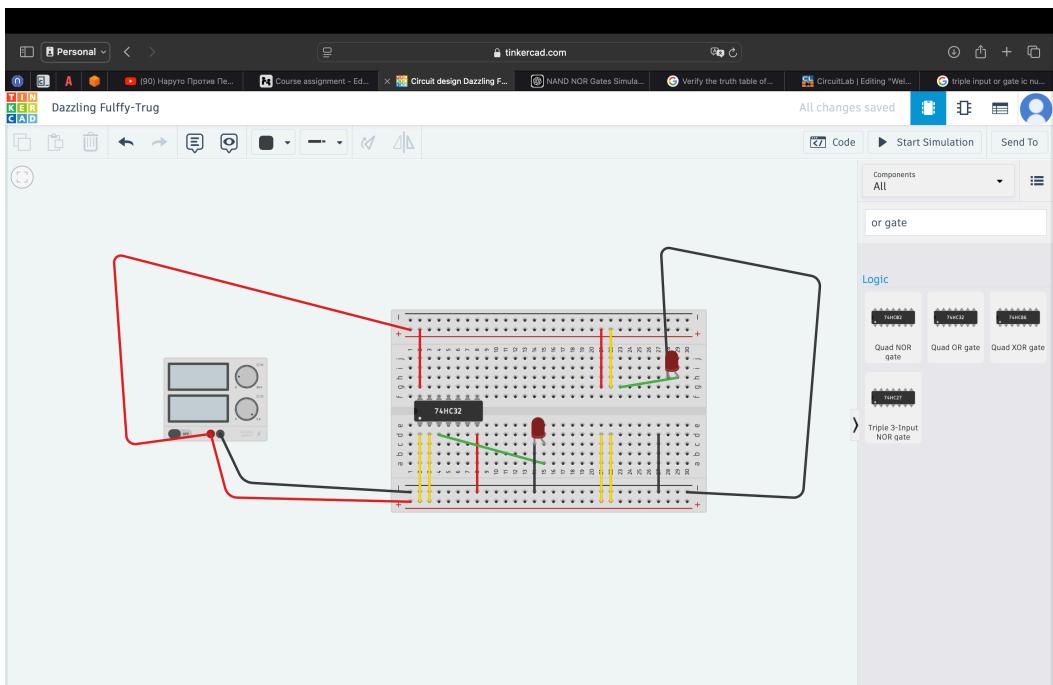




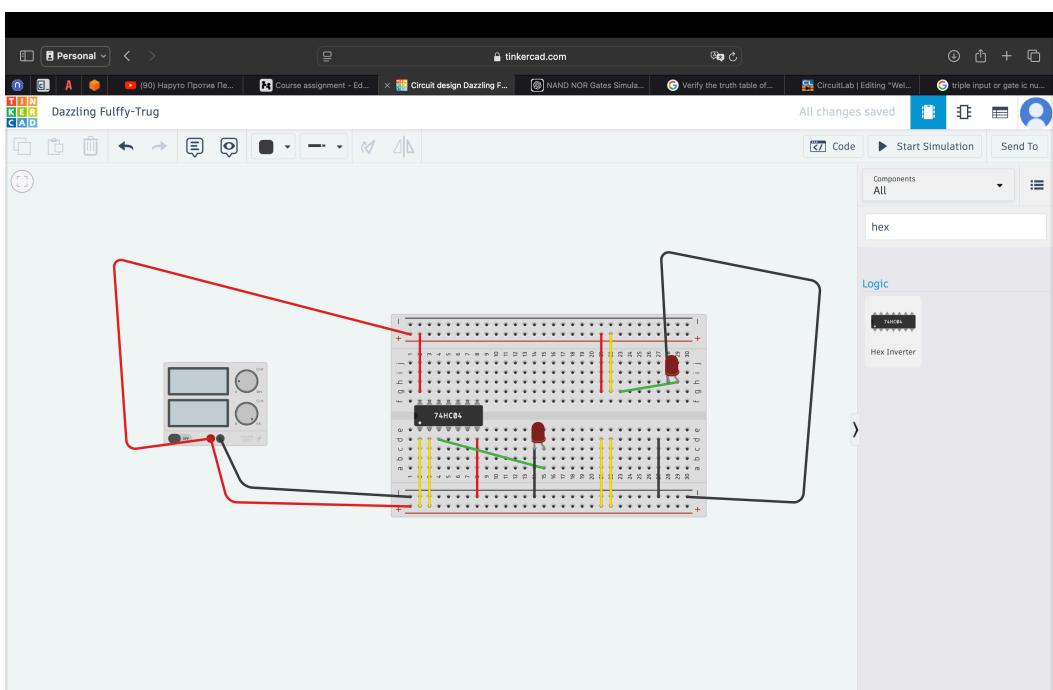
2-3 Gate And with IC

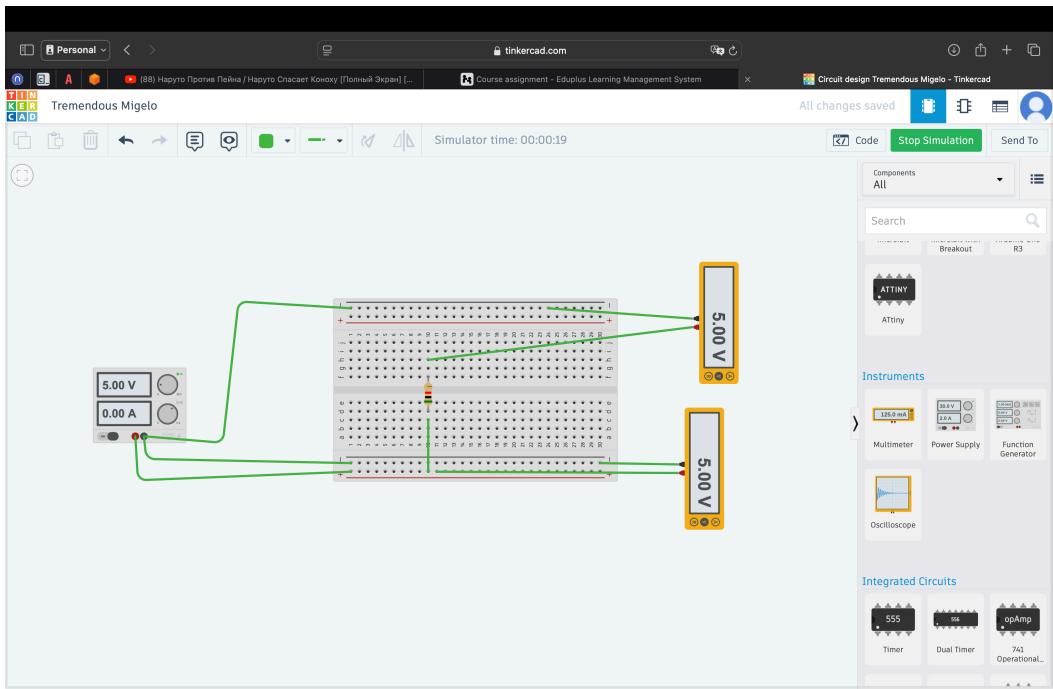


2 Gate OR with IC (in tinker card no 3 gate OR)



2-Gate NOT(HEX convertor) IC (NO 3 gate in tinker card)

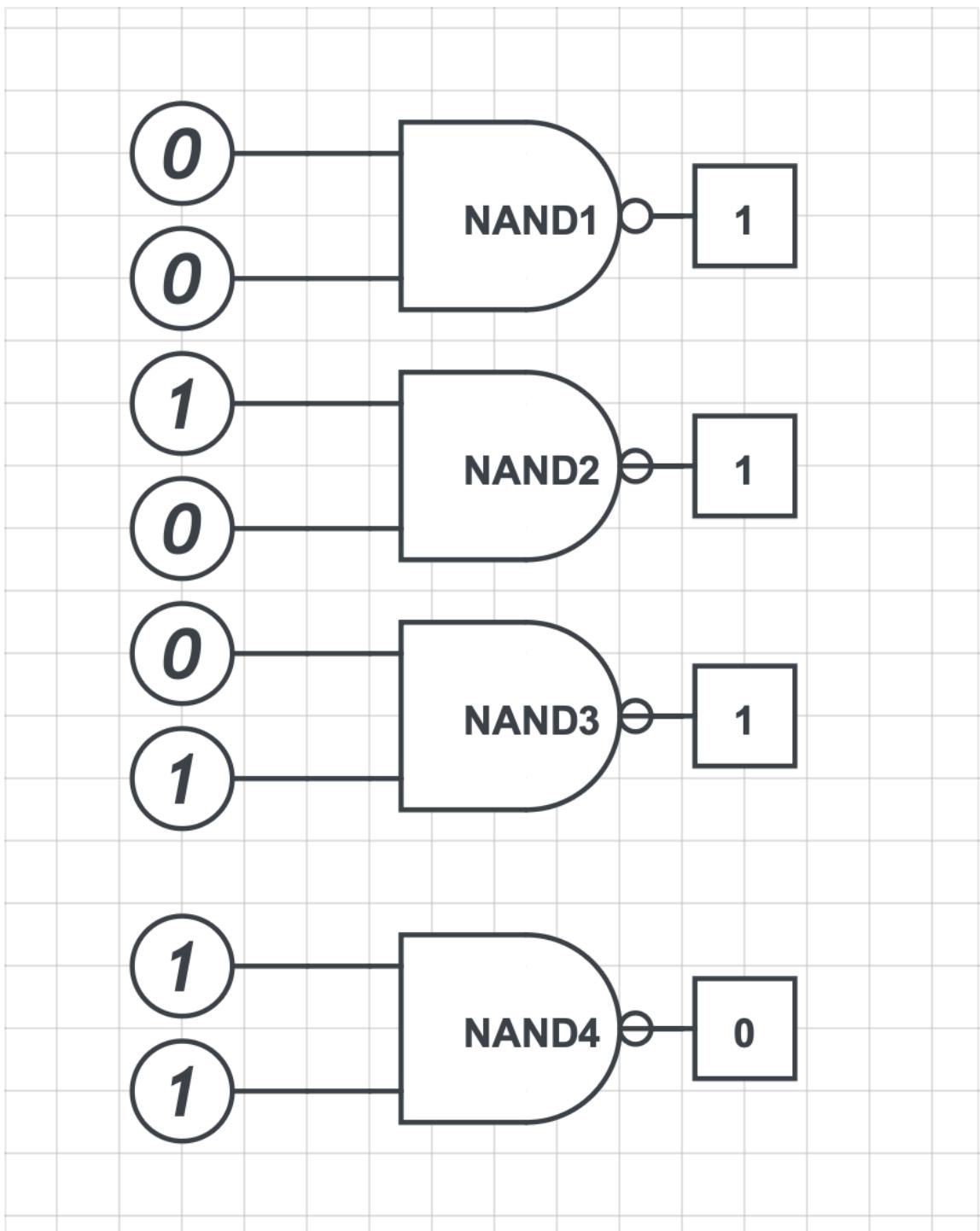


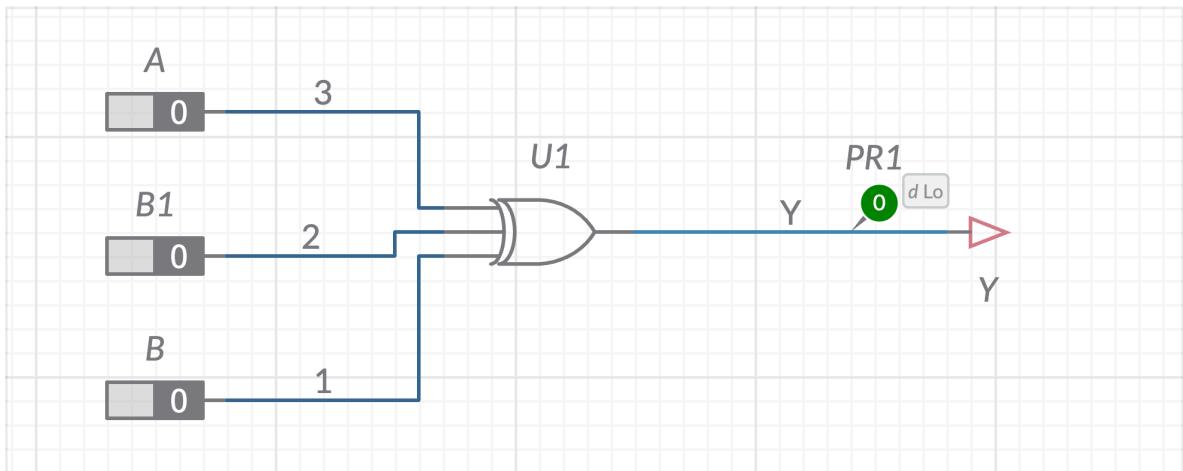
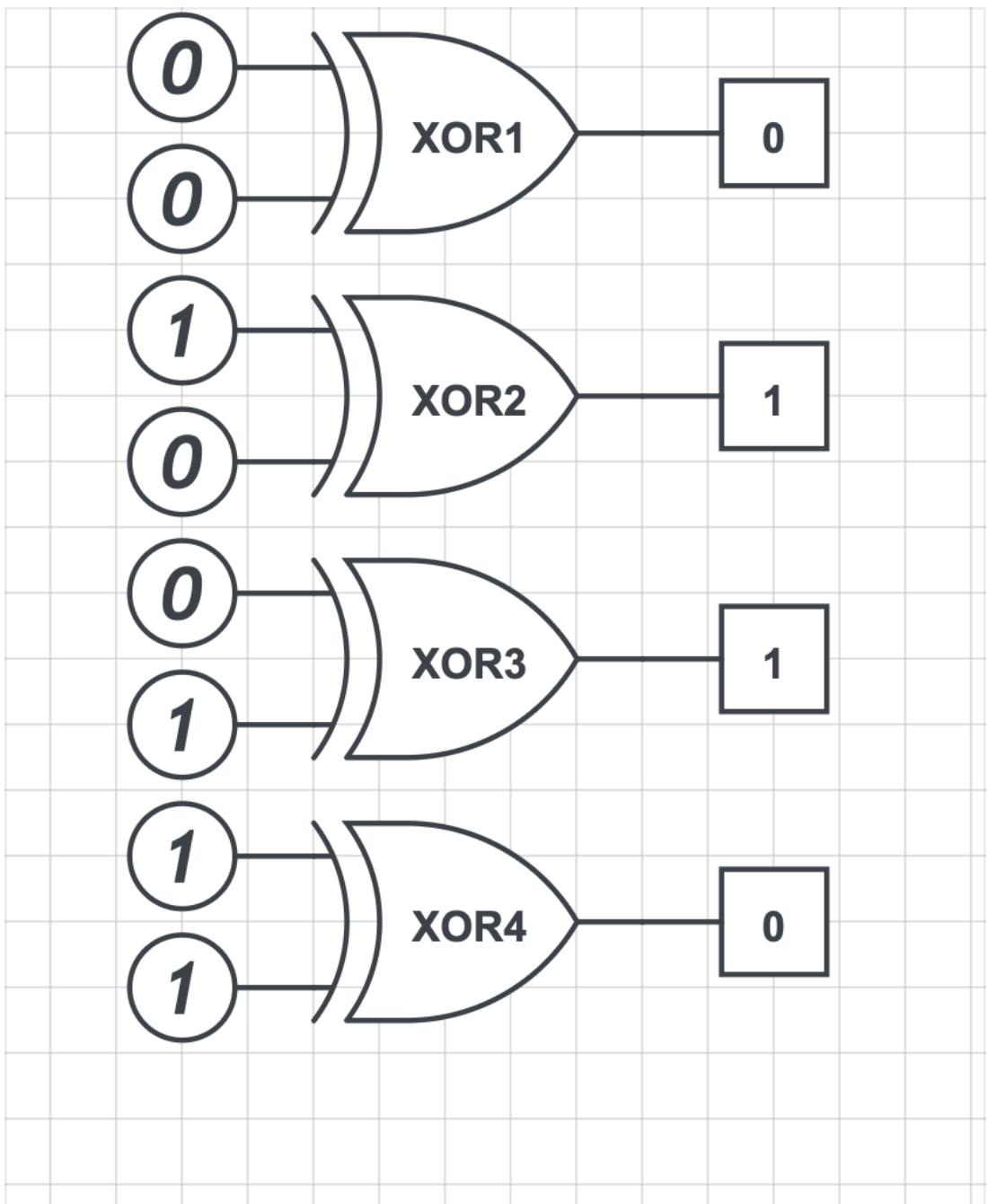


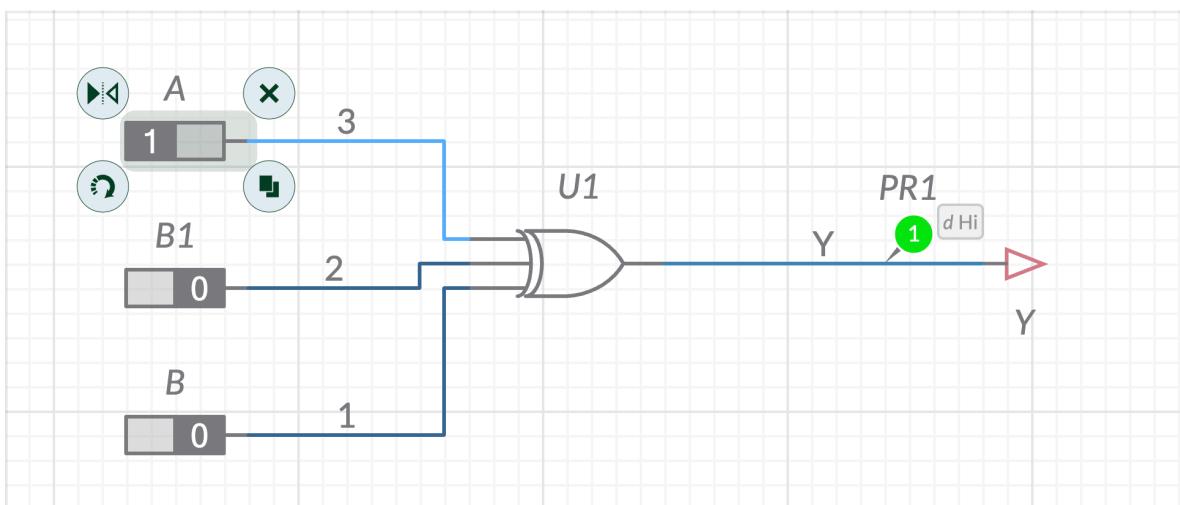
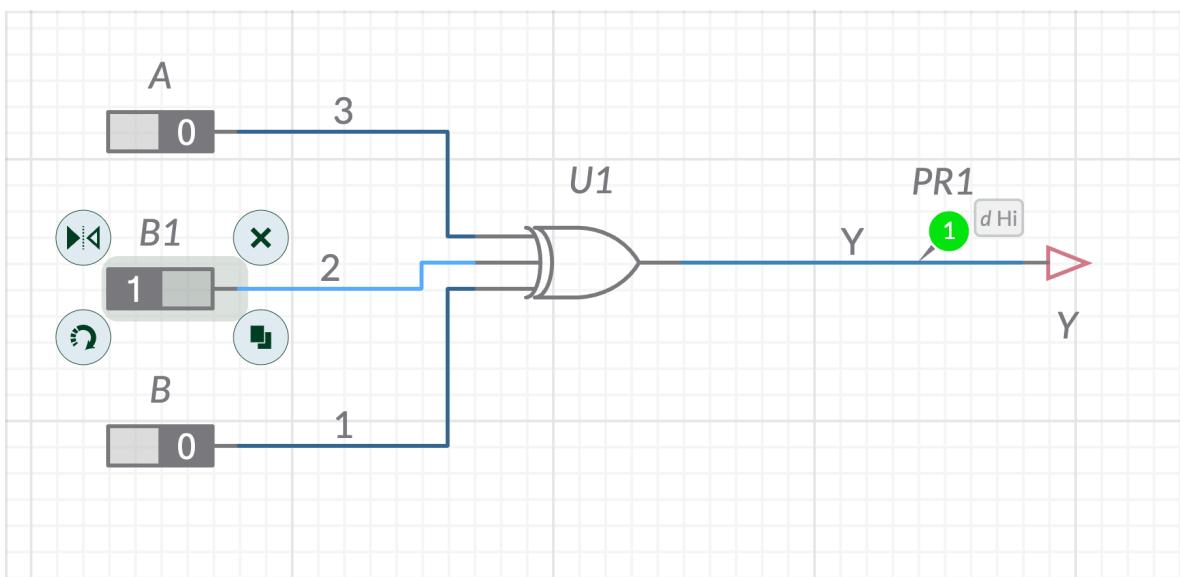
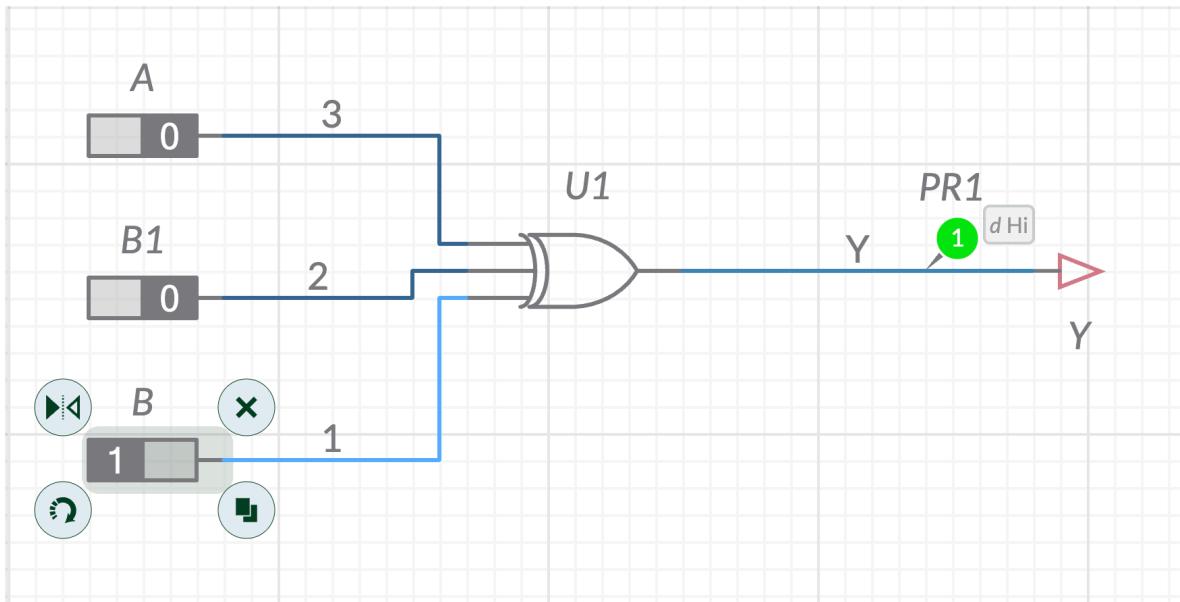
In this lab experiment, we verified the truth tables for AND, OR, and NOT gates using Multisim, utilizing relevant integrated circuits (ICs) such as the 7408, 7432, and 7404, along with a virtual breadboard and a digital power source. The experiment involved connecting the ICs to measure the output parameters and confirm the expected logic behavior.

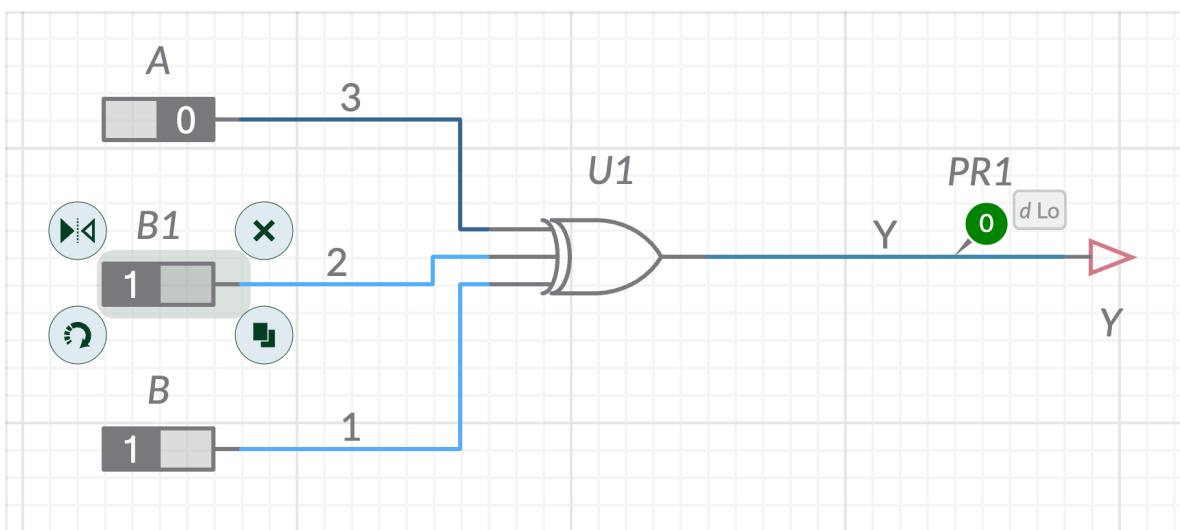
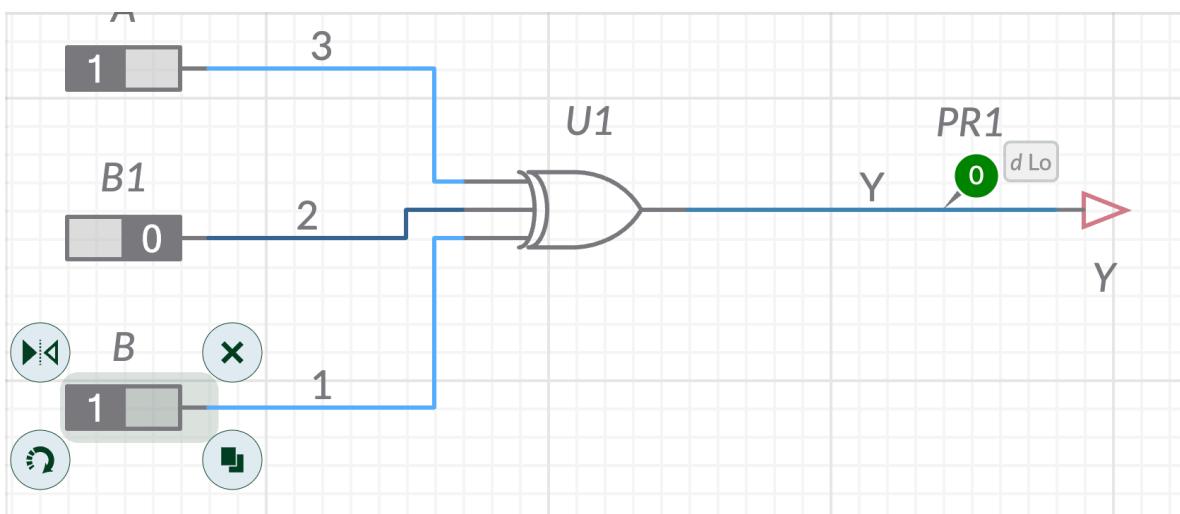
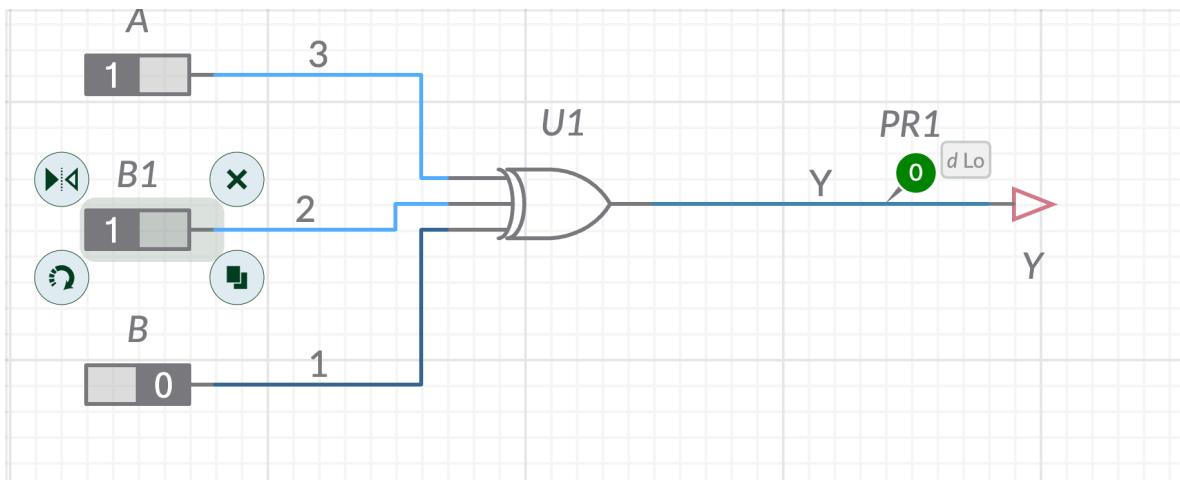
## LAB\_2

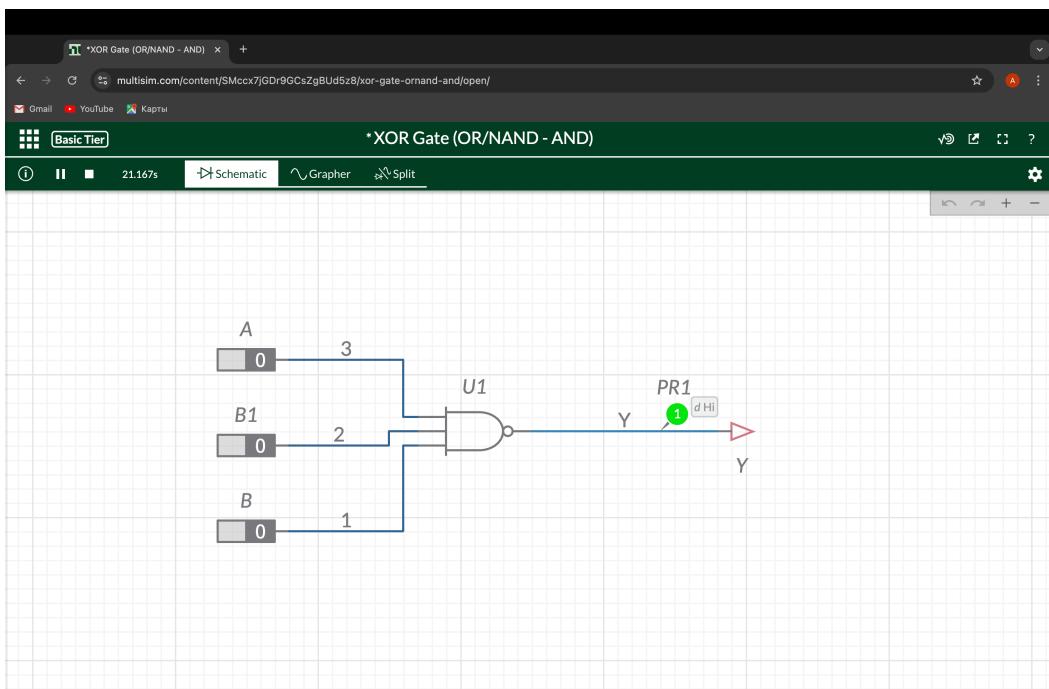
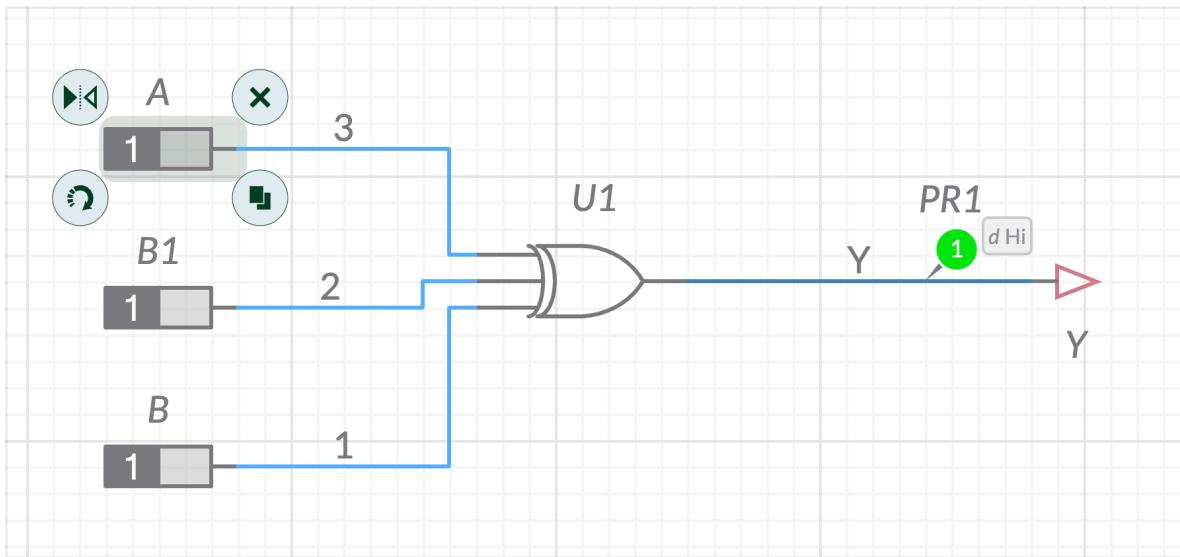
TASK1 NAND and NOR without IC in [circutlab.com](#) and [multism.com](#)

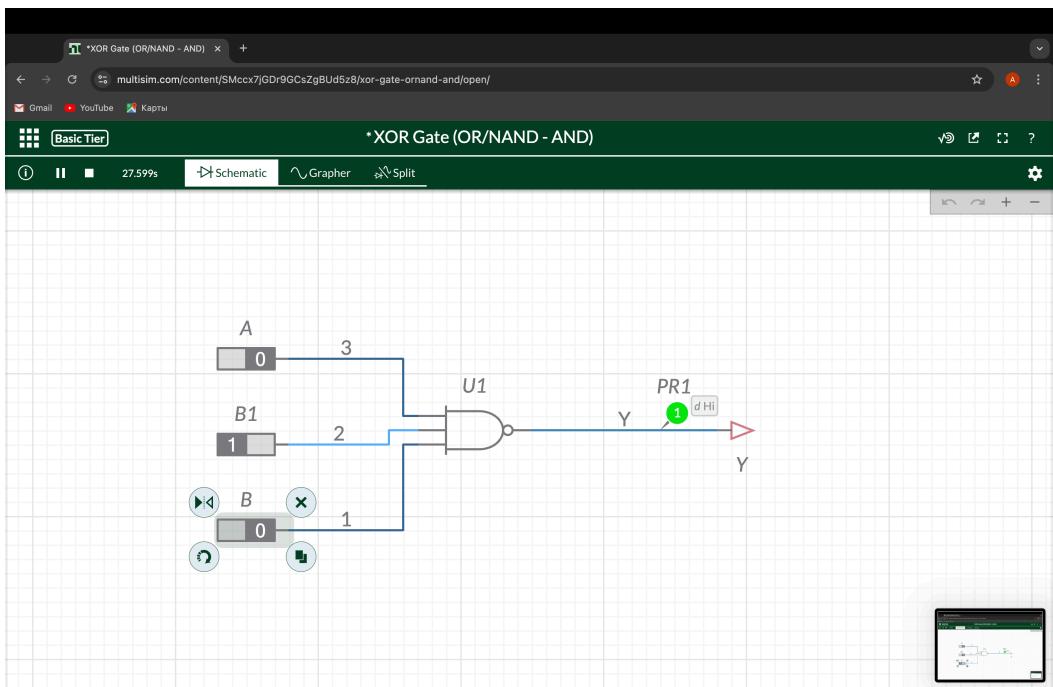
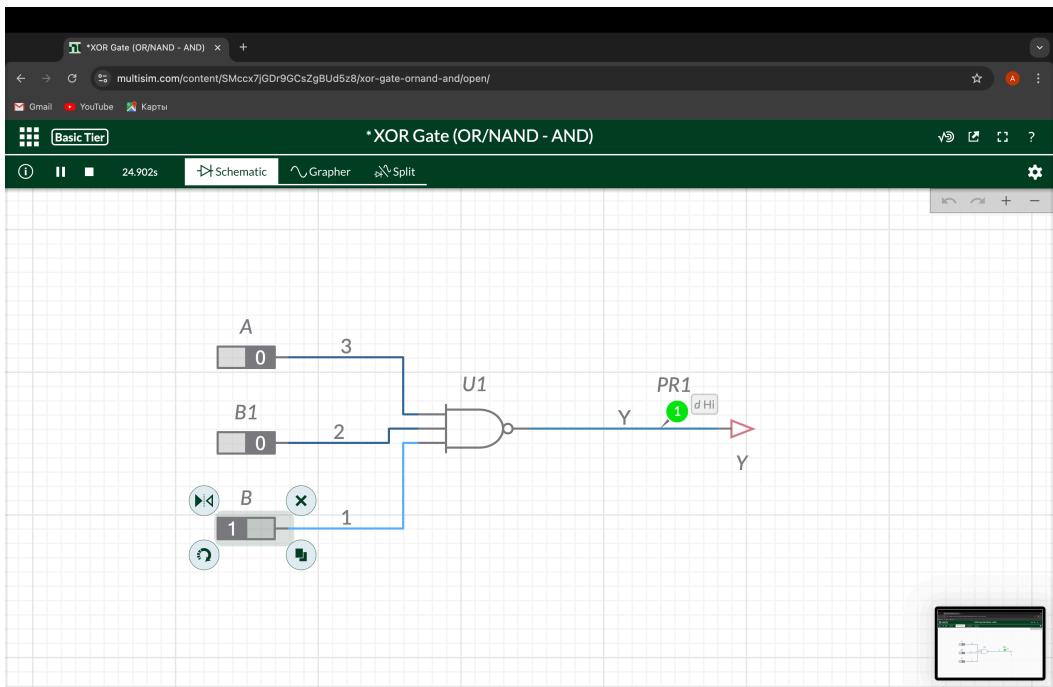


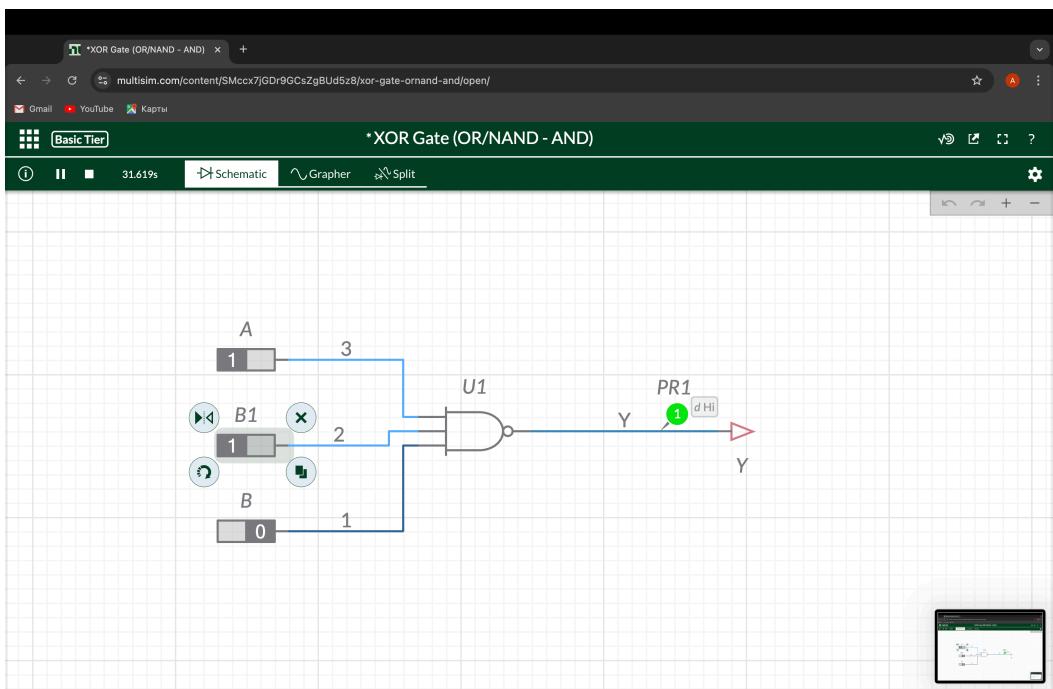
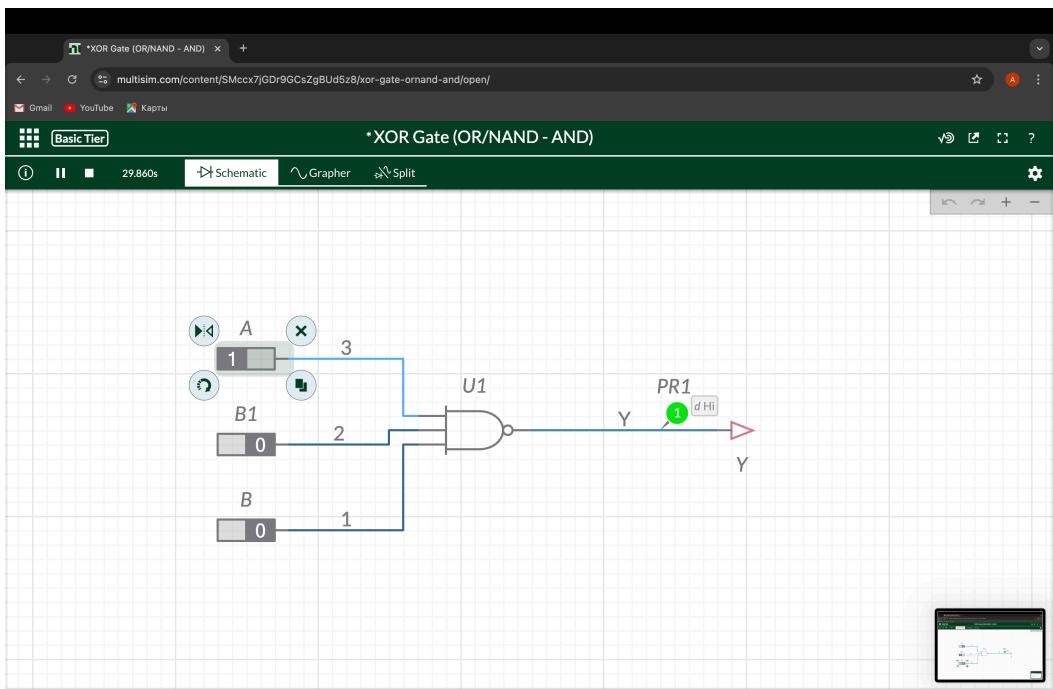


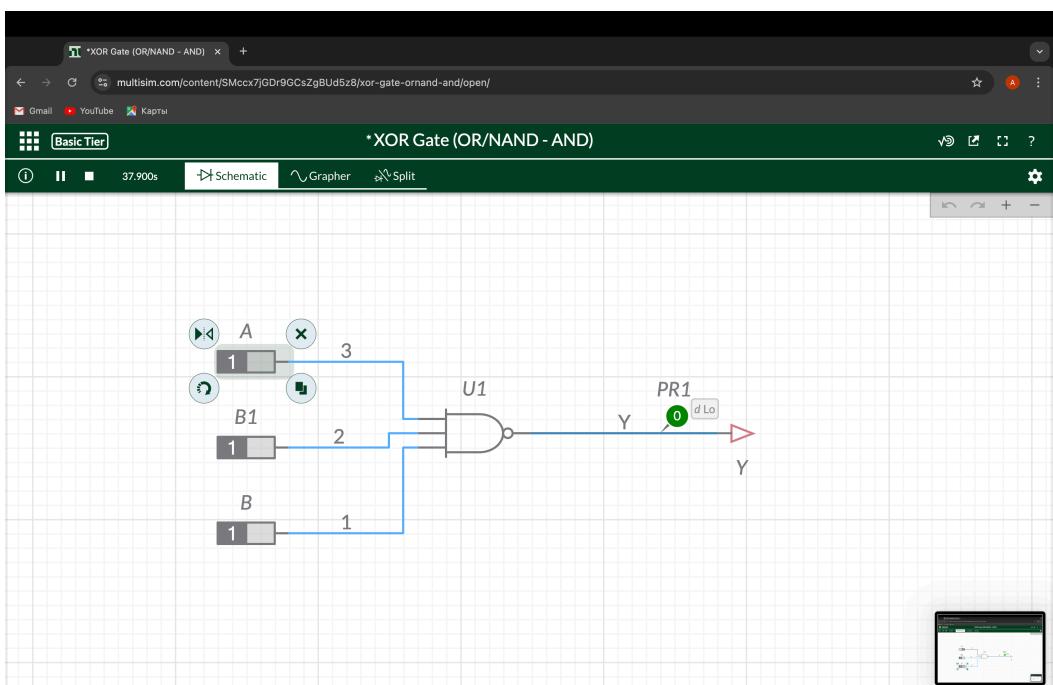
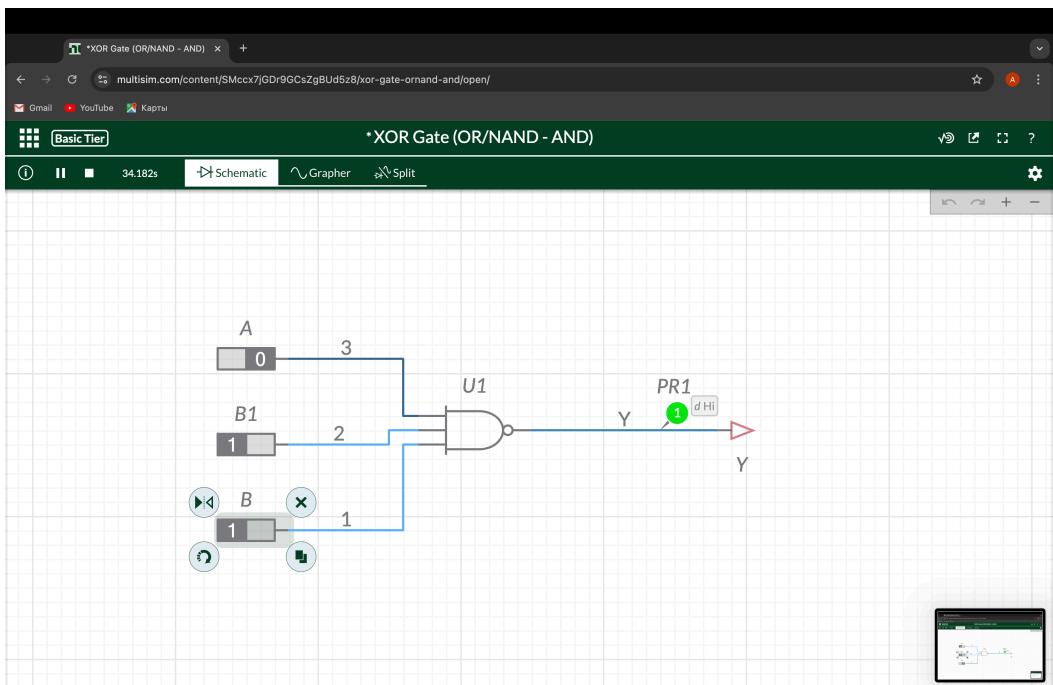


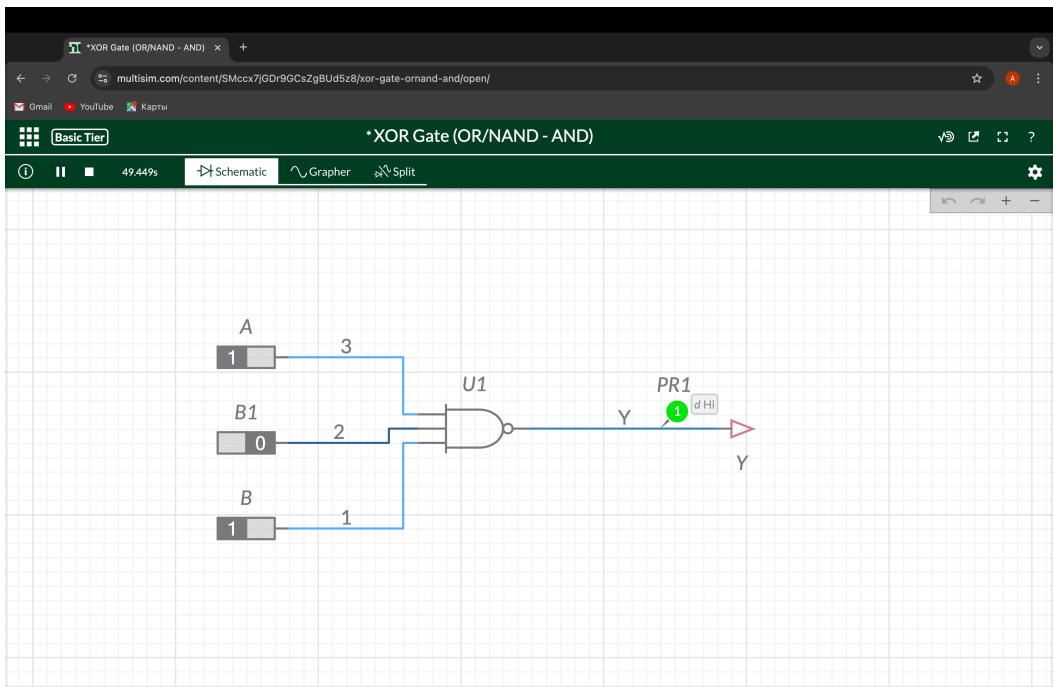




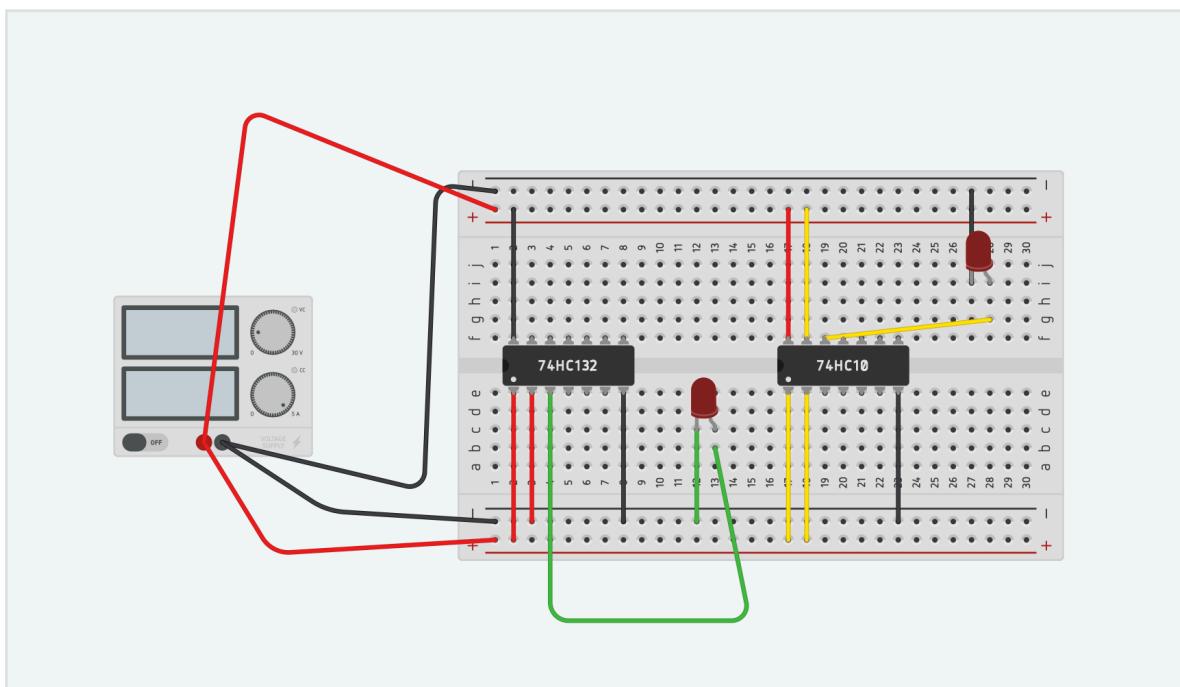


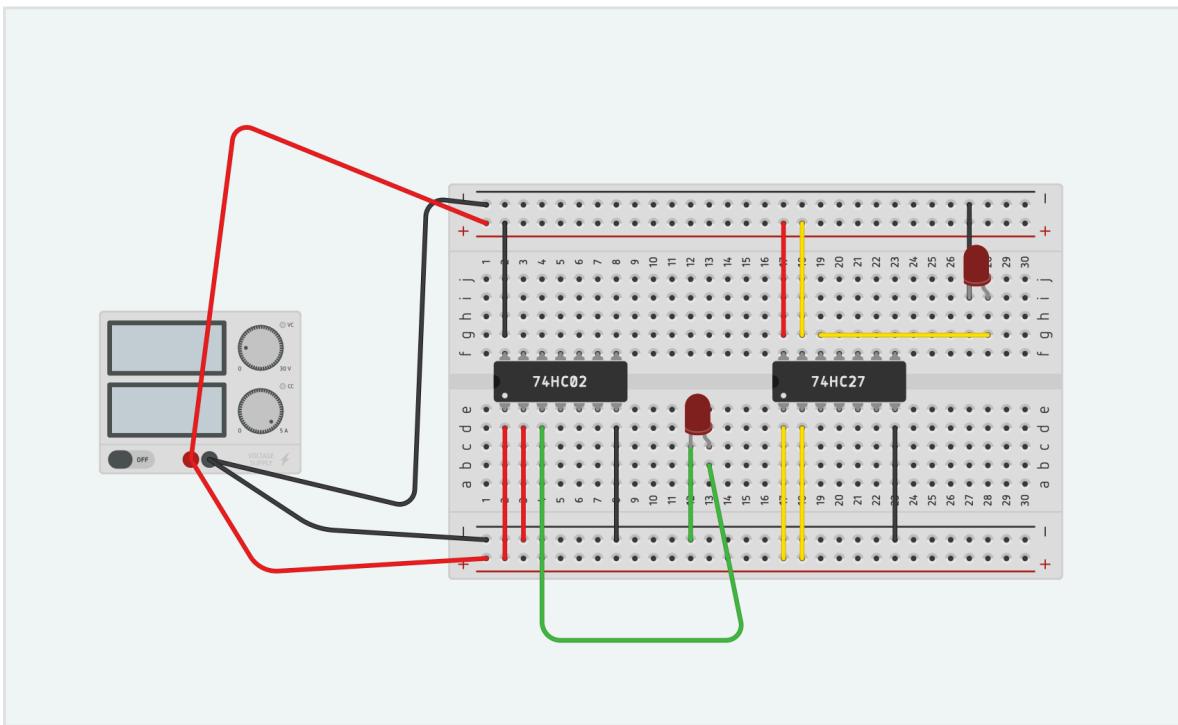




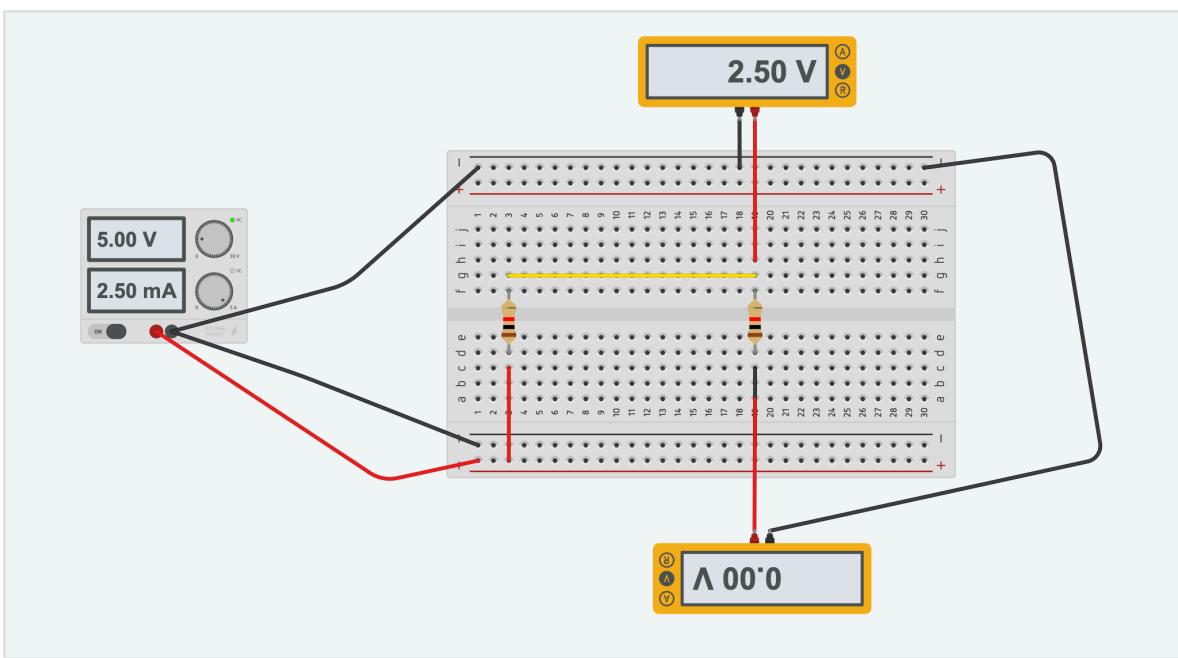


TASK2 NAND and NOR with IC in tinker card





Task 3.



In this experiment, we verified the truth tables for NAND and NOR gates, both of which are universal gates, using Multisim. The setup involved the relevant integrated circuits (ICs) 7400 and 7402, along with wires, a virtual breadboard, and a digital power source to measure the output parameters and confirm the expected logic results.