Class Roll - 19

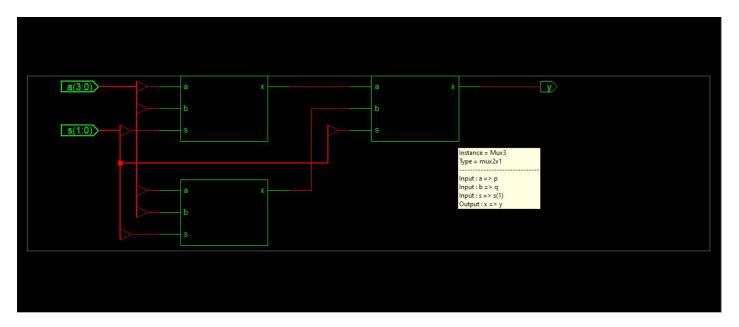
## 4 X 1 Multiplexer – Structural Model

Current Simulation Time: 900 ns		0 200		00 I	465.0 400		5.0	600		800	
■ 🚮 a[3:0]	4'h7	4'h0	4h8	4'h2	4'hF	4'h7		4'hD	4°h7	4"h6	4'h4
🎳 a[3]	0										
🎳 a[2]	1										
🎳 a[1]	1			The state of the s	-						
🎳 a[0]	1										
■ 🚮 s[1:0]	2'h3	2'h0		2'h1	2'h2	2'h3		2'h0	2'h1	2°h2	2'h3
<b>6</b> [] s[1]	1										
o[] s[0]	1										ľ
<mark>₀.∏</mark> y	0										

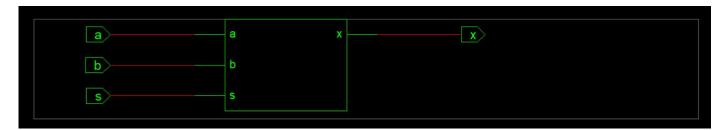
**Waveform** 



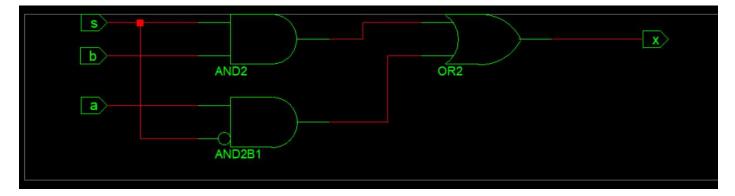
**RTL Schematic** 



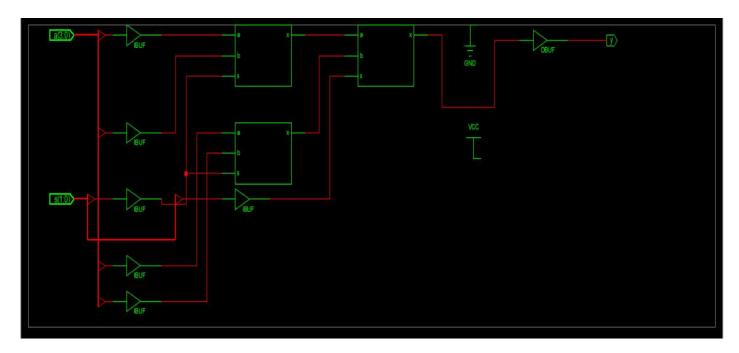
**Detailed RTL Schematic Circuit** 



RTL Schematic of Component 2 X 1 MUX



**Detailed RTL Schematic of Component 2 X 1 MUX** 



**Technology Schematic**