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[5559]-193

## S.E. (Computer) (Second Semester) EXAMINATION, 2019

## MICROPROCESSOR

## (2015 PATTERN)

a) DF b) VM c NT d) RF c) Explain shift and rotate instructions of 80386.  OR  2) a) Draw and explain the format of a selector. b) List and explain control registers of 80386. c) With help of diagram explain the 80386 mechanism to translate logical address to linear address.  3) a) List aspects of protection related to pages. b) With appropriate diagram explain the concept of privilege levels in 80386. c) How Call gate descriptor is used to locate the procedure in another code segment? How protection is provided?  OR  OR  10  OR		W.	ours					Maximum	n Marks	: 50
iii. Figures to the right indicate full marks.  Assume suitable data, if necessary.  1) a) List fundamental data types of 80386.  b) Describe following different flags defined in 80386 Processor - a) DF b) VM c) NT d) RF  c) Explain shift and rotate instructions of 80386.  OR  2) a) Draw and explain the format of a selector. b) List and explain control registers of 80386. c) With help of diagram explain the 80386 mechanism to translate logical address to linear address.  3) a) List aspects of protection related to pages. b) With appropriate diagram explain the concept of privilege levels in 80386. c) How Call gate descriptor is used to locate the procedure in another code segment? How protection is provided?  OR  OR  Define "Faults".  DEXPLAIN "How 80386 indentifies interrupts?" c) By which two ways, 80386 allows input/output to be performed? Explain each in [06]		Arm O	anaidates	•				A Company of the Comp		
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		c) By which	uu <sub>00</sub> 0	o maentif	ies interi	upts?"	" grapes" "	7		_
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P.T.O.

5)	a)	Explain features of "Virtual 8086 mode",	[03]					
	b)	. * .						
			[04]					
	c),	What all initializations required to start processor in protected mode after reset?	[06]					
		OR						
6)	a)	Write a short-note on "Switching to protected mode".	[02]					
	b)	San de la companya d						
	c)							
			[06]					
7)	a)	Draw and explain read cycle with non-pipelined address timing.	[08]					
	b)	Which data types are supported by 80387?						
		OR Co.	[05]					
8)	a)	Draw and explain write cycle with pipelined address timing.	TAO2					
	b)	The 80387 instructions are divided to the state of the st	[08]					
	•	The 80387 instructions are divided in to which functional groups? Explain with one example of each.	[05]					

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