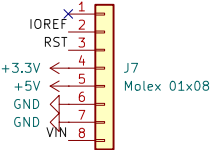
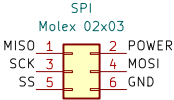
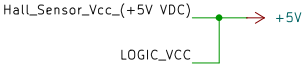
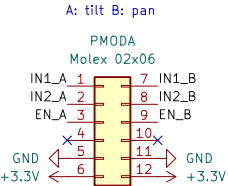
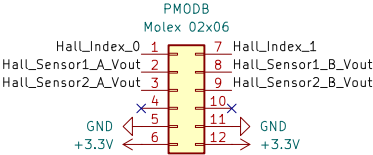


Connections from EMG30_protection schematic to FPGA board



Index (-0 when triggered, LOGIC_VCC when not)
Index 0 = Pan, Index 1 = Tilt



SDU

Sheet: /

File: pan-tilt.kicad_sch

Title: **PYNQ-Z2 FPGA BOARD CONNECTIONS**

Size: A4

Date: 2024-04-05

Rev:

KiCad E.D.A. 8.0.0

Id: 1/1