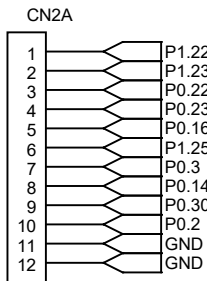
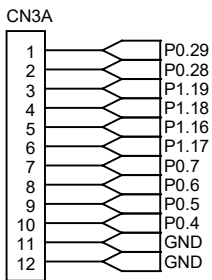


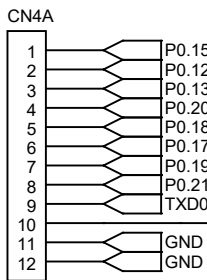
BERG 12 PIN MALE



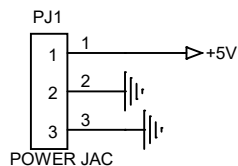
BERG 12 PIN MALE



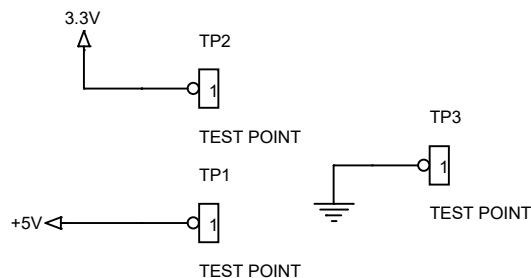
BERG 12 PIN MALE



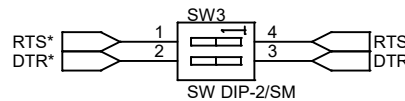
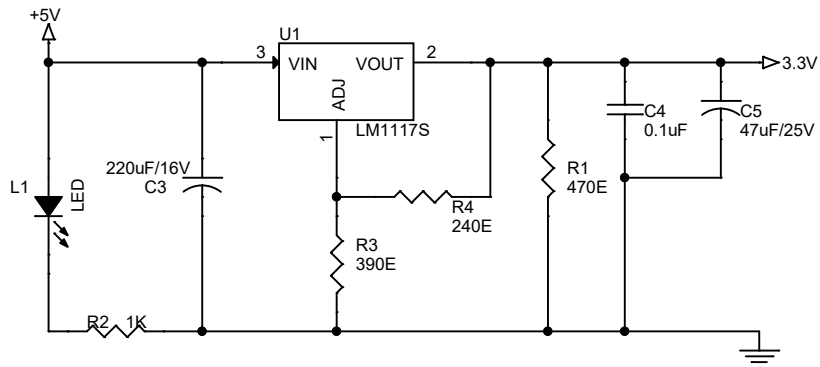
BERG 12 PIN MALE



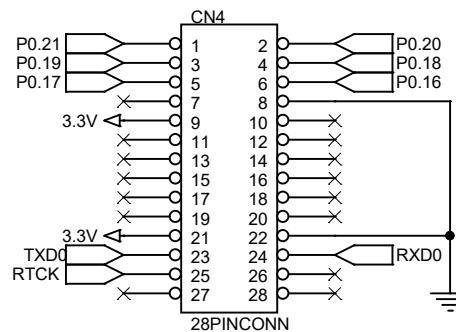
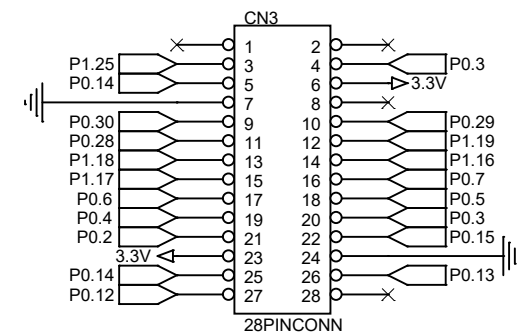
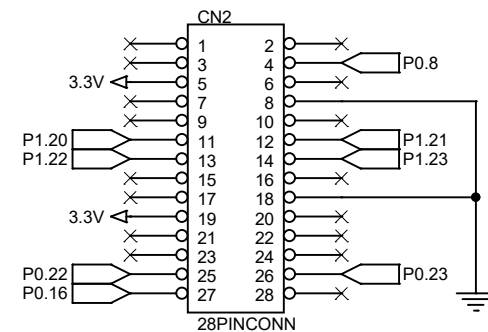
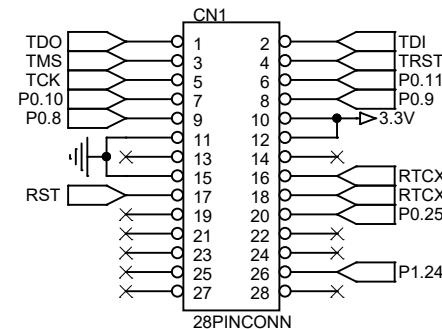
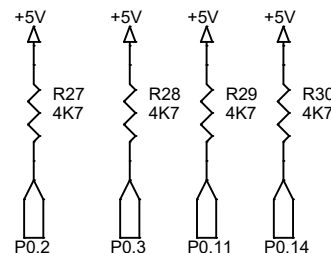
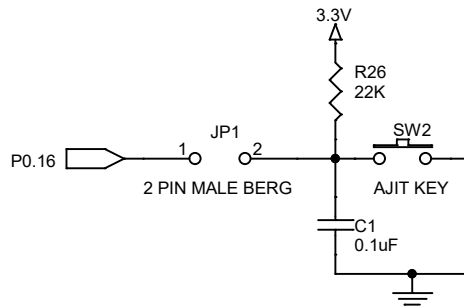
POWER JAC



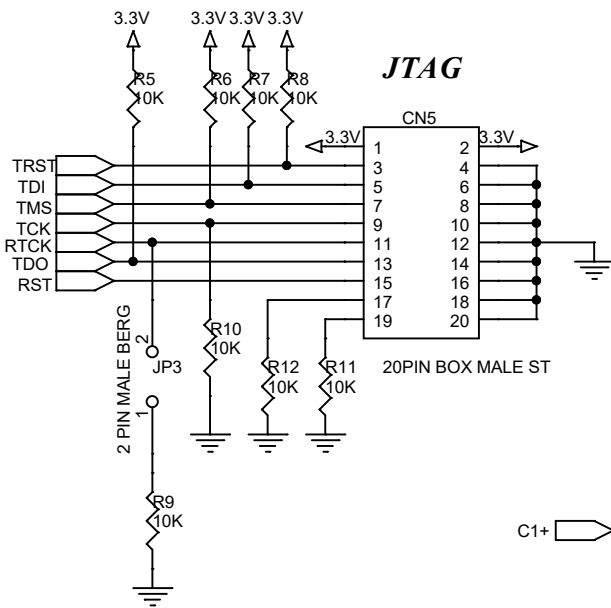
POWER SUPPLY



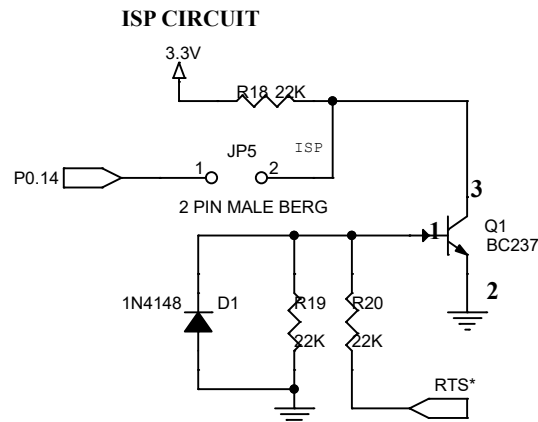
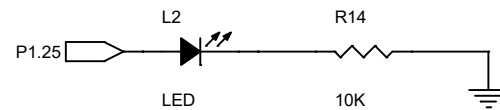
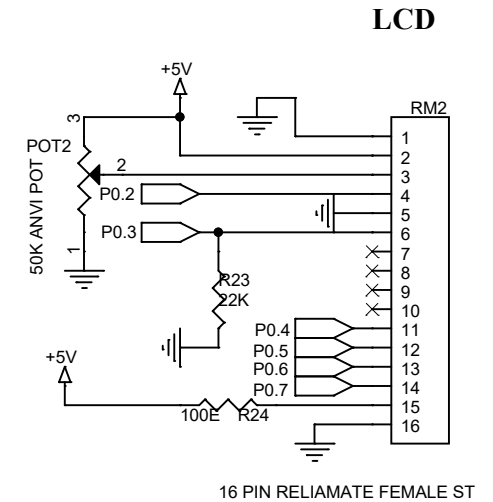
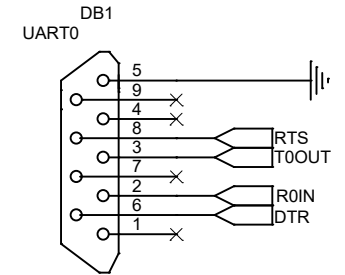
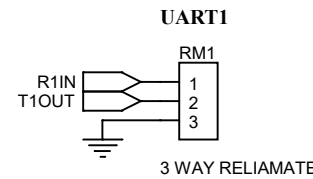
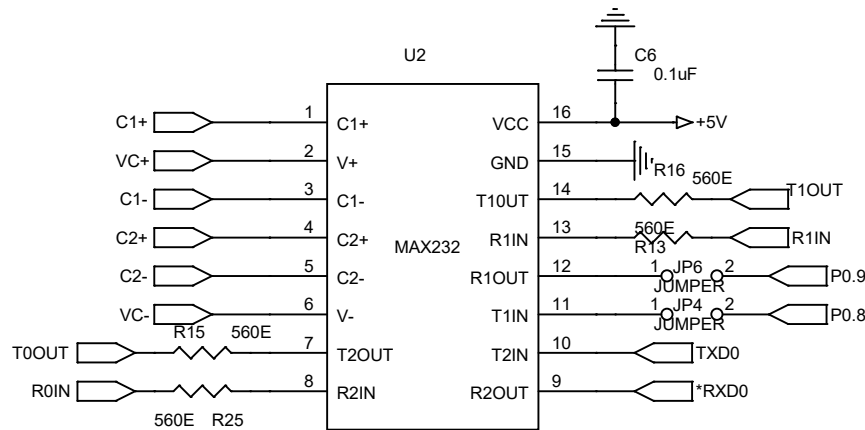
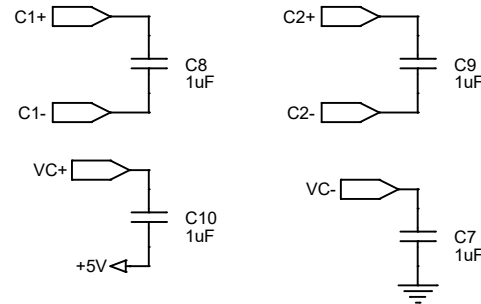
EXT INTERRUPT CIRCUIT



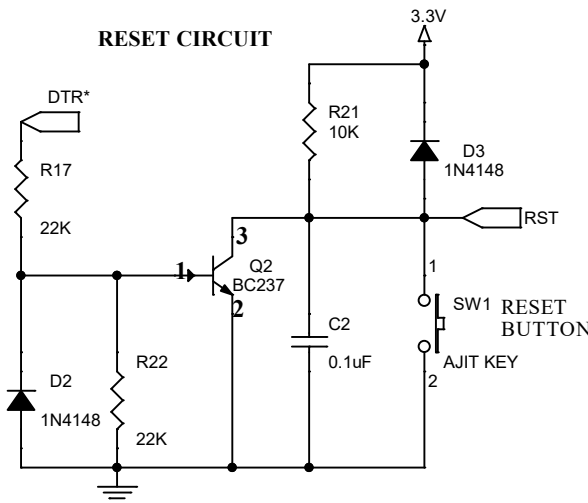
		Advanced Electronic Systems,	
		#143, 9th Main, Laggere Cross, 3rd Phase,	
		Peenya Industrial Area, BANGALORE- 560058	
PRODUCT NAME PJTBRD/ARM2148T2			
Size A4	PCB ID. 99-21-13	Rev 01	
Date: Wednesday, July 19, 2017		Sheet 1 of 1	
Prepared By: BEERAPPA Y G		Approved By: ALS	



P1.26 (RTCK) SHOULD BE GROUNDED BY JUMPER JP3 FOR TRACEPORT TO WORK.



RESET CIRCUIT



SW1 OF PINS 1 & 2 SHOULD BE ON FOR ISP & RESET

		Advanced Electronic Systems,	
		#143, 9th Main, Laggere Cross, 3rd Phase, Peenya Industrial Area, BANGALORE- 560058	
PRODUCT NAME		PJTBRD/ARM2148T2	
Size A4	PCB ID.	99-21-13	Rev 01
Date: Wednesday, July 19, 2017		Sheet 1 of 1	
Prepared By: BEERAPPA Y G		Approved By: ALS	