

# Tutorial 9

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## 1 Q1 Register and address descriptors

Line	Ori	Gen	r1	r2	r3	r4	i	j	a	re
1	t1 = 10 * i						i	j	a	
		LD r1, i	i				i	j	a	ge
		MUL r1, r1, \#10	t1				i, r1	j	a	NU
2	t2 = t1 + j	LD r2, j	t1	*j			i	j, r2	a	ge
		ADD r2, r1, r2	t2	*j			i	j, r2	a	NU
3	t3 = 8 * t2	MUL r1, r1, \#8	t3	*j			i	j, r2	a	ge
4	t4 = t3 - 88	SUB r1, r1, \#88	t4	*j			i	j, r2	a	ge
5	a[t4] = 0.0	LD r3, a	t4	*j	a		i	j, r2	a, r3	
		MUL r1, r1, 8	t4*8	*j	a		i	j, r2	a, r3	
6		ADD r1, r3, r1	a + t4*8	*j	a		i	j, r2	a, r3	
		ST r1, \#0	a + t4*8	*j	a		i	j, r2	a, r3	
6	j = j + 1	ADD r2, r2, 1	...	t = *j + 1	a		i	r2	a, r3	
		ST j, r2	...	*j	a		i	j, r2	a, r3	

## 2 Q2 Spilling cost

Cost of spilling probably depends on

- how soon it is reused

### 3 Q3 Convert code into 3-address code and if-gotos

3-address code:

```
1  getCofactor:
2  int mat[N][N];
3  int temp[N][N];
4  int p;
5  int q;
6  int n;
7  int i = 0;
8  int j = 0;
9  int row = 0;
10 for_0:
11 if row >= n goto for_0_end;
12 int col = 0;
13 for_0_0:
14 if col >= n goto for_0_0_end;
15 bool t1 = row != p;
16 if !t1 goto if_0_0_0_end;
17 bool t2 = col != q;
18 if !t2 goto if_0_0_0_end;
19 int rowbyte = N * 4;
20 int rowoff = row * rowbyte;
21 int colbyte = col * 4;
22 int matoff = rowoff + colbyte;
23 int matadd = mat + matoff;
24 int rhs = *(matadd)
25 rowoff = i * rowbyte;
26 colbyte = j * 4;
27 j = j + 1;
28 matoff = rowoff + colbyte;
29 matadd = mat + matoff;
30 *(matadd) = rhs;
31 int t3 = n - 1;
32 bool t4 = j == t3;
33 if !t4 goto if_0_0_0_0_end;
34 j = 0;
35 i = i + 1;
36 if_0_0_0_0_end:
```

```
37  if_0_0_0_end:
38  col = col + 1;
39  goto for_0_0;
40  for_0_0_end:
41  row = row + 1;
42  goto for_0;
43  for_0_end:
```

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