Assignment #15
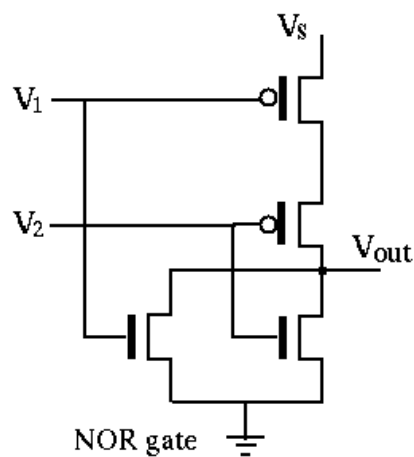
Switch Level Primitives
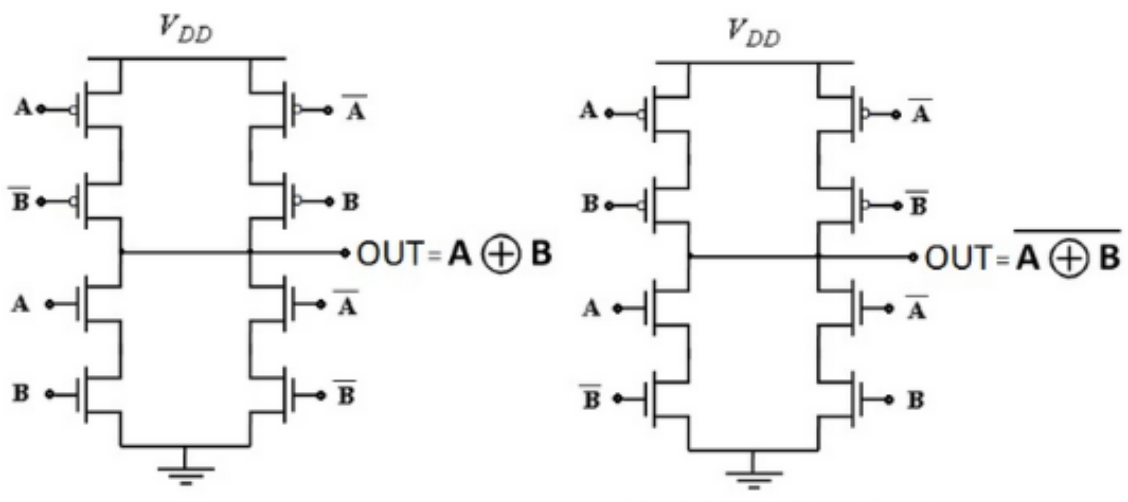
Using pmos and nmos

- Write Verilog code following circuits using switch level primitives.
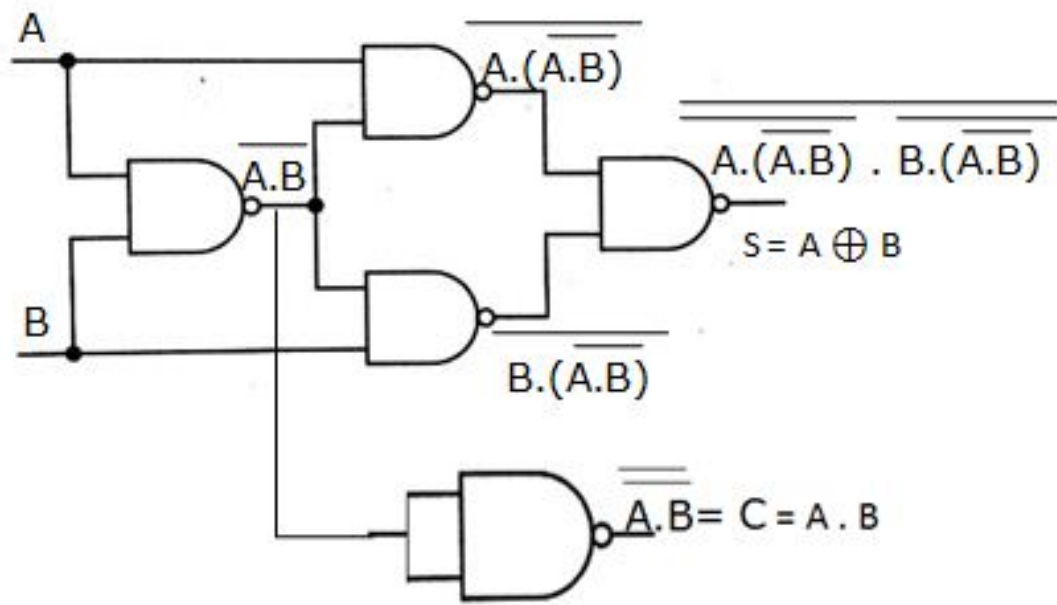
NOR Gate

XOR and XNOR Gate

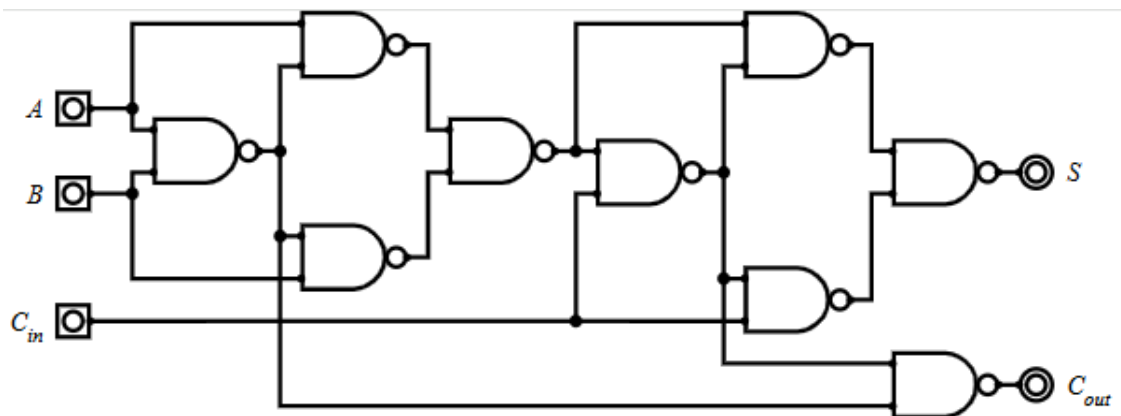### Half Adder using NAND gate

Full Adder using NAND gate

Half Adder using NOR Gate