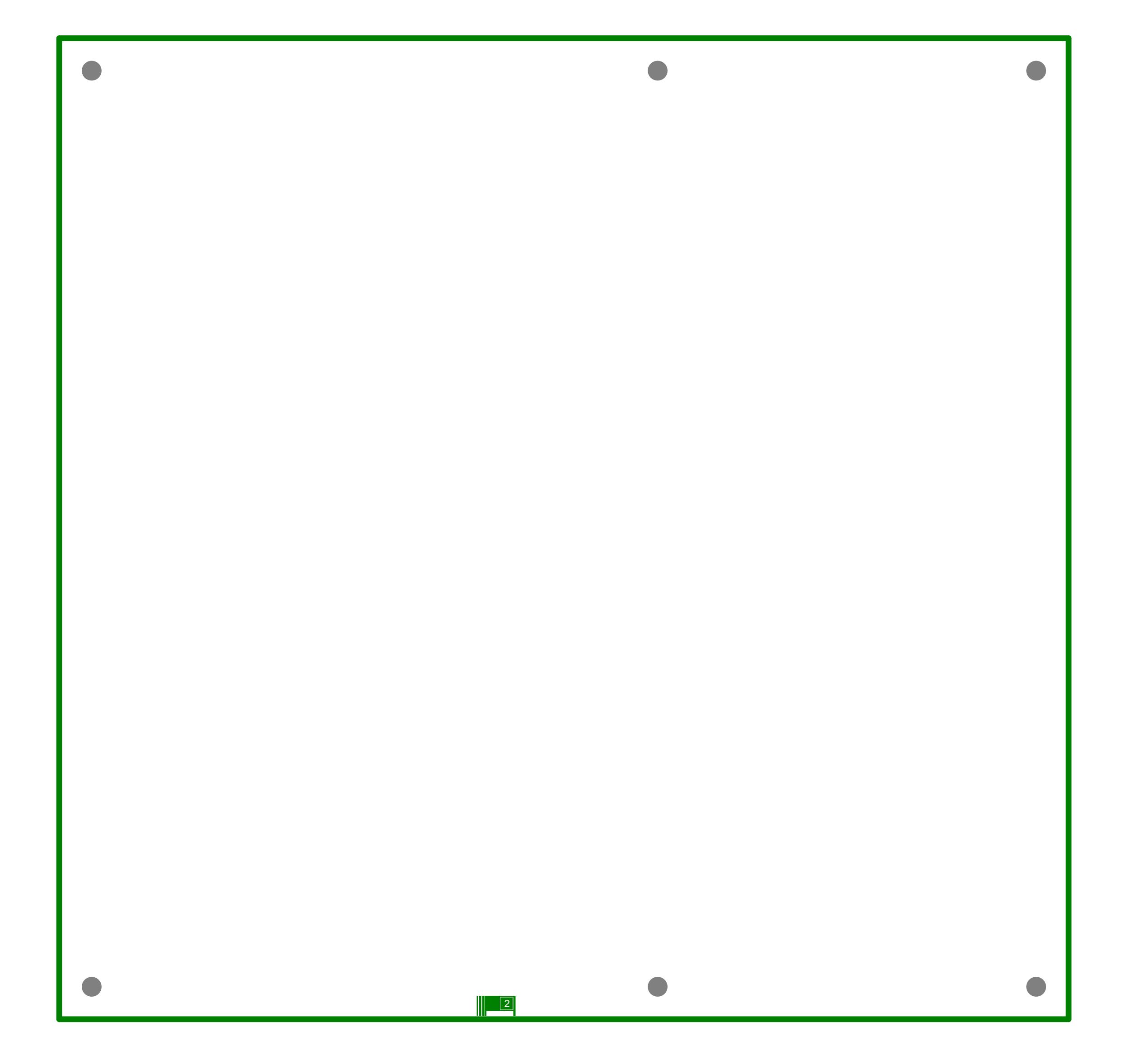
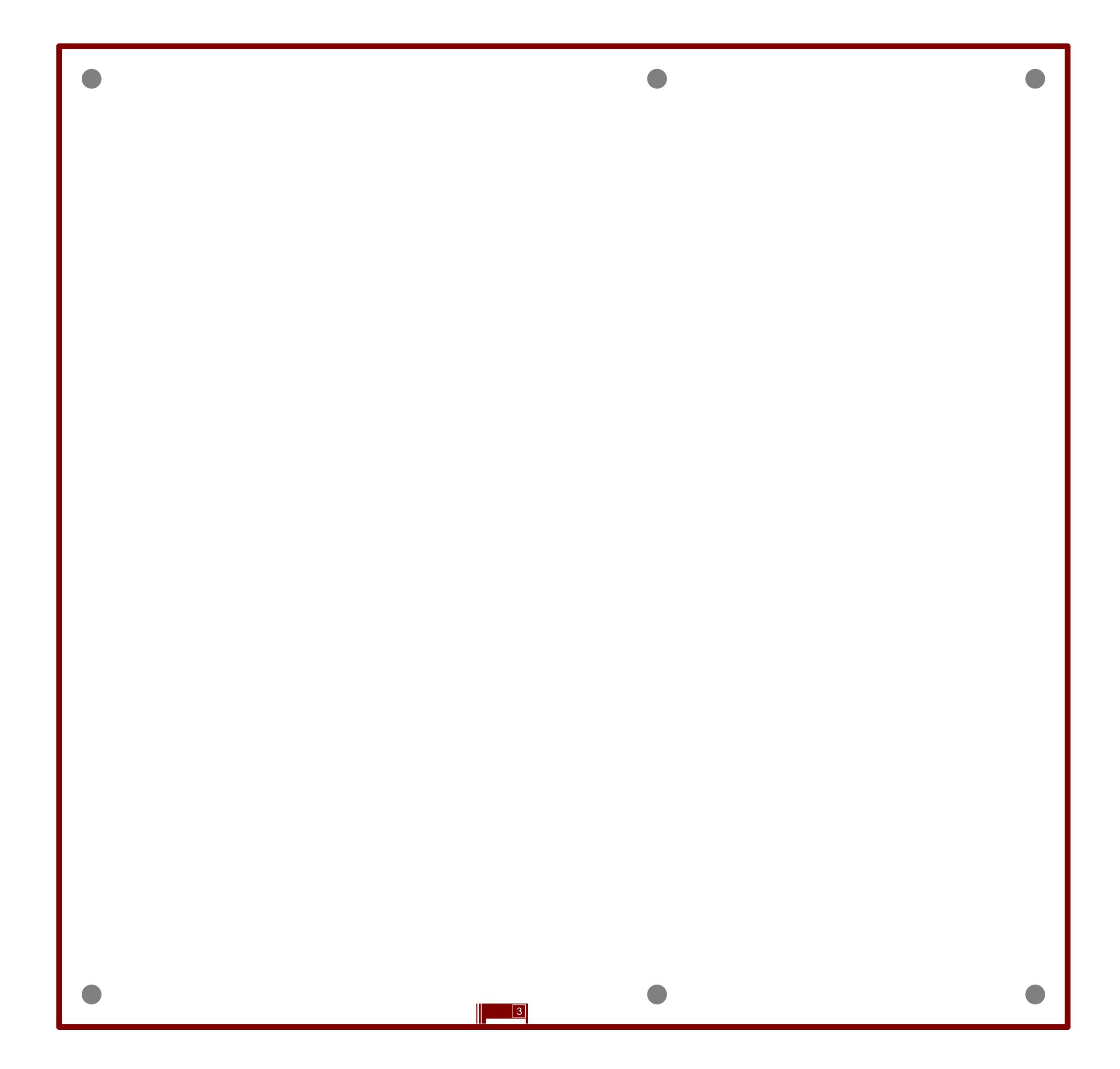
LAYER 1: TOP

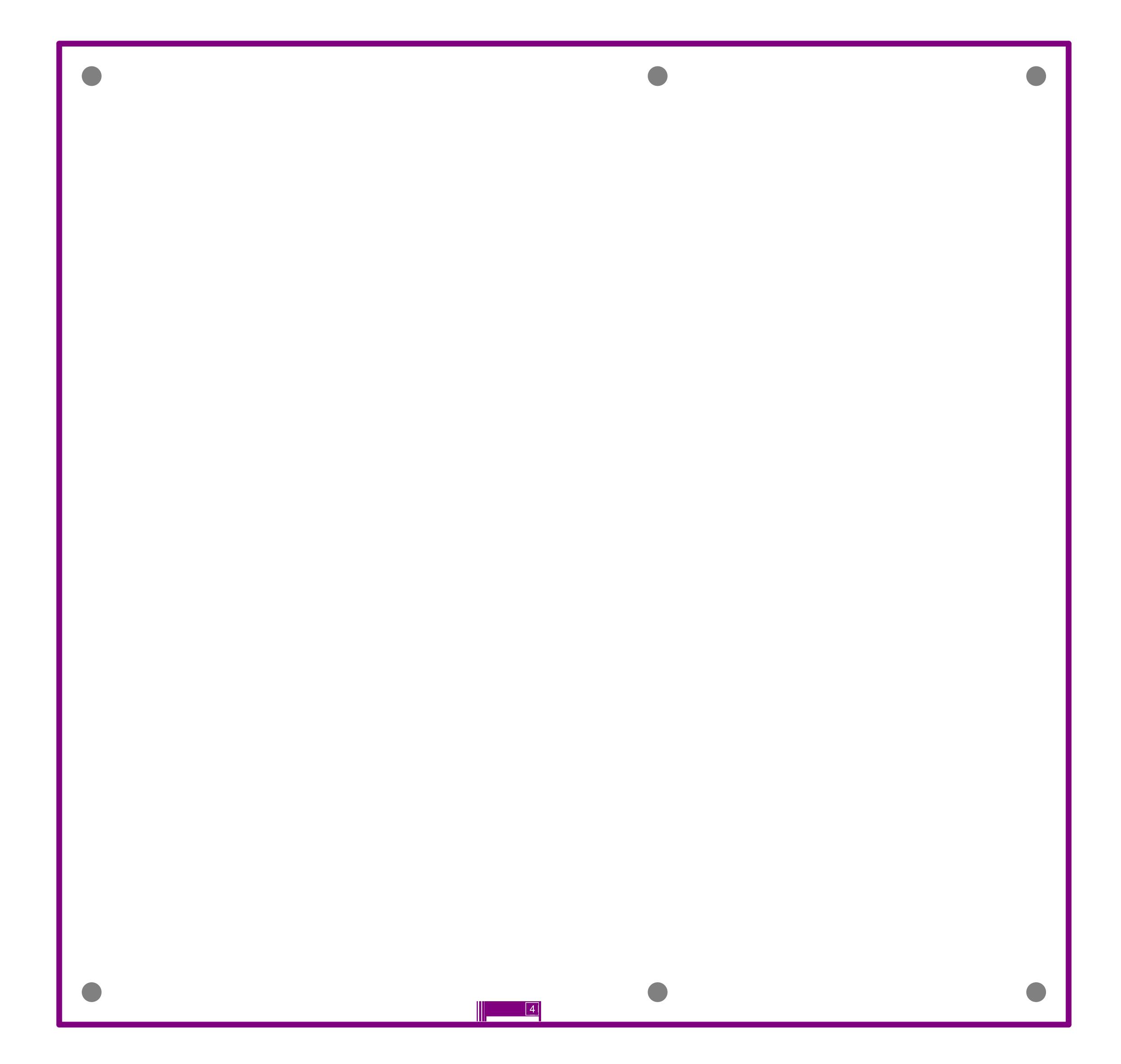


LAYER 2: GND1

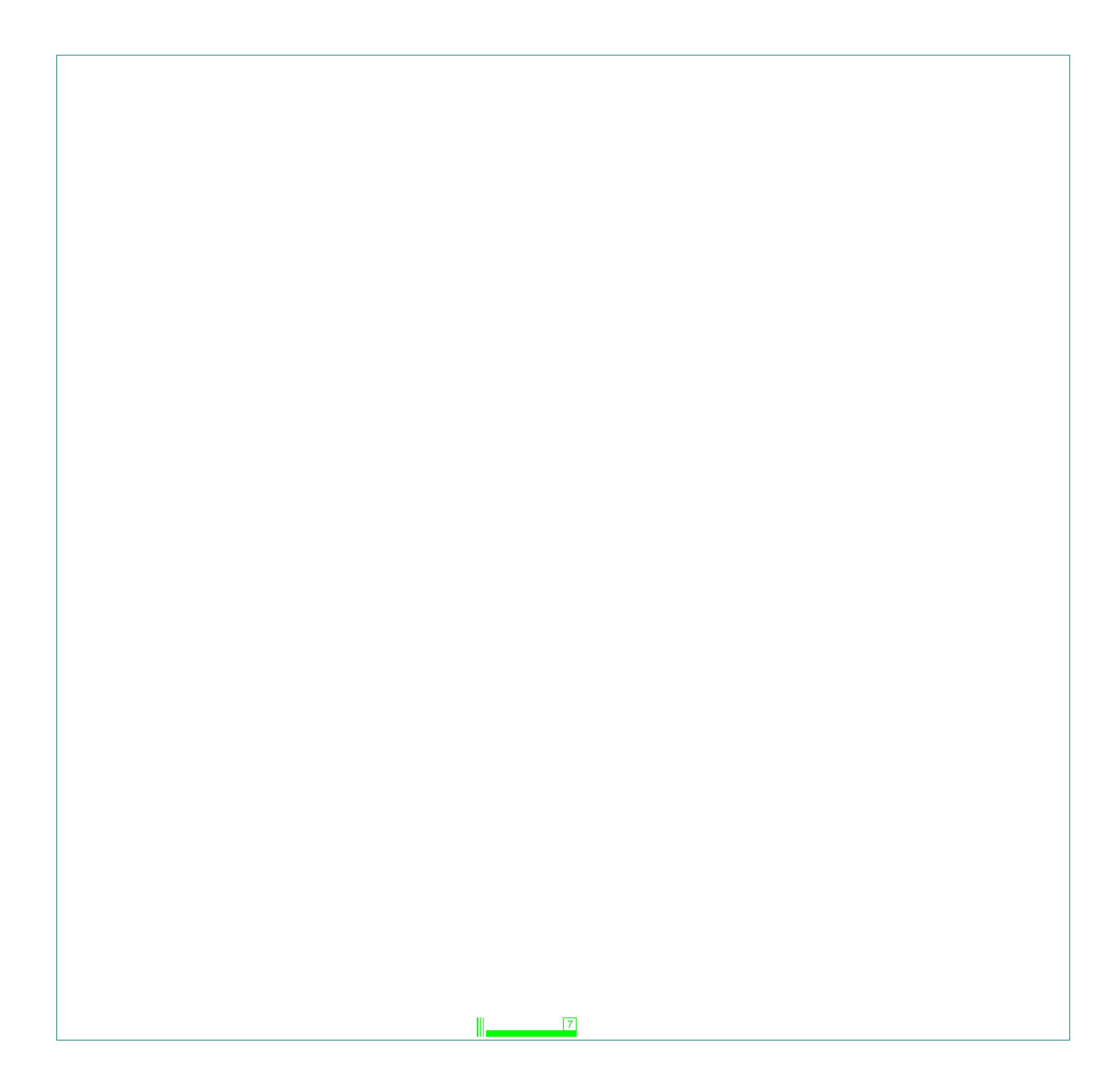
LAYER 5: SIG1



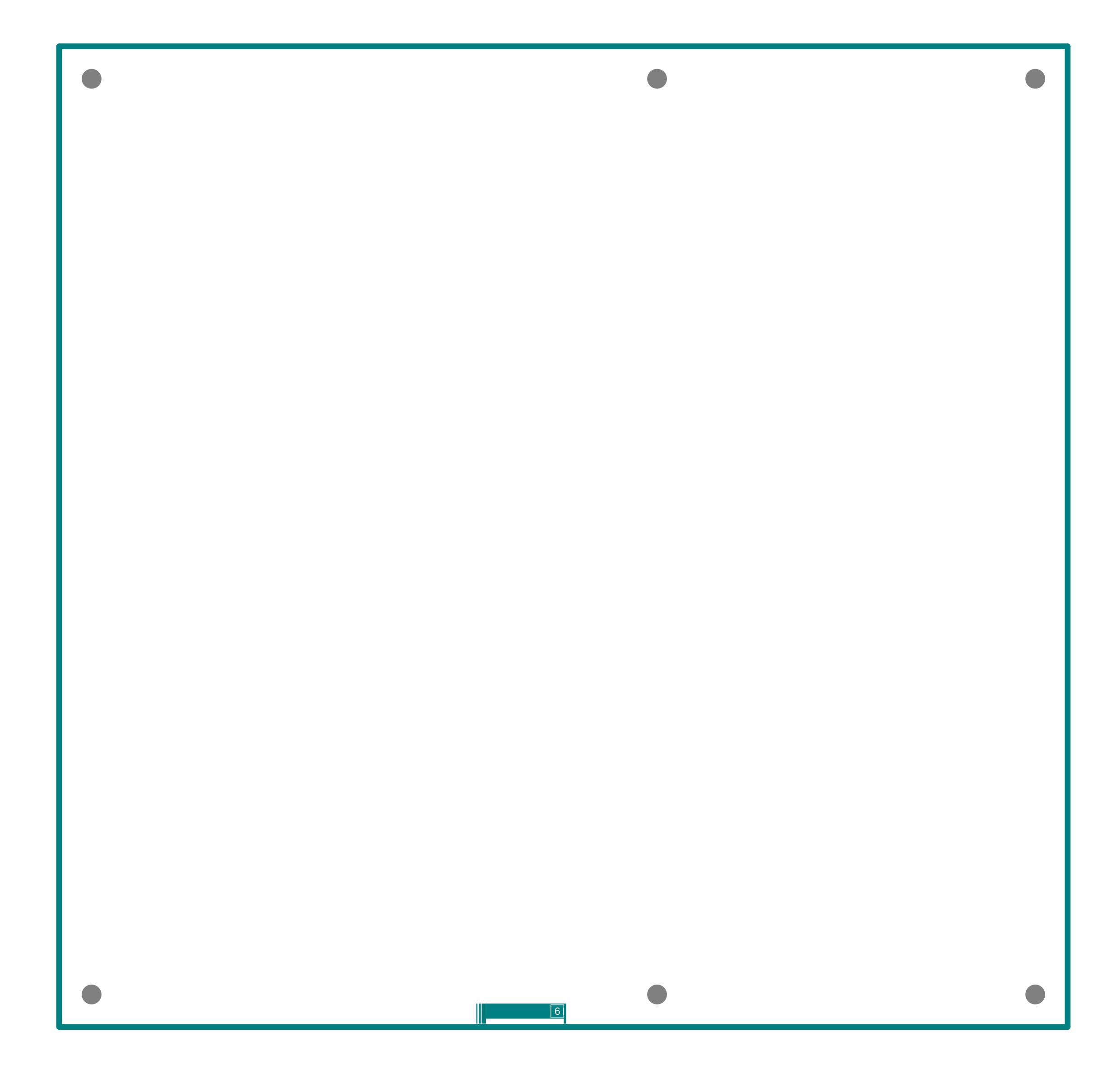
LAYER 3: PWR1



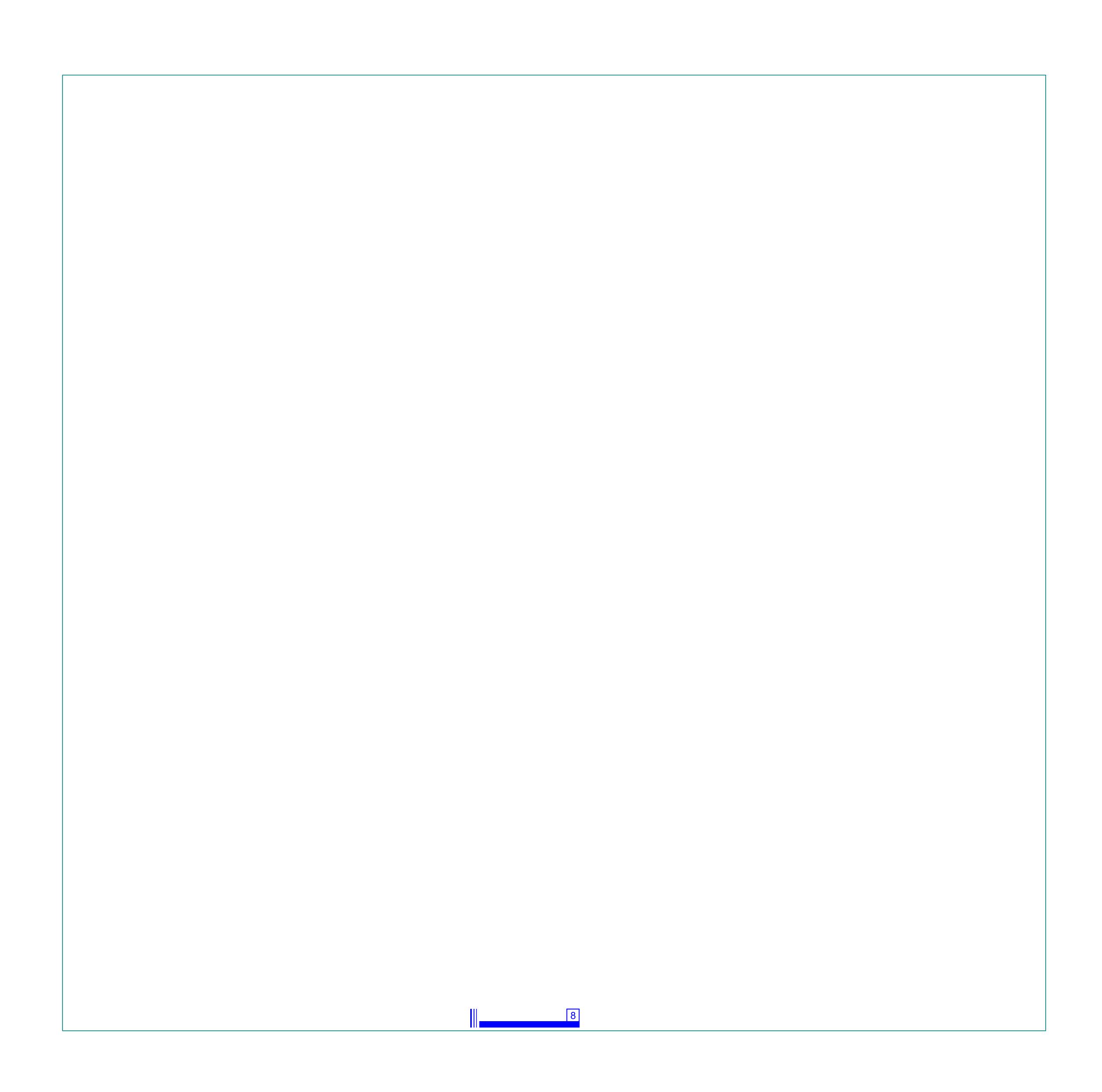
LAYER 4: GND2



LAYER 7: SIG2



LAYER 6: GND3



LAYER 8: BOTTOM

TOP SILKSCREEN

BOTTOM SILKSCREEN

TOP SOLDERMASK LAYER 1: TOP

LAYER 8: BOTTOM
BOTTOM SOLDERMASK

LAYER 1: TOP

BOARD OUTLINE

TOP PASTEMASK

TOP

1

USB-C BREAKOUT

LAYER 8: BOTTOM

BOARD OUTLINE

BOTTOM PASTEMASK

USB-C BREAKOUT

1.000

+

+

+

BOARD OUTLINE

TOP ASSEMBLY
TOP DESIGNATORS
TOP COURTYARD

(+)

(+)

(+)

+

+

+

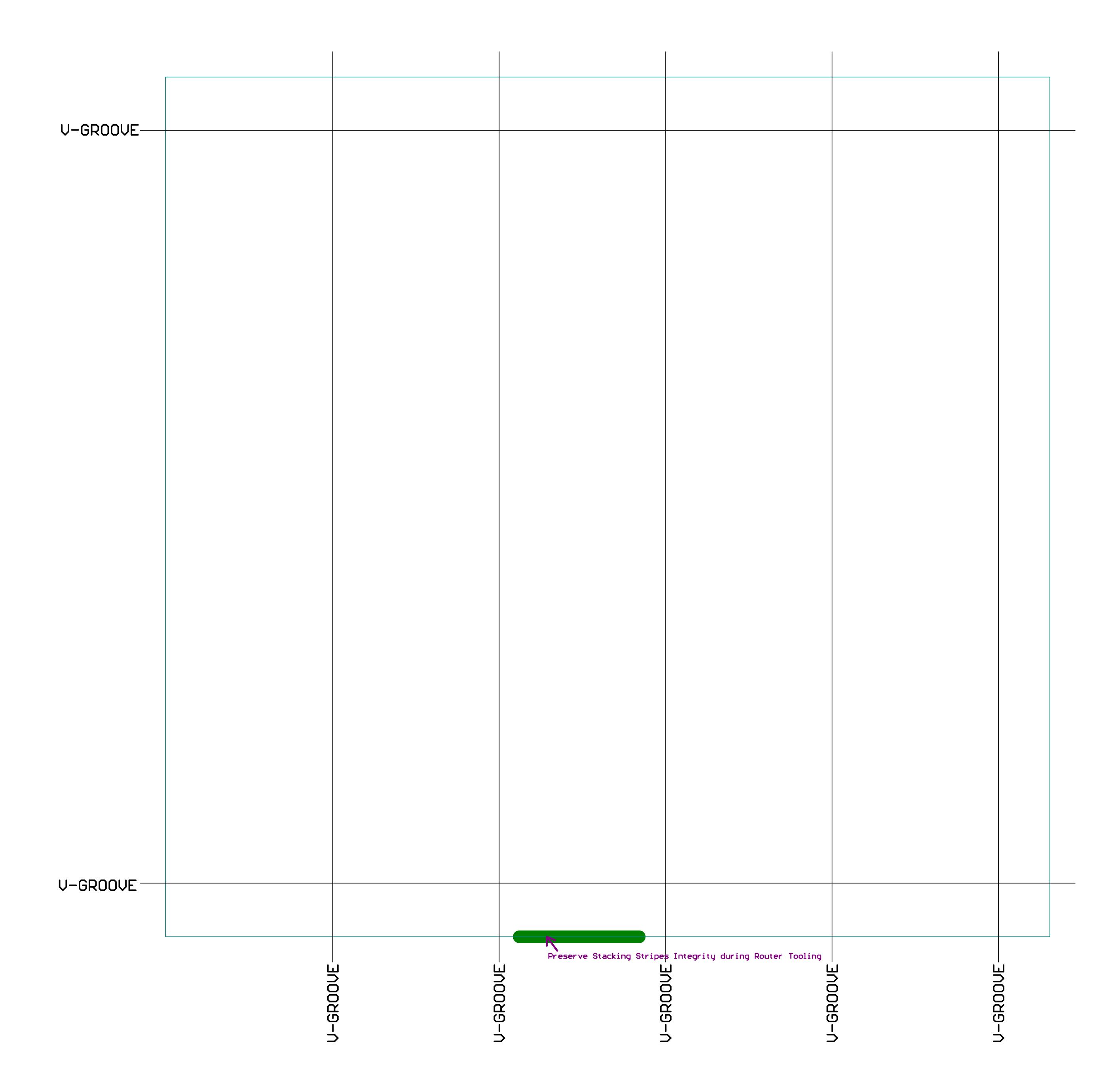
BOARD OUTLINE

BOTTOM ASSEMBLY
BOTTOM COURTYARD
BOTTOM DESIGNATORS

+

(+)

(+



BOARD OUTLINE
PANEL OUTLINE
ROUTE TOOL PATH
PANEL V-GROOVE

MANUFACTURING NOTES