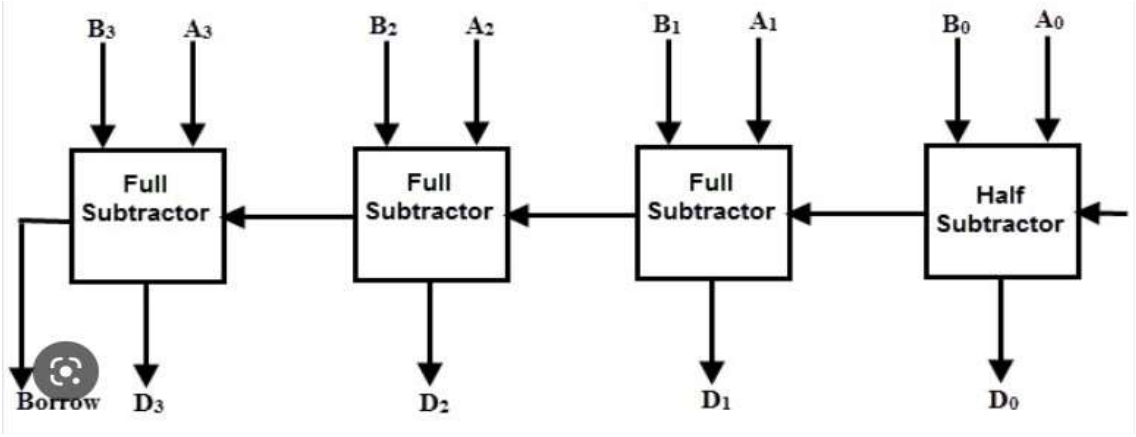
EXPERIMENT-4

4Bit Subtractor

Objective: Verification of 4bit Subtractor by using EDA playground

Theory: A 4bit binary subtractor is a combinational logic gates

Block Diagram: -



Implementation: -

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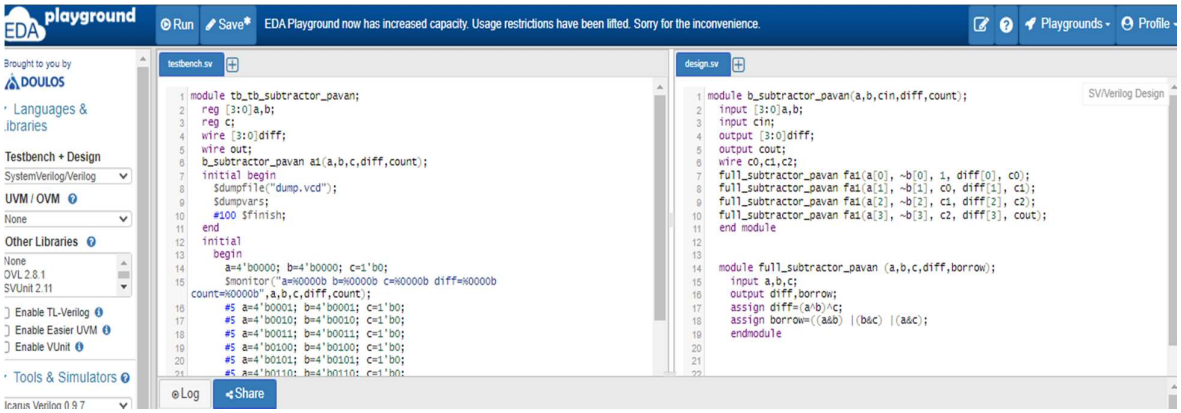
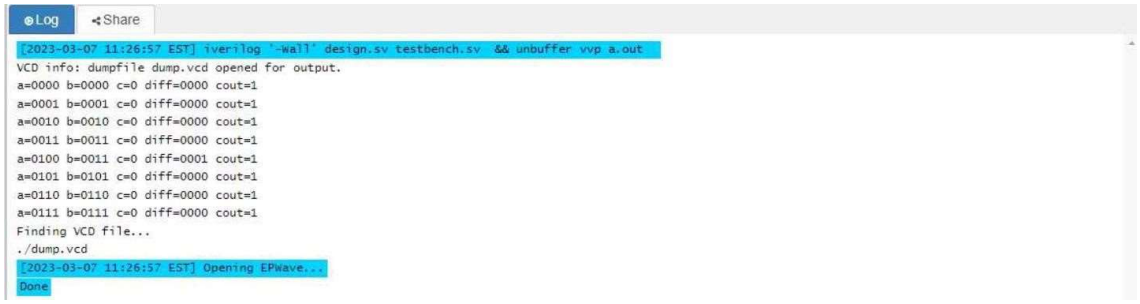
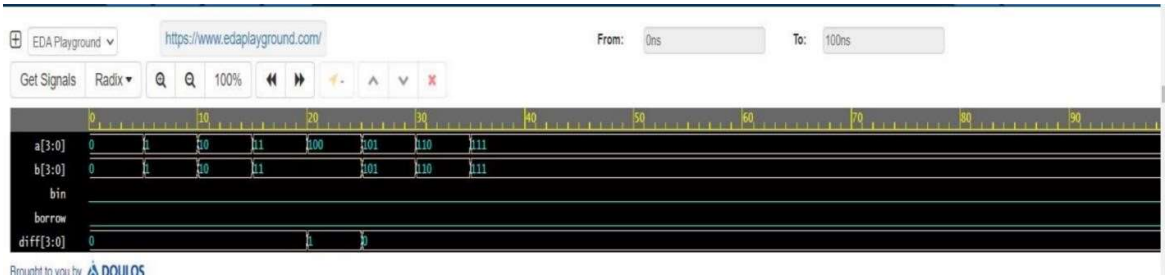


Output: -



Waveform: -



Observations:

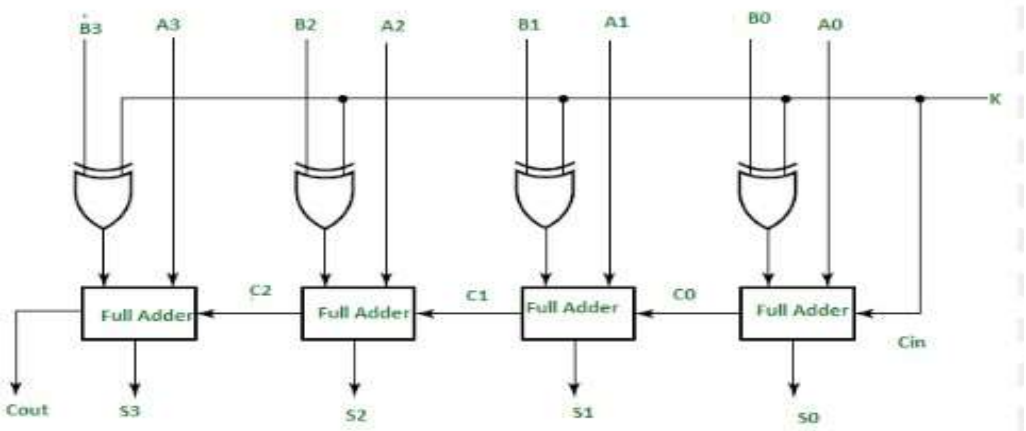
Finally we verified 4-bit subtractor by Verilog code by using EDA play ground

4Bit adddder

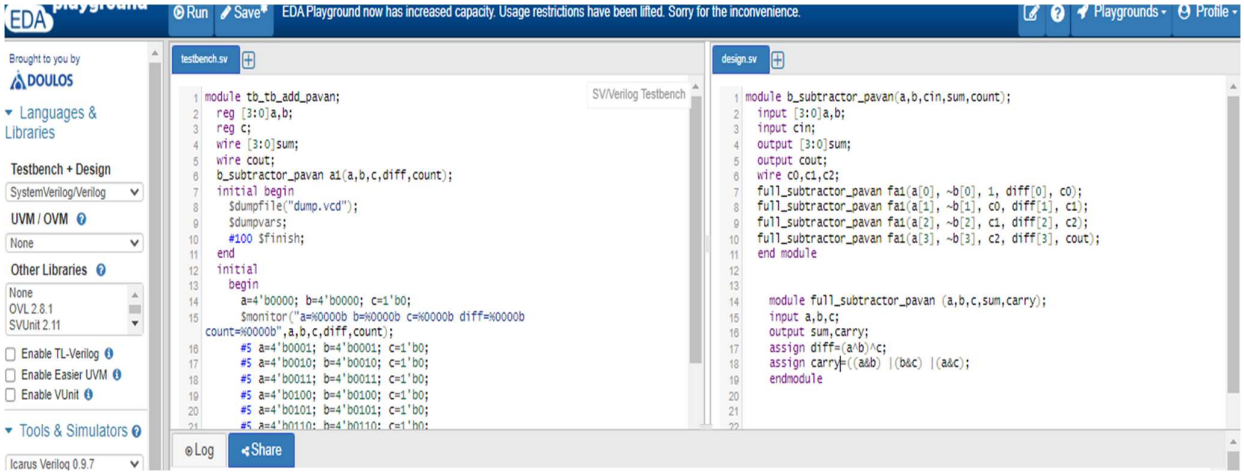
Objective: Verification of 4bit Adder by using EDA playground

Theory: A 4bit binary adder is a combinational logic gates

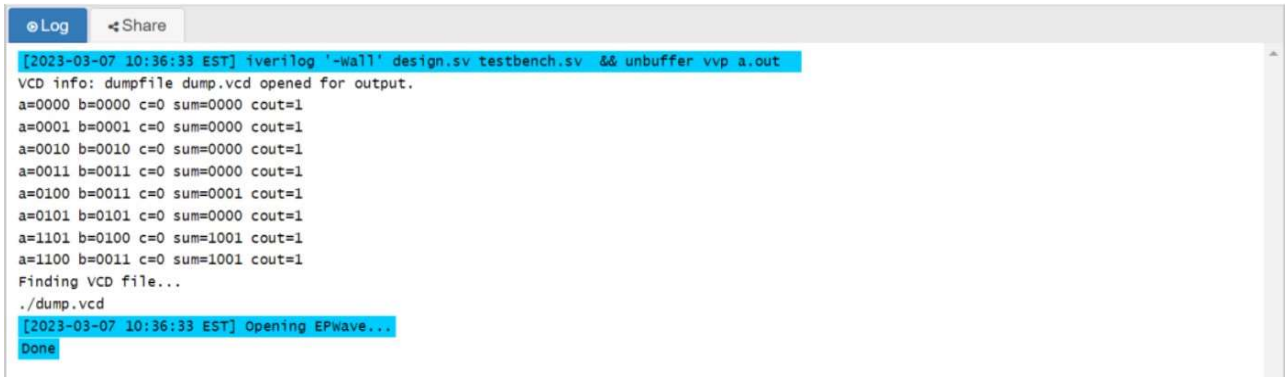
Block Diagram: -



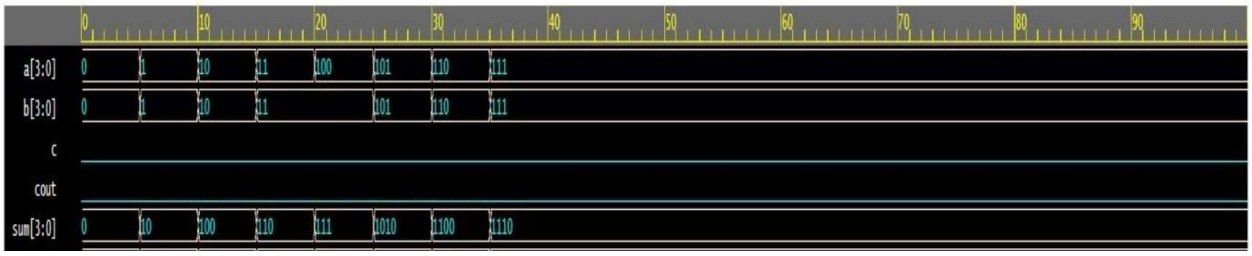
Implementation: -



Output:



Waveform: -



Observations:

Finally we verified 4-bit Adder by Verilog code by using EDA play ground