EC21101 Basic Electronics

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Book: Electronic Circuits: Analysis & Design Author: D. A. Neamen

Syllabus (brief):

- 1. RC circuit theory
 2. Diodes

 - 3. Transistors: BJTs 4 FETs
 - 4. Op-amps
 - 5. Digital circuits

Assessments:

- 1. Attendance : 5 (De-registration by IITKGP)
- 2. Class Tests 1 & 2 : 10
- 3. Assignments 1 & 2 : 10
- 4. Mid-sem : 25
- 5. End-sem

R.C Circuit Theory: Filters

R → Resistors; C → Capacitors; L → Inductors

Component's resistance to an AC.

Reactance by 'R' = R = XR

"'C' = $\frac{1}{\omega c} = \frac{1}{2\pi f c} = Xc$ "'L' = $\omega L = 2\pi f L = XL$

Filters: DPassive 4 Active (types)

2) Low pass, high pass, band pass, band stop & all pass (types)

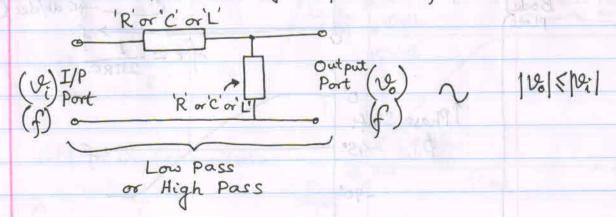
3) First order, second order, ... (types)

4) Butterworth, Chebycher, Bessel, etc. (types)

>Filters are primarily freq. selective circuits.

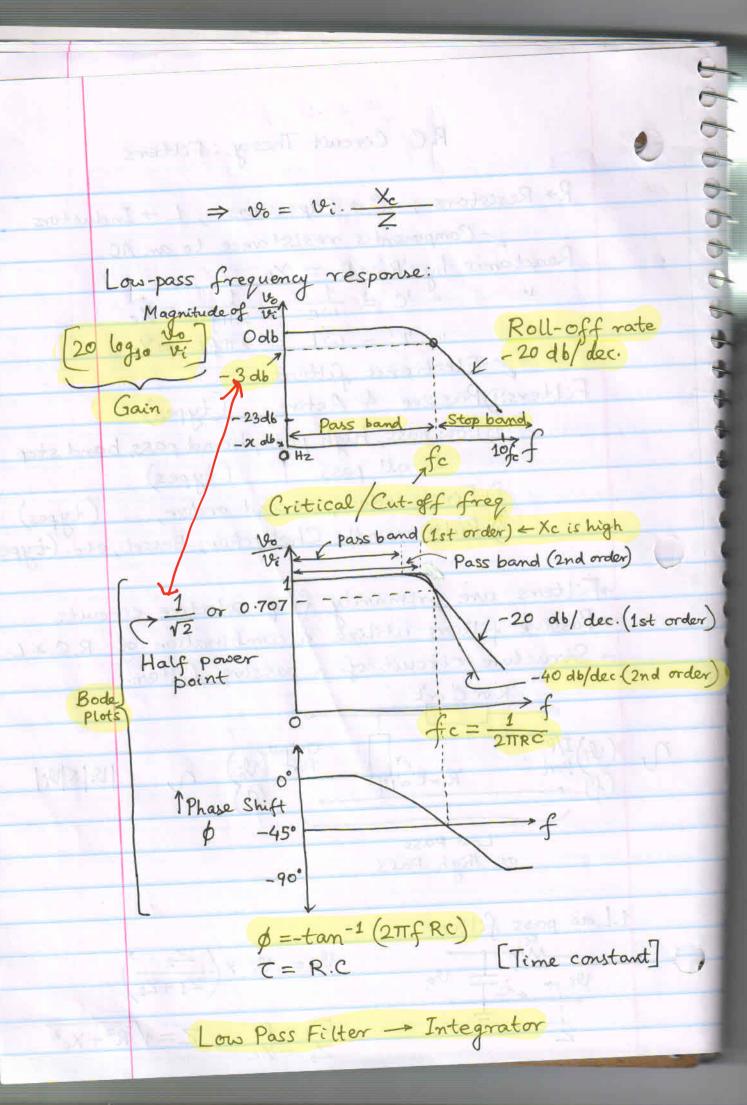
-> Passive filters utilize a combination of R,C&L.

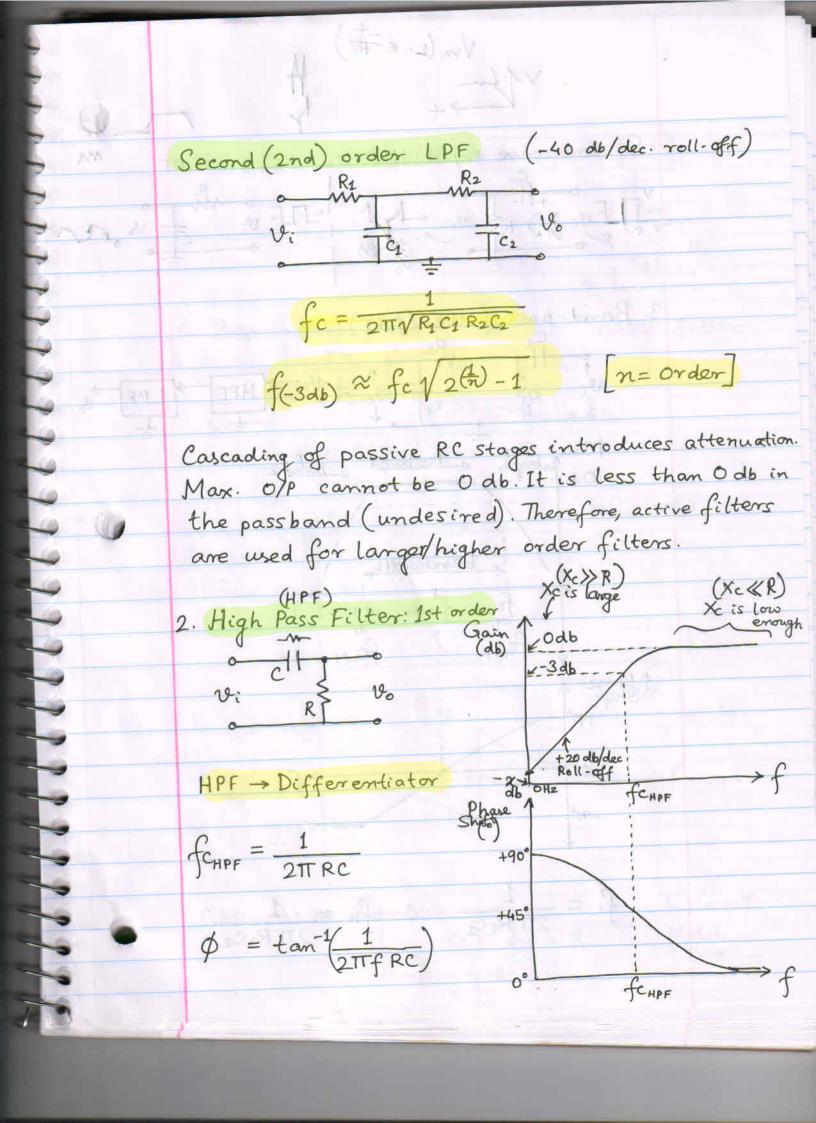
-> Structure/circuit of a passive filter:



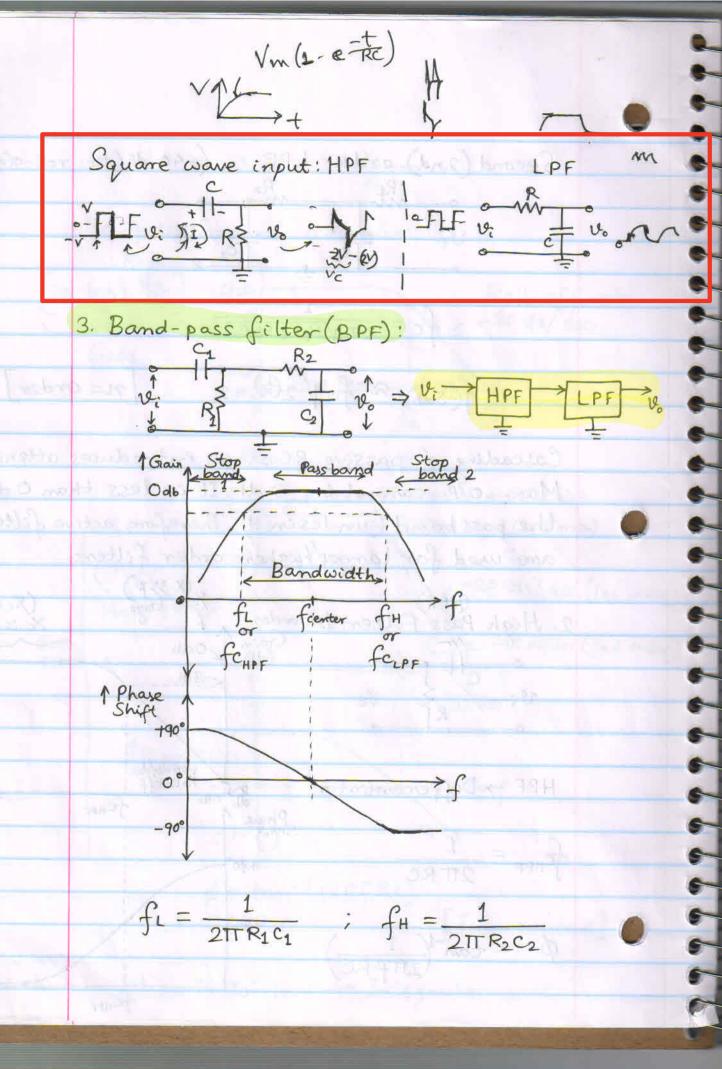
1. Low pass filter

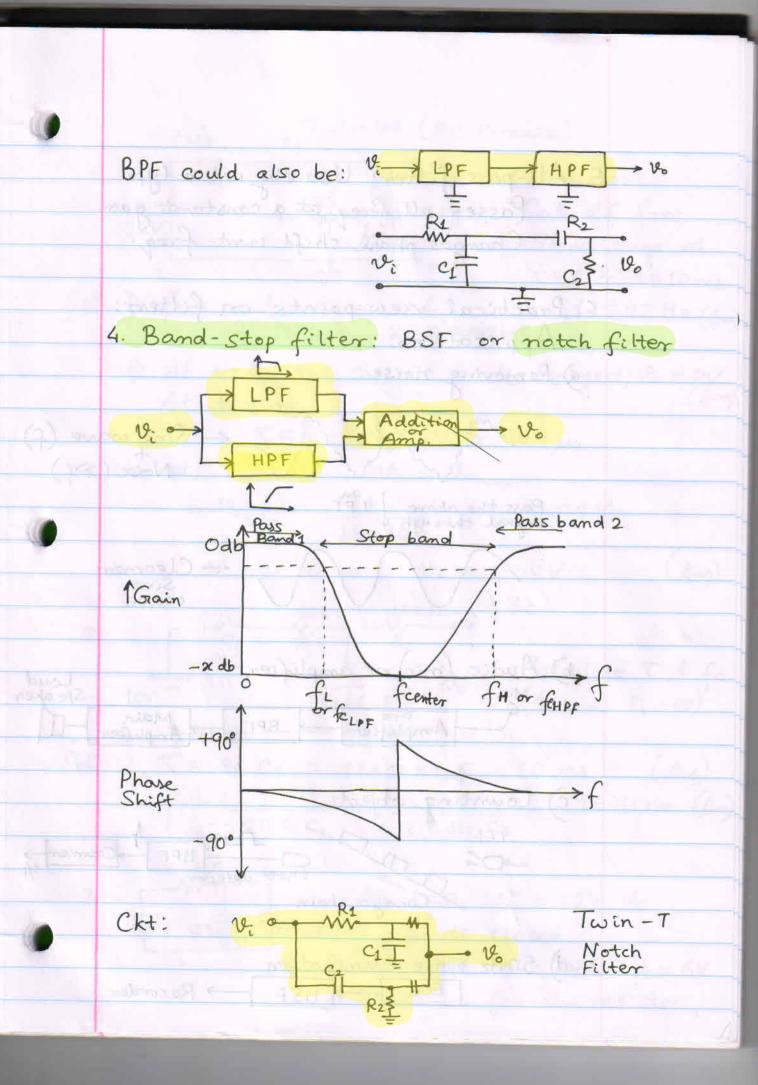
$$v_0 = v_1 \times \left(\frac{Z_2}{Z_1 + Z_2}\right)$$
 $v_1 + v_2 = v_3$
 $v_2 = v_1 \times \left(\frac{Z_2}{Z_1 + Z_2}\right)$
 $v_3 = v_4 \times \left(\frac{Z_2}{Z_1 + Z_2}\right)$
 $v_4 = v_2 \times \left(\frac{Z_2}{Z_1 + Z_2}\right)$

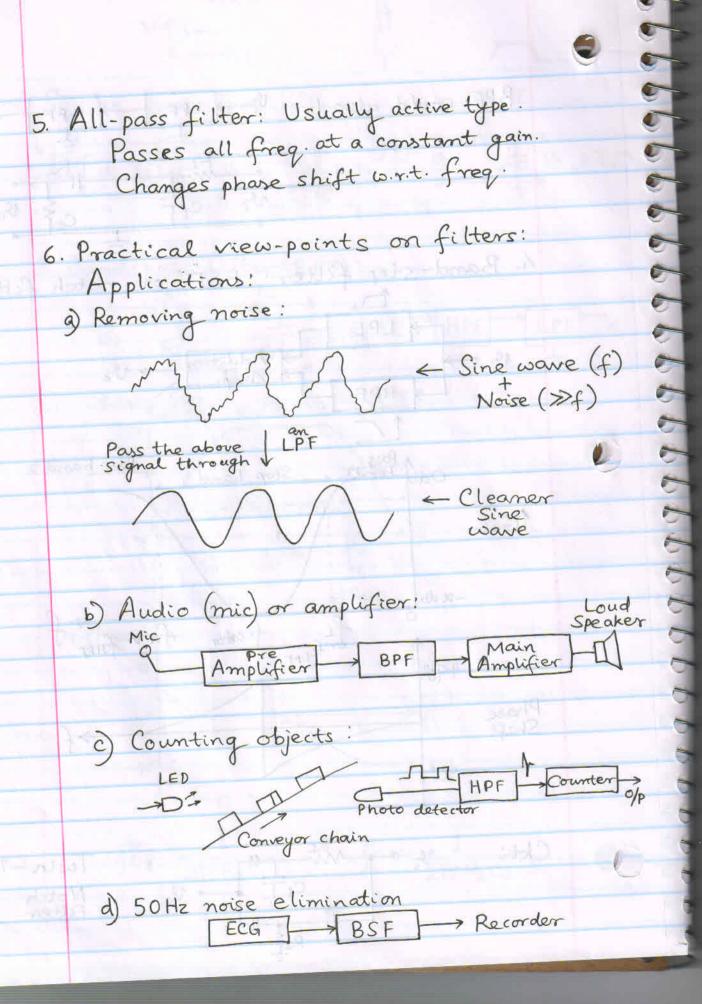


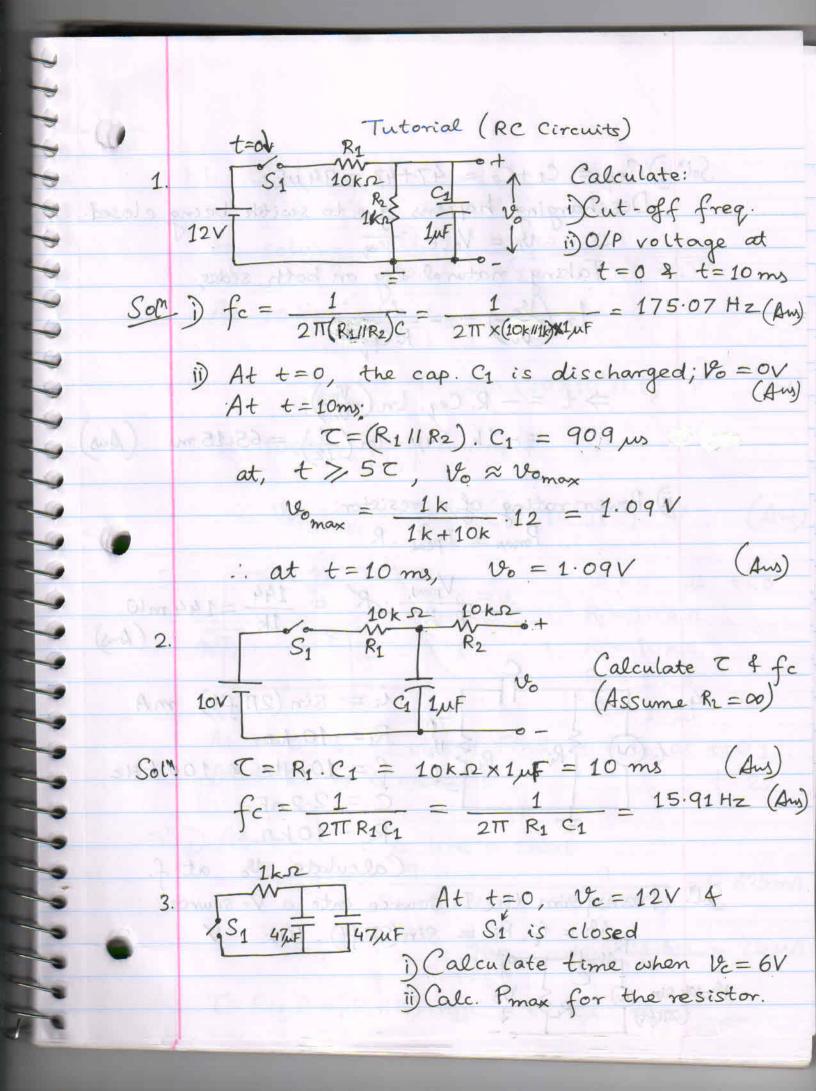


Sq. Wave Output Input Wave









Sol. i) Ceq = C1+C2 = 47+47 = 94 MF Discharging happens due to switch being closed.

... $v_c = V_0 \cdot e^{-\frac{t}{R c_{eq}}}$ Taking natural log on both sides, $\Rightarrow t = -R. Ceq. ln(\frac{V_E}{V_O})$ $=-1k.94\mu.\left(n\left(\frac{6}{12}\right)=65.15 \text{ ms}\right)$ ii) Power rating of a resistor: Pmax = Ipeak . R $=\frac{V_{\text{peak}}^2}{RR} \cdot R = \frac{144}{1k} = 144 \text{ mW}$ i= Sin (2TTft) mA Ri= 10 ks f = 10kHz & 100 kHz = 2.2nFR1 = 10 K2 Calculate to at f. Sol". Transform the I- source into a V- source. Vi = i, Ri = Sin(2TIft). 10k V

 $\left| \frac{V_0}{V_i} \right| = \frac{R_L}{\sqrt{R_L^2 + (X_c + R_i)^2}}$ by solving, $\frac{|V_0|}{|V_i|} = \frac{\omega R_L C}{\sqrt{1 + \omega^2 C^2 (R_i + R_L)^2}}$ At 10 KHz, | vo | = 0.47 :. Vo | = 4.7 Sin (20 x 103 TTt) V (Aw) At 100kHz, | 100 | = 0.499 .. Vo 1006Hz = 5 Sin (20 x 104 TT t) V

 S_1 $\overline{I_1}$ $\overline{I_2}$ $\overline{I_$ Vc = 0 at t<0 R3 = 1 k.r. C = 10 uf

At t=0, S_1 is closed.) Find I_1 & I_2 at t=0,1S. At t=2s, S_1 is opened i) " " " t=2s.

Application to the following the design of t Solⁿ i) At t=0, C is like a short. $I_1|_{o_s} = \frac{9}{R_1 + (R_2/IR_3)} = \frac{9}{10\kappa + (10\kappa/I/1k)} = 0.825 \text{ mA}$

 $I_2|_{os} = \frac{R_3 \cdot I_{1(os)}}{R_2 + R_3} = \frac{1k \times 0 \cdot 825m}{10k + 1k} = 75 \mu A$

T= Req. C = [(R1 11 R2) + R3]. C = 6k. 10 ms

: 1s >> T

at 4.5V

At 1s, C is fully charged, No 'I' through C. $I_{1(1s)} = I_{2(1s)} = \frac{9}{R_1 + R_2} = \frac{9}{20k} = 0.45 \text{ mA}$ (Aus)

i) At t=2s, battery is disconnected.

 $I_{1(2s)} = 0$ $I_{2(2s)} = \frac{4.5}{R_2 + R_3} = \frac{4.5}{10k + 1k} = 409\mu A$