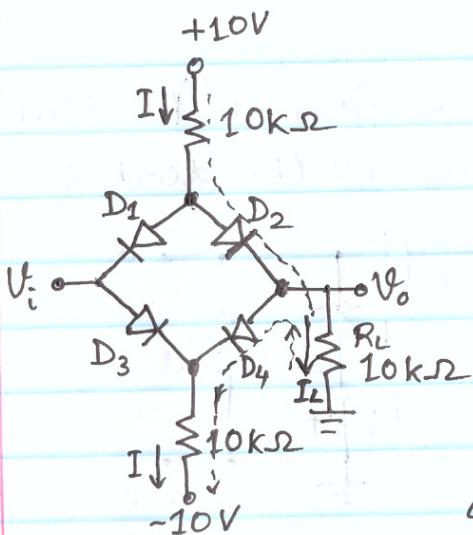


Sol":

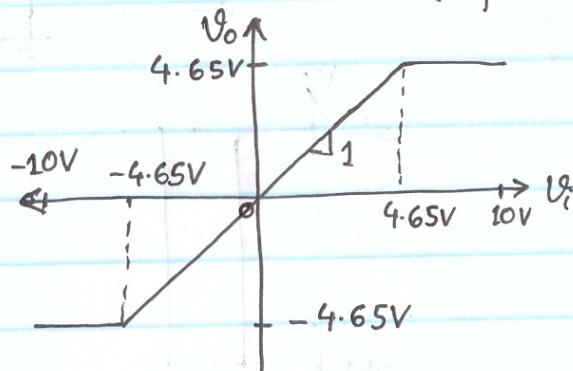


For,  $v_i > 0$ ,  $D_1$  &  $D_4$  are off.

$$I_L = \frac{10 - 0.7}{20 \times 10^3} = 0.465 \text{ mA}$$

$$V_o = I_L R_L = 0.465 \times 10^3 \times 10 \times 10^3 = 4.65 V$$

$$\therefore V_o = V_i \text{ for } V_i = \pm 4.65V$$

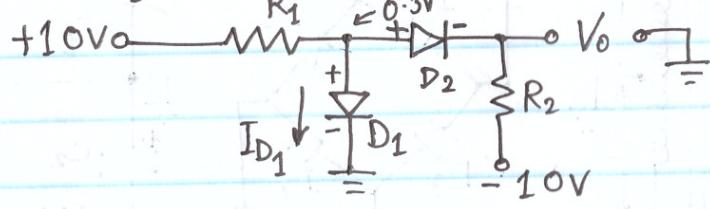


Similarly, while  $v_i < 0$ ,  $D_2$  &  $D_3$  are off.

$$-I_L = I_1 = \frac{10 - 0.7}{20k} = 0.465 \text{ mA}$$

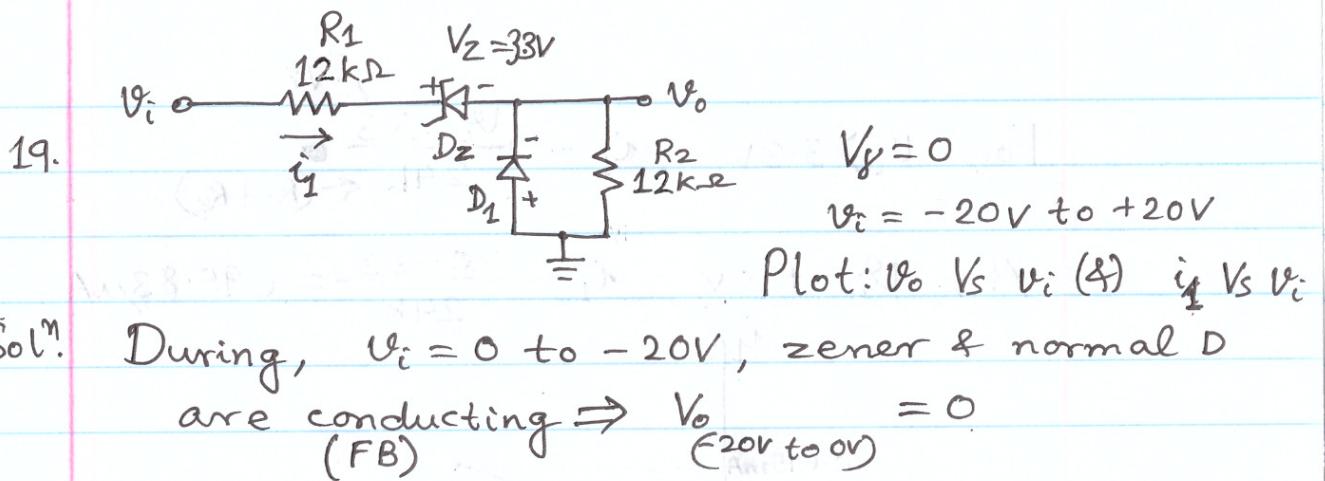
$$V_o = - I_L \cdot R_L = -4.65 V$$

18. Find  $I_{D_1}$  &  $V_o$  if  $V_g = 0.3V$ ,  $R_1 = 6.8k\Omega$  &  $R_2 = 12k\Omega$



Sol<sup>n</sup>: While,  $D_1 \neq D_2$  are ON,  $V_o = +3 - 3 = 0V$  (Ans)

$$\text{Now, } I_{D_1} = \frac{10 - 0.3}{6.8k} - \frac{0 - (-10)}{12k} = 0.593 \text{ mA} \quad (\text{Ans})$$



Sol<sup>n</sup>. During,  $V_i = 0 \text{ to } -20V$ , zener & normal D are conducting  $\Rightarrow V_o = 0$

For,  $V_i = 0 \text{ to } +3.3V$ , zener is in reverse blocking mode (not in breakdown)  $\Rightarrow i_1 = 0 \text{ & } V_o = 0$

For,  $V_i > 3.3V$ , Z-diode enters into brk-dwn state.

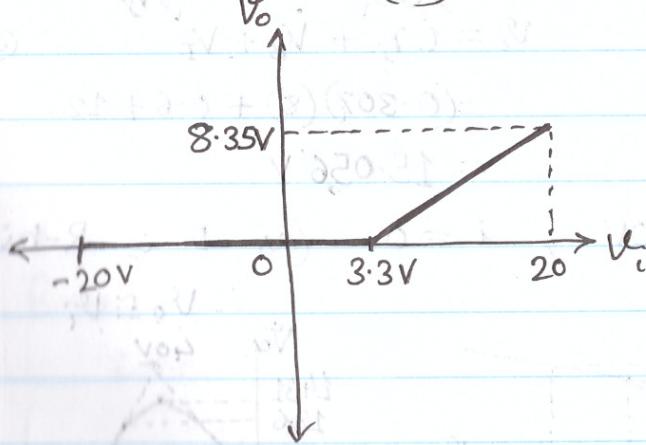
$$i_1 = \frac{V_i - 3.3}{12k + 12k} \text{ A}$$

$$\text{&}, \quad V_o = \left[ \frac{V_i - 3.3}{12k + 12k} \right] (12k) = \frac{V_i - 3.3}{2}$$

At,  $V_i = +20V$ ,

$$i_1 = \frac{20 - 3.3}{24k} = 695.83 \mu\text{A}$$

$$V_o = 8.35V$$

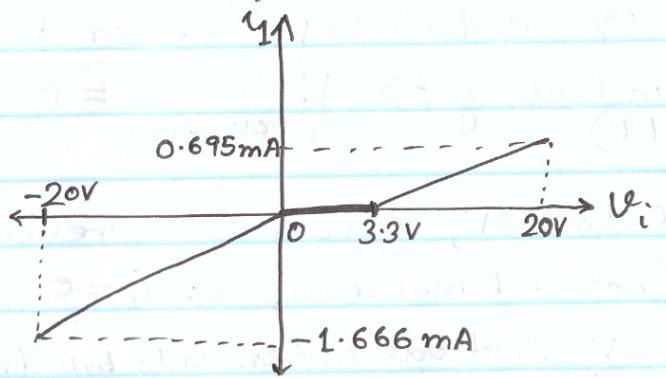


For,  $V_i < 0$ ,  $V_o = 0V \text{ & } i_1 = \frac{0 - V_i}{12k} = \frac{0 - (-20)}{12k}$

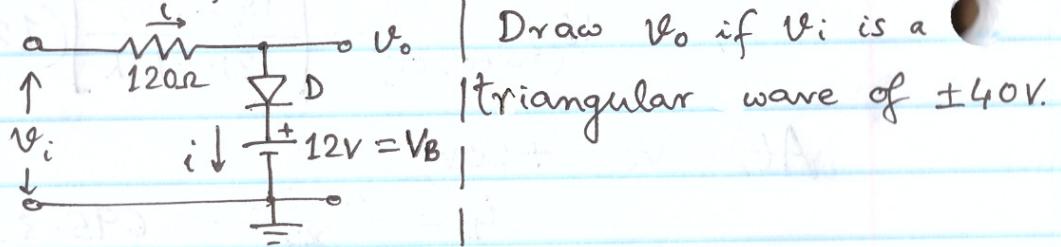
$i_1 = -1.666 \text{ mA}$

$$\text{For, } V_i > 3.3V, \quad i_1 = \frac{V_i - 3.3}{24k} \leftarrow (R_1 + R_2) \quad \swarrow V_2$$

$$\text{At, } V_i = +20V, \quad i_1 = \frac{20 - 3.3}{24k} = 695.83 \mu A$$



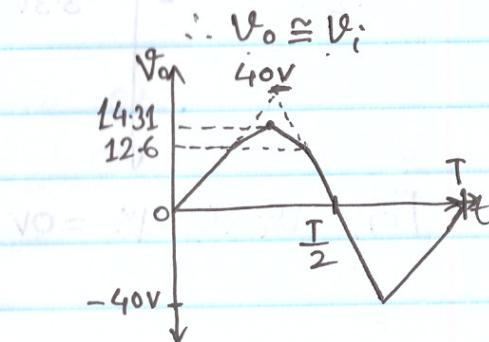
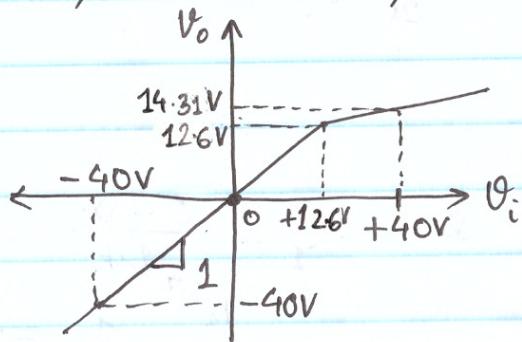
20. If  $V_y = 0.6V$ ,  $r_f = 8\Omega$ ,  $V_i = \pm 40V$ , plot  $V_o$  Vs.  $V_i$ .



$$\text{Soln. For, } V_i = +40V, \quad i = \frac{40 - 0.6 - 12}{120 + 8} = \frac{214.06}{128} \text{ mA}$$

$$\begin{aligned} V_o &= i \cdot r_f + V_B + V_D \quad @ D_1 \text{ is FB} \\ &= (0.214)(8) + 0.6 + 12 \\ &= 14.31V \end{aligned}$$

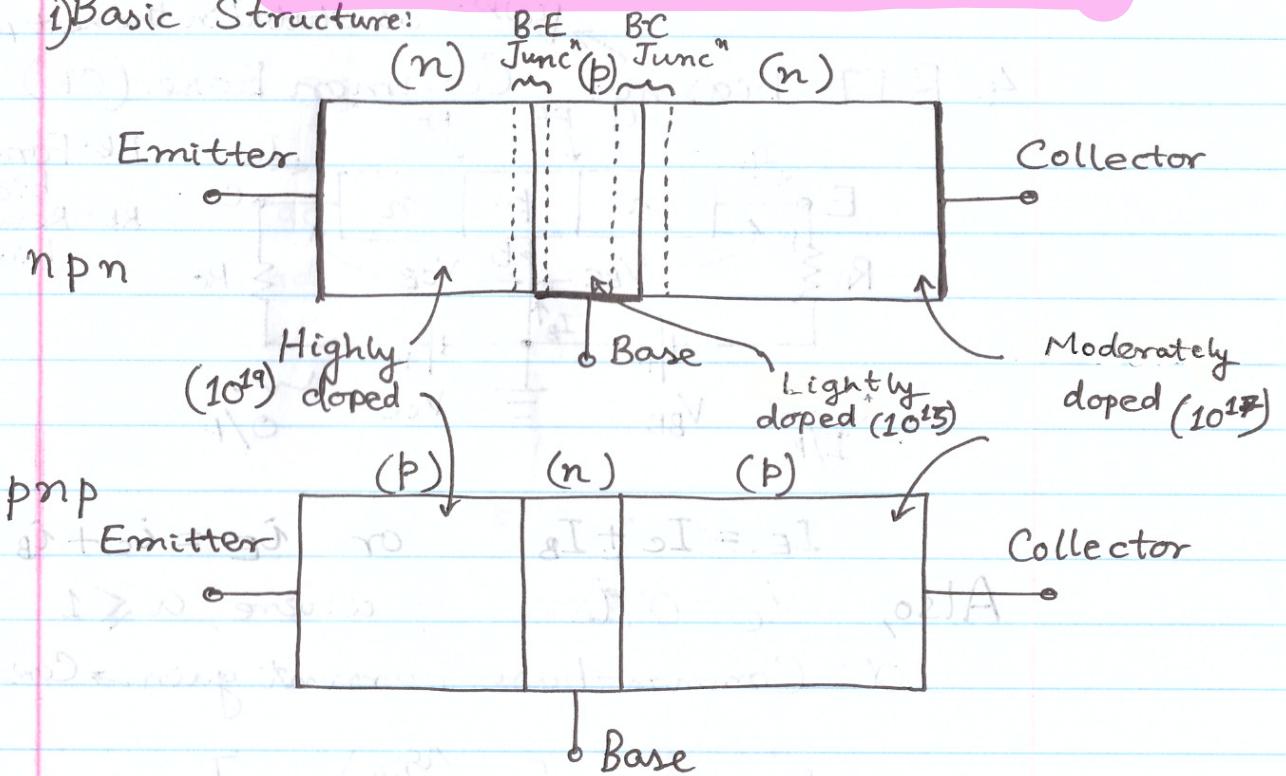
For,  $V_i = -40V$ ,  $i = 0$  (as D is R-biased)



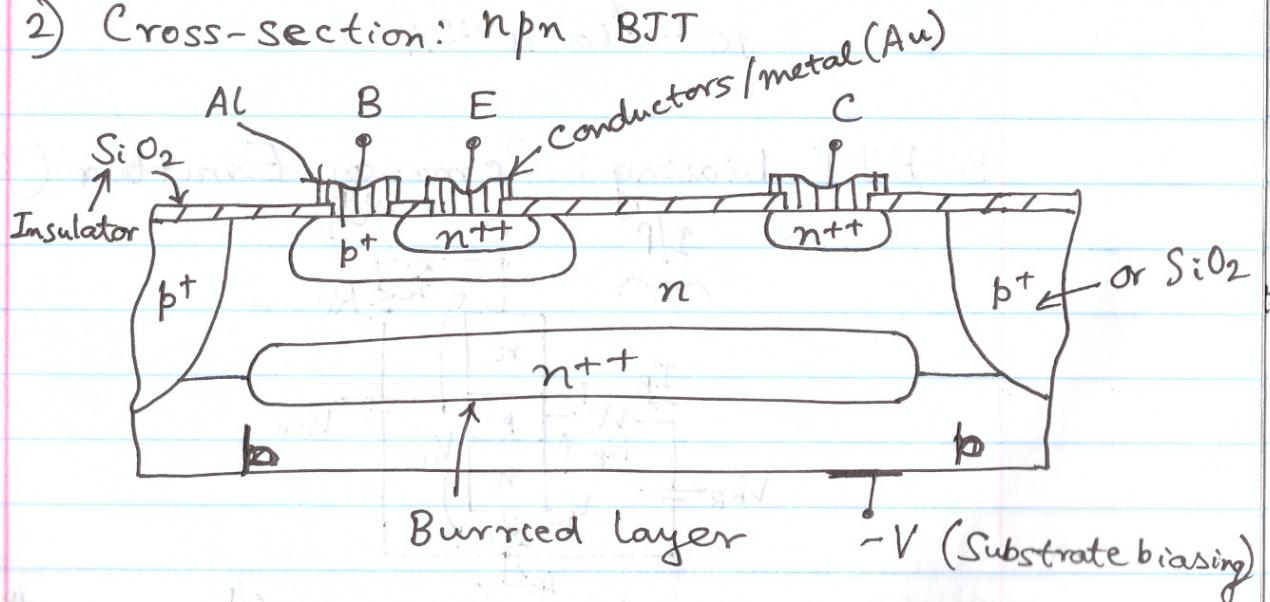
## BJT

### (Bipolar Junction Transistors)

#### i) Basic Structure:



#### 2) Cross-section: Npn BJT



#### 3) Types of BJTs:

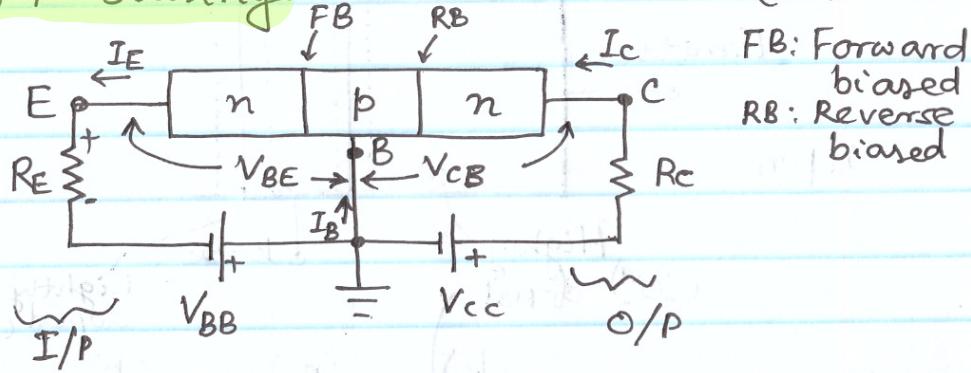
- 1)  $n-p-n$
- 2)  $p-n-p$

• BE junct<sup>h</sup> is Fwd. biased.  
 • BC junct<sup>h</sup> is reverse biased.

## Active Region

Based on modes of operation,  
 as stated in the next page.

### 4. BJT biasing: Common Base (CB)



FB: Forward biased  
 RB: Reverse biased

$$I_c = \alpha \cdot I_e$$

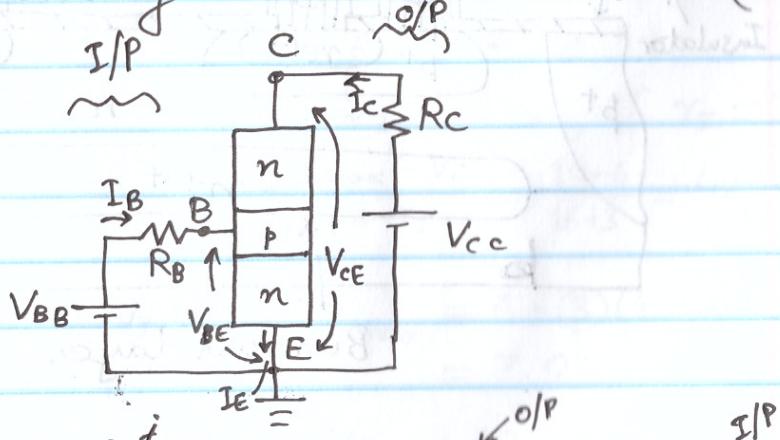
$$i_c = \alpha \cdot i_e \quad ; \text{ where, } \alpha \leq 1$$

$\alpha$ : Common-base current gain  $\rightarrow$  Constant no.

$$i_e = I_{EO} \left[ e^{\left( \frac{V_{BE}}{V_T} \right)} - 1 \right]$$

$10^{-12}$  to  $10^{-15} A$

### 5. BJT biasing: Common Emitter (CE)



$$i_e = i_c + i_b \quad ; \quad i_c = \beta \cdot I_B$$

$$\Rightarrow i_e = (1 + \beta) i_b \quad \beta \gg 1$$

$$\therefore i_c = \left( \frac{\beta}{1 + \beta} \right) i_e$$

$\beta$ : Common emitter current gain.

## Current Relationships

If we treat the bipolar transistor as a single node, then, by Kirchhoff's current law, we have

$$i_E = i_C + i_B \quad (5.7)$$

If the transistor is biased in the forward-active mode, then

$$i_C = \beta i_B \quad (5.8)$$

Substituting Equation (5.8) into (5.7), we obtain the following relationship between the emitter and base currents:

$$i_E = (1 + \beta)i_B \quad (5.9)$$

Solving for  $i_B$  in Equation (5.8) and substituting into Equation (5.9), we obtain a relationship between the collector and emitter currents, as follows:

$$i_C = \left( \frac{\beta}{1 + \beta} \right) i_E \quad (5.10)$$

We can write  $i_C = \alpha i_E$  so

$$\alpha = \frac{\beta}{1 + \beta} \quad (5.11)$$

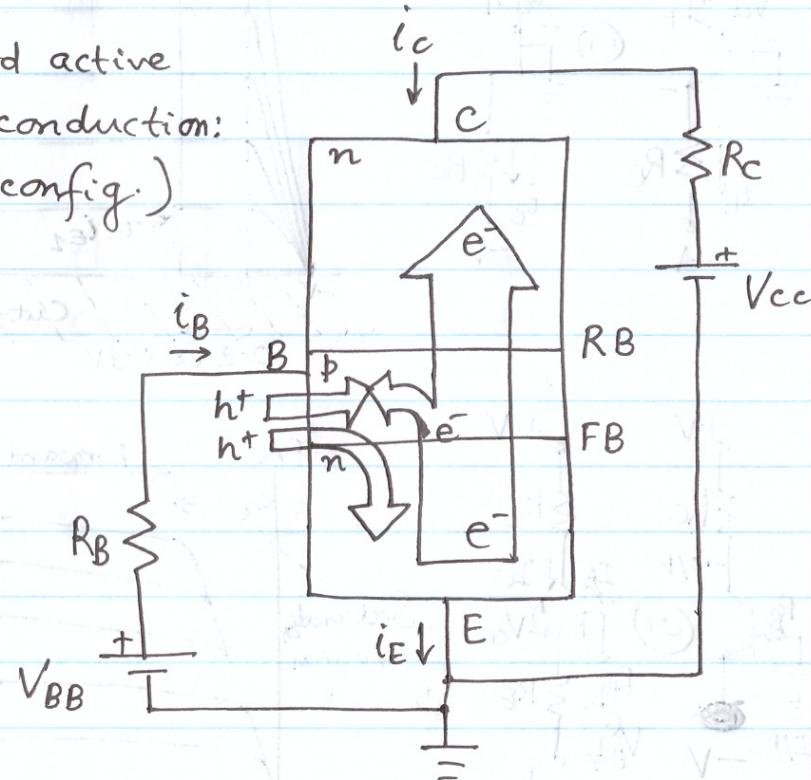
The parameter  $\alpha$  is called the common-base current gain and is always slightly less than 1. We may note that if  $\beta = 100$ , then  $\alpha = 0.99$ , so  $\alpha$  is indeed close to 1. From Equation (5.11), we can state the common-emitter current gain in terms of the common-base current gain:

$$\beta = \frac{\alpha}{1 - \alpha} \quad (5.12)$$

$$\alpha = \frac{\beta}{1+\beta}$$

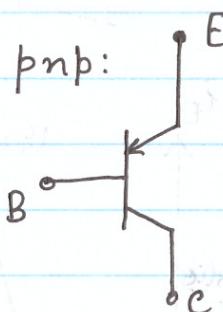
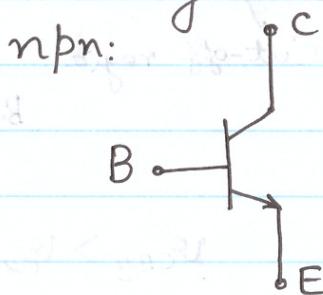
$$(f) \quad \beta = \frac{\alpha}{1-\alpha}$$

5. Forward active mode conduction:  
(CE config.)



$$V_{CC} > V_{BB}$$

6. BJT symbols:



7. a. Transistor modes of operation: (BJT)

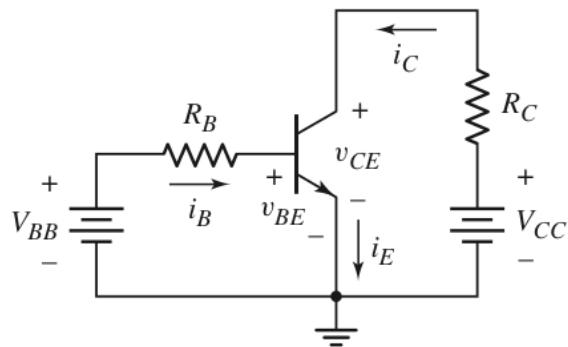
- 1) Common base (CB)
- 2) Common emitter (CE)
- 3) Common collector (CC)

- b. Transistor (BJT) regions of operation:

- 1) Cut-off (OFF switch)
- 2) Saturation (resistor-like)
- 3) Active - Forward (ON switch)  
Reverse with resistance  
(special resistor)
- 4) Breakdown (causes permanent damage)

CB

The C–B voltage can be varied by changing the  $V^+$  voltage (Figure 5.11(a)) or the  $V^-$  voltage (Figure 5.11(b)). When the collector–base junction becomes forward biased in the range of 0.2 and 0.3 V, the collector current  $i_C$  is still essentially equal to the emitter current  $i_E$ . In this case, the transistor is still basically biased in the forward-active mode. However, as the forward-bias C–B voltage increases, the linear relationship between the collector and emitter currents is no longer valid, and the collector current very quickly drops to zero.

CE

**Common-Emitter (CE).** In this circuit, the  $V_{BB}$  source forward biases the B–E junction and controls the base current  $i_B$ . The C–E voltage can be varied by changing  $V_{CC}$ .

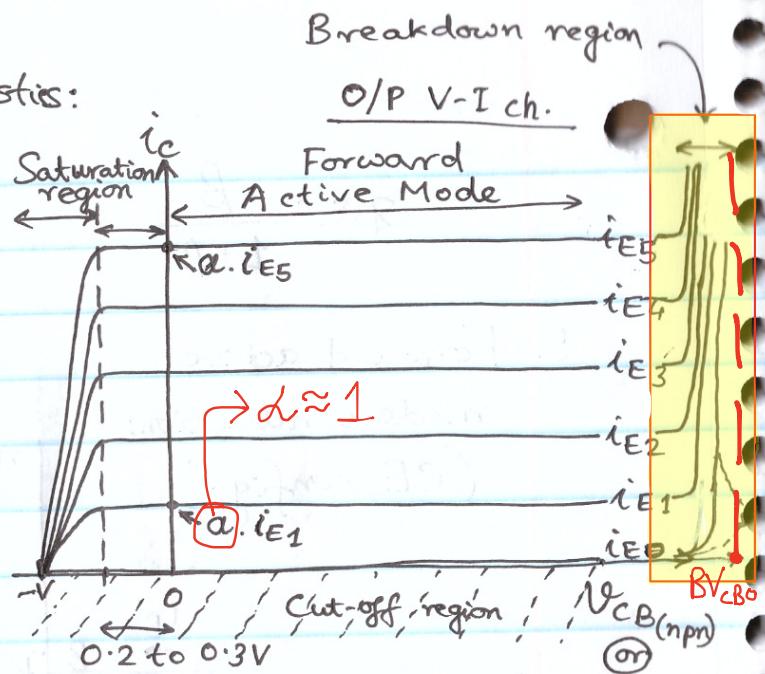
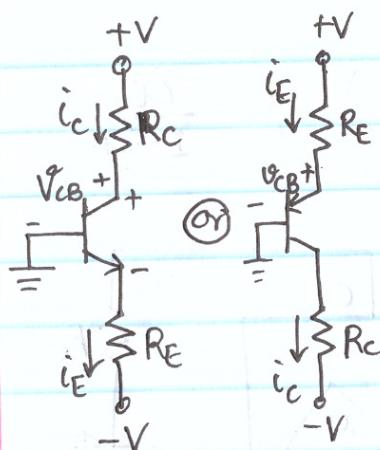
In the npn device, in order for the transistor to be biased in the forward-active mode, the B–C junction must be zero or reverse biased, which means that  $V_{CE}$  must be greater than approximately  $V_{BE}(\text{on})$ .<sup>5</sup> For  $V_{CE} > V_{BE}(\text{on})$ , there is a finite slope to the curves. If, however,  $V_{CE} < V_{BE}(\text{on})$ , the B–C junction becomes forward biased, the transistor is no longer in the forward-active mode, and the collector current very quickly drops to zero.

7c.

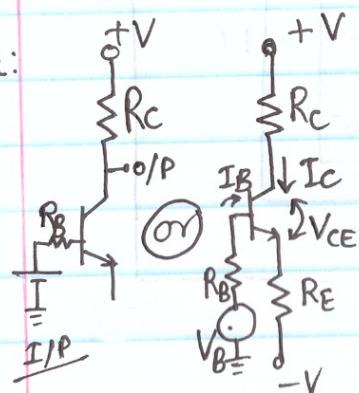
V-I characteristics:

CB:

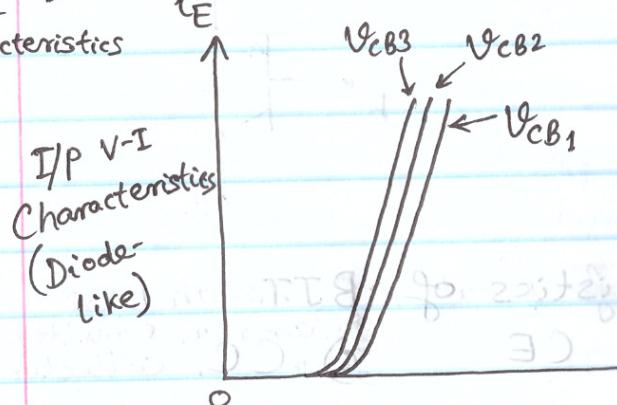
Can be treated as a const. I source.



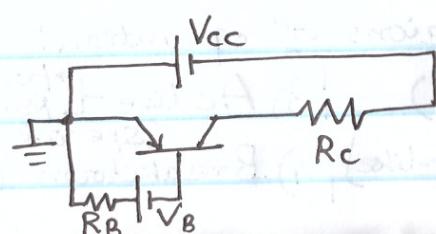
CE:



O/P  
V-I  
Characteristics



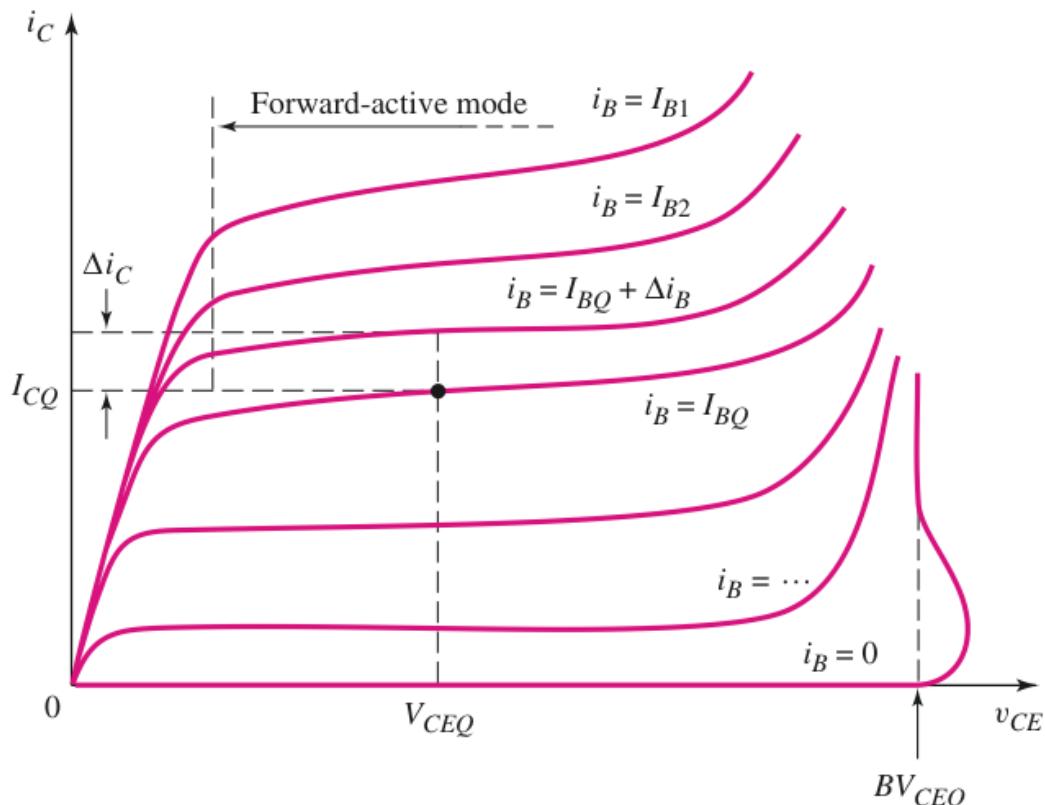
$$V_{CB3} > V_{CB2} > V_{CB1}$$



CE config  
for pnp

## Breakdown Voltage: Common-Emitter Characteristics

Figure 5.18 shows the  $i_C$  versus  $v_{CE}$  characteristics of an npn transistor, for various constant base currents, and an ideal breakdown voltage of  $BV_{CEO}$ . The value of  $BV_{CEO}$  is less than the value of  $BV_{CBO}$  because  $BV_{CEO}$  includes the effects of the transistor action, while  $BV_{CBO}$  does not. This same effect was observed in the  $I_{CEO}$  leakage current.



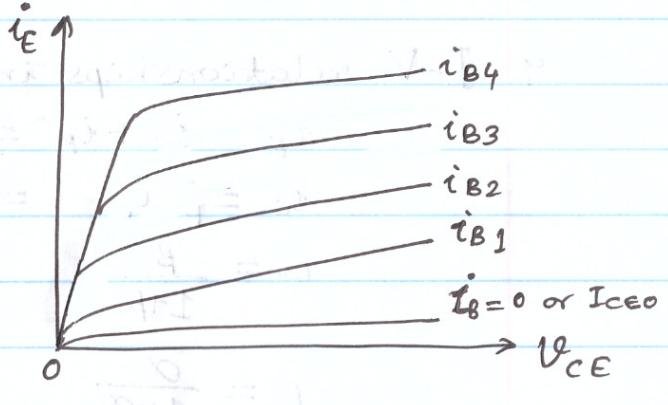
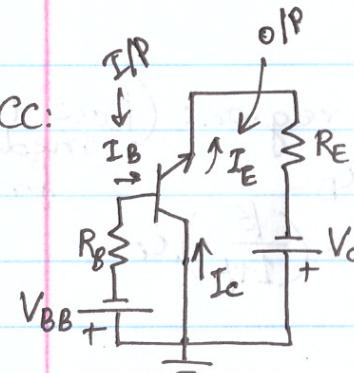
**Figure 5.18** Common-emitter characteristics showing breakdown effects

The breakdown voltage characteristics for the two configurations are also different. The breakdown voltage for the open-base case is given by

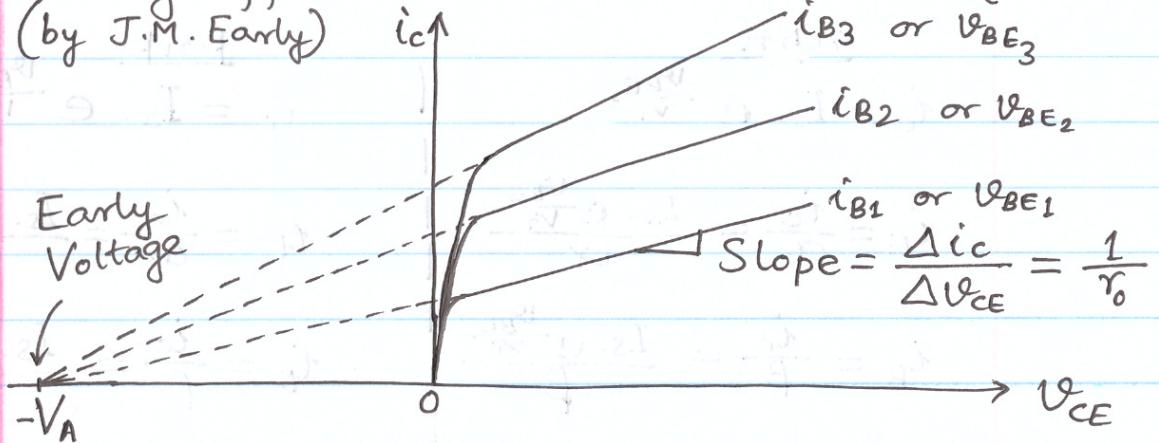
$$BV_{CEO} = \frac{BV_{CBO}}{\sqrt[n]{\beta}} \quad (5.22)$$

where  $n$  is an empirical constant usually in the range of 3 to 6.

## BJT (cont.)



8. Early effect & base width modulation (CE):  
(by J.M. Early)



$r_0$ : O/P resistance

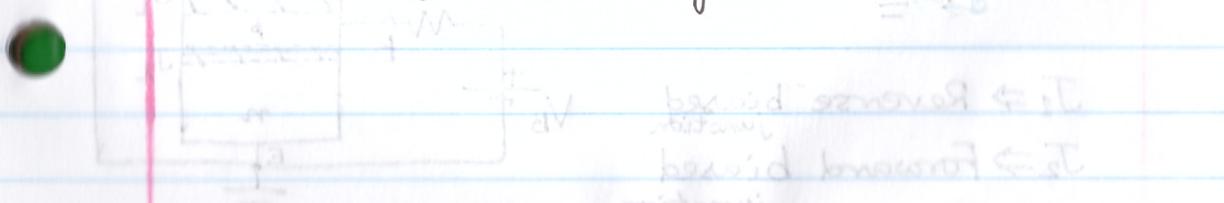
$$r_0 \approx \frac{V_A}{I_C}$$

$$i_C = I_s \cdot e^{\left(\frac{V_{BE}}{V_T}\right)} \cdot \left(1 + \frac{V_{CE}}{V_A}\right)$$

While early effect is considered.

$|V_A|$ : 50 to 300V (typically)

Base width modulation:  $V_{BE} \uparrow \quad i_B \uparrow \quad \text{Junc"} \text{ width (CB)} \downarrow$   
Width of base region  $\uparrow \quad i_C \uparrow$



~~with respect to the C-E voltage in the forward active mode.~~ The slope in these characteristics is due to an effect called base-width modulation that was first analyzed by J. M. Early. The phenomenon is generally called the *Early effect*. When the curves are extrapolated to zero current, they meet at a point on the negative voltage axis, at  $v_{CE} = -V_A$ . The voltage  $V_A$  is a positive quantity called the **Early voltage**. ~~Typical~~