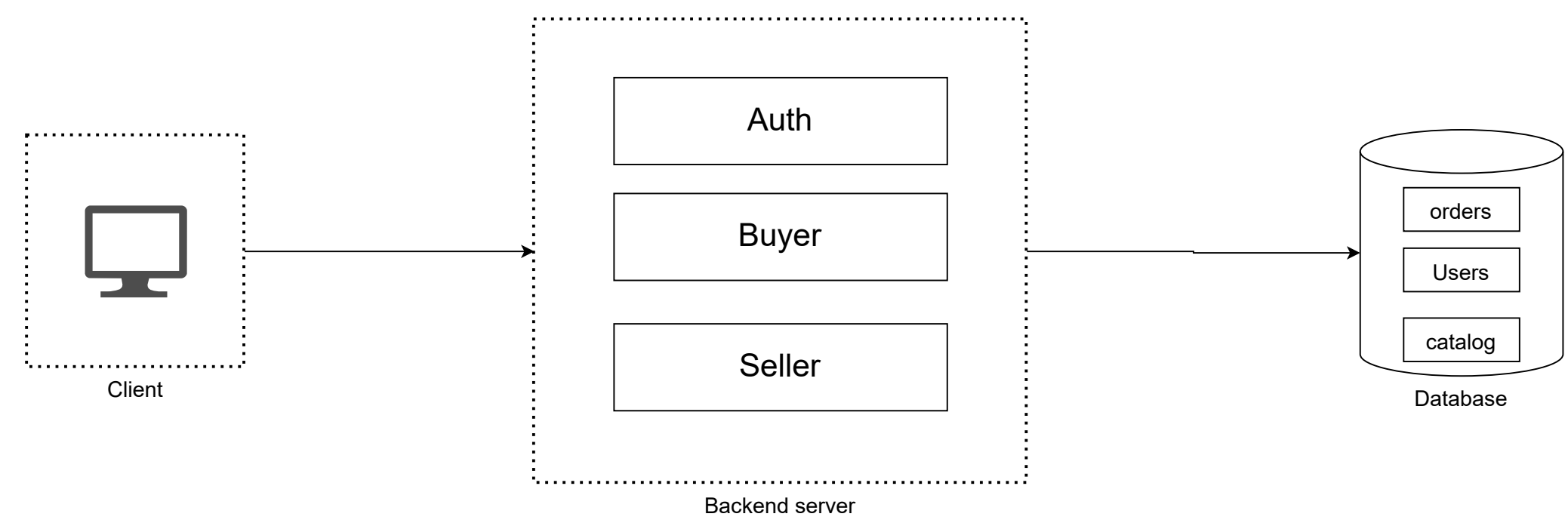


Higher leve overview of the system



system architecture

