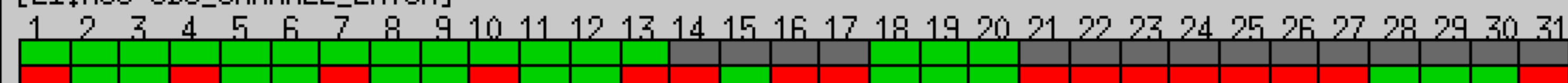


L1:ASC

State-vector (ODC)

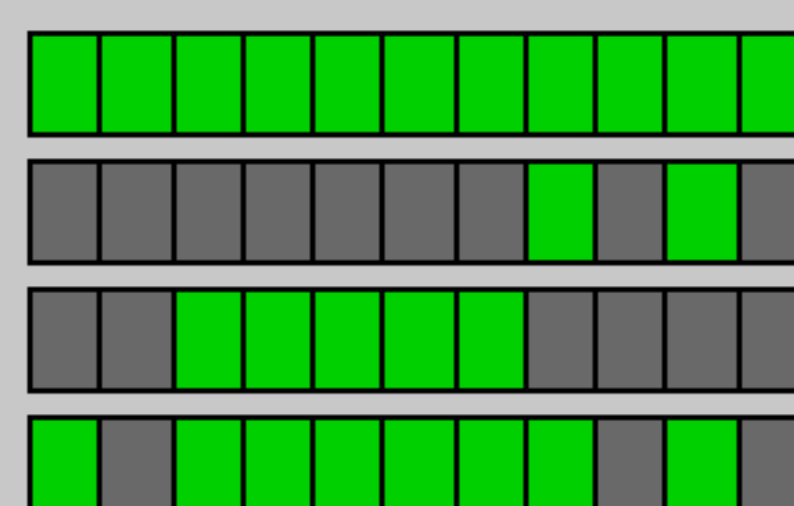
[L1:ASC-ODC\_CHANNEL\_LATCH]



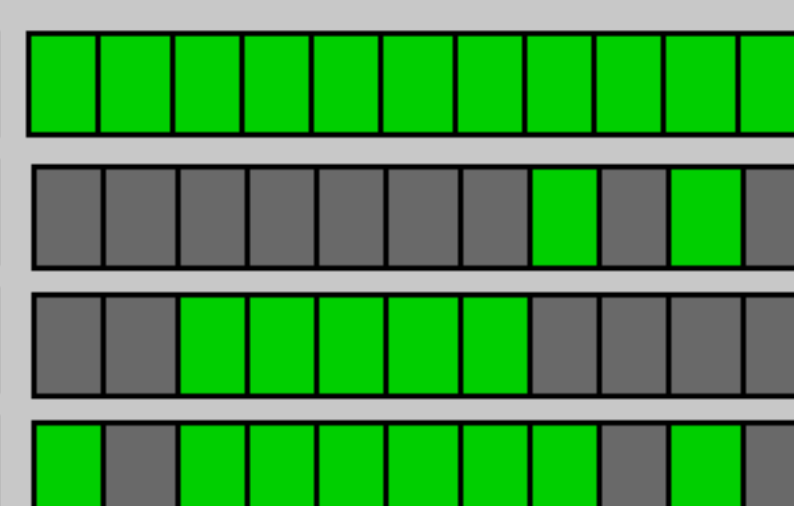
### DoF Control Signals

INP1	PIT:	0.000	<	500.000	
	YAW:	0.000	<	200.000	
INP2	PIT:	0.000	<	100.000	
	YAW:	0.000	<	10.000	
PRC1	PIT:	0.000	<	500.000	
	YAW:	0.000	<	600.000	
PRC2	PIT:	0.000	<	3000.000	
	YAW:	0.000	<	3000.000	
MICH	PIT:	0.000	<	2500.000	
	YAW:	0.000	<	500.000	
SRC1	PIT:	0.000	<	4000.000	
	YAW:	0.000	<	3500.000	
SRC2	PIT:	0.000	<	100.000	
	YAW:	0.000	<	50.000	
DHARD	PIT:	0.000	<	3000.000	
	YAW:	0.000	<	3000.000	
DSOFT	PIT:	0.000	<	400.000	
	YAW:	0.000	<	400.000	
CHARD	PIT:	0.000	<	5000.000	
	YAW:	0.000	<	5000.000	
CSOFT	PIT:	0.000	<	300.000	
	YAW:	0.000	<	100.000	

### PIT



### YAW



0x87ff87ff

0x2800280

0x7c007c

0x2fd02fd

BIT 18:

BIT 19:

BIT 20:

TRANS\_X\_A

PIT:

-1.906

<

0.200

YAW:

-2.339

<

0.200

BIT 14:

TRANS\_X\_B

PIT:

-156422.566

<

0.200

YAW:

-441649.164

<

0.400

BIT 15:

TRANS\_Y\_A

PIT:

-5.977

<

0.200

YAW:

-1.881

<

0.200

BIT 16:

TRANS\_Y\_B

PIT:

3145443.261

<

0.100

YAW:

-2150723.022

<

0.300

BIT 17:

### QPD Saturations

QREFL A RF9	BIT 2:	
QREFL A RF45	BIT 3:	
QREFL A DC	BIT 4:	
QREFL B RF9	BIT 5:	
QREFL B RF45	BIT 6:	
QREFL B DC	BIT 7:	
QAS A RF36	BIT 8:	
QAS A RF45	BIT 9:	
QAS A DC	BIT 10:	
QAS B RF36	BIT 11:	
QAS B RF45	BIT 12:	
QAS B DC	BIT 13:	