

## CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

**1:** Summarize and compare the specifications of the algorithms and of the correctness properties. Include at least these 4 attributes: specification sizes; ease of understanding; how closely are different aspects of the algorithms followed; and what properties are specified and in what way.

### Specification 1 (LamportMutex.tla):

- **Summary:** Process's clock value is initialized with 1 and it can make one request at a time. Process clock is updated upon receiving an event and acknowledgement is sent.

- **Constants:**

<b>N</b>	Total Process
<b>maxClock</b>	Max Clock of Process.

- **Initial state:**

<b>clock[p]</b>	Process p's clock set to 1 in the beginning
<b>req[p][q]</b>	Stores clock of Request from q to p, and is set to 0 at the beginning
<b>ack[p]</b>	Set of Process that send ack message for p's request.
<b>network[p][q]</b>	When process p sends a message to process q, they are stored in First In First Out Order
<b>crit</b>	Processes in Critical Section

## CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

- **Actions:**

<b>Request(p)</b> When process hasn't sent any request	<b>1.</b> req[p][p] is set to clock value of P.. <b>2.</b> Send a request message to all other processes and update ack with p.
<b>ReceiveRequest(p, q):</b> When p receives message from q	<b>1.</b> Set p's req to the clock of q's message received. <b>2.</b> Set p's clock to MAX(current_clock_of_p, clock_of_q's_message) <b>3.</b> Delete received message from the network
<b>Receive Ack(p, q):</b> If q sends an 'ack' to p	<b>1.</b> Update p's ack set with process q <b>2.</b> Delete acknowledge message from the network.
<b>Enter(p):</b> When p gets 'ack' from all other processes	<b>1.</b> If p's request has highest priority than any other process, it enters critical sections.
<b>Exit(p):</b> When p enters	<b>1.</b> Delete p from critical section and send a release message to other processes. <b>2.</b> Update p's req and ack to None.
<b>ReceiveRelease(p, q)</b> When q sends release to p	<b>1.</b> Remove q's message from p.

**State Constraint:**  $\text{ClockConstraint} == \forall p \in \text{Proc} : \text{clock}[p] \leq \text{maxClock}$

**Invariants:**

1. **Mutex:**  $== \forall p, q \in \text{crit} : p = q$  (Only one process in Critical region)
2. **BoundedNetwork**  $== \forall p, q \in \text{Proc} : \text{Len}(\text{network}[p][q]) \leq 3$  (Maximum number of messages = 3 in given network)

**Properties:**

1. **Liveness**  $== \forall p \in \text{Proc} : \langle \rangle (p \in \text{crit})$

**Specification size:** 123 lines

**Ease of understanding:**

1. Easier than other specifications
2. Able to relate and understand, if we go through lecture notes and slides.

## CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

---

### Specification 2 (Spec2.tla):

- **Summary:** Process's clock value is initialized with  $0 \dots \text{NumNats}$  and it can make one request at a time. Process clock is updated upon receiving an event and acknowledgement is sent only if p has not sent a message to q with timestamp greater than the received message from q.

- **Constants:**

<b>NumProcs</b>	Total Process
<b>NumNats</b>	Max Clock of Process.
<b>_ \II _</b>	$p < q$

- **Initial state:**

<b>state[p]</b>	Process p's state set to idle in the beginning
<b>msgQ[p][q]</b>	Stores messages from p to q, and is set to empty at the beginning
<b>reqSet[p]</b>	Set of req messages to/from process p is set to empty at the beginning
<b>clock[p]</b>	Process p clock's range is from 0 to NumNats In the beginning
<b>lastTSent[p][q]</b>	P to Q message's latest clock value is set to 0 in the beginning.
<b>lastTReceived[p][q]</b>	Q to P message's latest clock value is set to 0 in the beginning.

### CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

- **Actions:**

<b>Request(p)</b> P->'idle'	<ol style="list-style-type: none"><li>1. Change P from 'idle' -&gt;'waiting'</li><li>2. Change P's clock to any greater value than its current ,in the range of 0 to NumNats</li><li>3. Send 'acquire' to other process and it to its own request set.</li><li>4. Change lastTSent to timestamp of the latest message sent to other processes.</li></ol>
<b>Acquire(p)</b> P->'waiting' and sent 'acquire' to other processes.	<ol style="list-style-type: none"><li>1. If P's req has lower timestamp than other process request,and if P has received ack from other process,then set P-&gt;'owner'.</li><li>2. Delete P's req from request set</li></ol>
<b>Release(p)</b> P->'owner' and wants to come out of critical section	<ol style="list-style-type: none"><li>1. Change P-&gt;'idle' from P-'owner' and send release to other process</li><li>2. Change lastTSent to timestamp of the latest message sent to other processes.</li></ol>
<b>RcvMsg(p, q)</b> P receives Q's message	<ol style="list-style-type: none"><li>1. Set p's clock to <math>\text{MAX}(\text{current\_clock\_of\_p}, \text{clock\_Of\_q's\_message})</math></li><li>2. Change lastTRcvd to timestamp of the latest message received to other processes.</li><li>3. If an acquire message is received,send an acknowledgement message to q only if the q's message's clock is greater than the clock of p's latest message to q, and add it to reqSet</li><li>4. If a release message is received, remove it from reqSet</li></ol>
<b>Tick(p)</b>	<ol style="list-style-type: none"><li>1.Set P's clock to next greater value in the range</li></ol>

**State Constraint:**  $\text{ClockConstraint} == \forall p \in \text{Proc} : \text{clock}[p] < \text{NumNats}$

**Invariants:**  $\text{Mutex} == \forall s, t \in \text{Proc} : \text{state}[s] = \text{"crit"} \wedge \text{state}[t] = \text{"crit"} \Rightarrow s = t$

## CSE 535 Assignment 3

Swethasri Kavuri

SBU ID: 111465291

---

**Properties:**  $\text{Liveness} == \bigwedge p \in \text{Proc} : \bigwedge \text{WF\_vars}(\text{Acquire}(p))$   
 $\bigwedge \bigwedge q \in \text{Proc} \setminus \{p\} : \text{WF\_vars}(\text{RcvMsg}(p,q))$

**Specification size:** 100 lines of code

**Ease of understanding:**

- Difficult to understand compared to Spec 1.
- Able to relate with Lamport's paper.

### Specification 3 (Mutex.tla):

- **Summary :** Process's clock value is initialized with 1 and it can make one request at a time. Process clock is updated upon receiving an event and acknowledgement is sent. Site and communicator are present in this spec. Site requests access to critical section and communicator handles received messages
- **Constants:**

<b>N</b>	Total Process
<b>maxClock</b>	Max Clock of Process.

- **Initial state:**

<b>network[p][q]</b>	Messages sent from Site p to Site q
<b>reqQ[p]</b>	Set of req messages to Site p, is set to empty at the beginning. Messages are sorted as per their logical clock
<b>clock[p]</b>	Process p clock's range is set to 1
<b>acks[p]</b>	All the ack messages to Site p.
<b>pc[p]</b>	P's state is 'start' and 'comm' for sites and communicators respectively

### CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

Actions: (P-> represents P's state)

start	Change P->'start' to P->'ncrit'
ncrit	Change P->'ncrit' to P->'try'
try	If P->'try', then send request message across all the sites in network, and then Change P->'try' to P->'enter'
enter	If P->'enter', then it checks the priority based on time stamp, and also checks if it has received ack from all other sites. If So, then Change P->'enter' to P->'crit'
crit	Change P->'crit' to P->'exit'
exit	If P->'exit' then send release message to other sites and then Change P->'exit' to P->'start'

- **State Constraint:**  $\text{ClockConstraint} == \forall s \in \text{Sites} : \text{clock}[s] \leq \text{maxClock}$
- **Invariants:**  $\text{Mutex} == \forall s, t \in \text{Sites} : \text{pc}[s] = \text{"crit"} \wedge \text{pc}[t] = \text{"crit"} \Rightarrow s = t$   
 $\text{TypeInvariant} ==$   
 $\wedge \text{network} \in [\text{Sites} \rightarrow [\text{Sites} \rightarrow \text{Seq}(\text{Message})]]$   
 $\wedge \text{clock} \in [\text{Sites} \rightarrow \text{Nat}]$   
 $\wedge \text{reqQ} \in [\text{Sites} \rightarrow \text{Seq}([\text{site} : \text{Sites}, \text{clk} : \text{Nat}])]$   
 $\wedge \forall s \in \text{Sites} : \forall i \in 1 \dots \text{Len}(\text{reqQ}[s]) - 1 : \text{beats}(\text{reqQ}[s][i], \text{reqQ}[s][i+1])$   
 $\wedge \text{acks} \in [\text{Sites} \rightarrow \text{SUBSET Sites}]$   
 $\text{Invariant} ==$   
 $\wedge (* \text{ each queue holds at most one request per site } *)$   
 $\forall s \in \text{Sites} : \forall i \in 1 \dots \text{Len}(\text{reqQ}[s]) :$   
 $\quad \forall j \in i+1 \dots \text{Len}(\text{reqQ}[s]) : \text{reqQ}[s][j].\text{site} \neq \text{reqQ}[s][i].\text{site}$   
 $\wedge (* \text{ requests stay in queue until "free" message received } *)$   
 $\forall s, t \in \text{Sites} :$   
 $\quad (\exists i \in 1 \dots \text{Len}(\text{network}[s][t]) : \text{network}[s][t][i].\text{kind} = \text{"free"})$   
 $\Rightarrow \exists j \in 1 \dots \text{Len}(\text{reqQ}[t]) : \text{reqQ}[t][j].\text{site} = s$   
 $\wedge (* \text{ site is in critical section only if at the head of every request queue } *)$   
 $\forall s \in \text{Sites} : \text{pc}[s] = \text{"crit"} \Rightarrow \forall t \in \text{Sites} : \text{Head}(\text{reqQ}[t]).\text{site} = s$

## CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

---

**Properties:** Liveness ==  $\backslash A s \backslash in Sites : pc[s] = "enter" \leadsto pc[s] = "crit"$

**Specification size:** 150 lines

**Ease of understanding:** Difficult to understand

2. Summarize and compare the efficiency of running TLC, separately for checking safety and liveness. Include at least these attributes: ease of setting up and running; and for different process numbers and maximum clock values, the number of states searched and the time taken to check (in a similar form as Page 278 of Lamport's PODC 2000 Tutorial slides).

### Specification LamportMutex.tla

Procs	MaxClock	States found	Distinct States	Time(MM:SS) safety	Time(MM:SS) liveness
2	5	1326	685	00:13	00:14
2	6	2001	1043	00:12	00:13
2	7	2812	1475	00:10	00:12
2	8	3759	1981	00:13	00:15
2	9	4842	2561	00:14	00:18
3	2	2203	511	00:15	00:16
3	3	41533	10209	00:11	00:13
3	4	276114	70472	00:12	00:23

**Table Specification Lamport Mutex.tla safety and liveness statistics**

### CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

#### Specification 2

Procs	MaxClock	States found	Distinct States	Time(MM:SS) <b>safety</b>	Time(MM:SS) <b>liveness</b>
2	5	35833	5733	00:15	00:18
2	6	119876	18009	00:13	00:15
2	7	347524	49365	00:16	00:21
2	8	903671	122045	00:19	00:31
2	9	2157020	278208	00:26	01:15
3	2	6828	887	00:18	00:19
3	3	301566	35706	00:16	00:20
3	4	5606761	622560	00:46	04:39

**Table Specification 2 safety and liveness statistics**

#### Specification Mutex.tla

Procs	MaxClock	States found	Distinct States	Time(MM:SS) <b>safety)</b>	Time(MM:SS) <b>liveness</b>
2	5	23888	7711	00:09	00:11
2	6	43121	13863	00:08	00:17
2	7	67446	21625	00:09	00:19
2	8	95988	30727	00:16	00:18
2	9	128362	41025	00:13	00:23
3	2	21646	3990	00:15	00:22
3	3	578165	99411	00:14	01:15



## CSE 535 Assignment 3

Swethasri Kavuri  
SBU ID: 111465291

---

3	4	7763351	1309654	01:34	43:08
---	---	---------	---------	-------	-------

**Table Specification Mutex.tla safety and liveness statistics**

### Summary:

1. All the specs requires Number of process and clock value as inputs
2. For Spec 2 we need to specify order of process.
3. Liveness takes longer than safety

### References:

- <https://lamport.azurewebsites.net/tla/tla.html>
- Discussed Spec 2 implementation with Saish Sali
- <https://amturing.acm.org/p558-lamport.pdf>