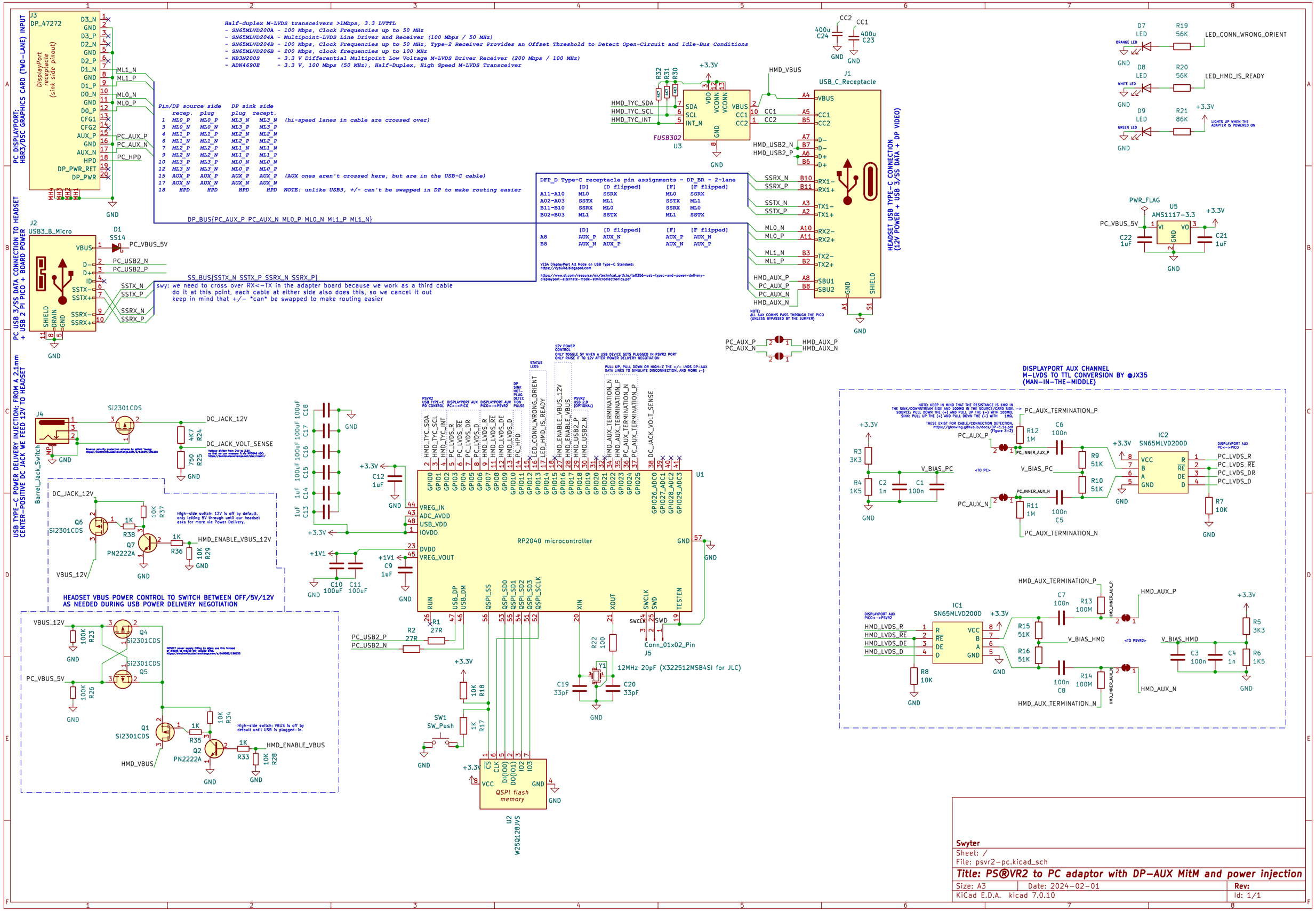
PC DISPLAYPORT: HBR3/DSC GRAPHICS CARD (TWO-LANE) INPUT
J3
DP_47272
DisplayPort receptacle (sink side pinout)
Half-duplex M-LVDS transceivers >1Mbps, 3.3 LVTTL
- SN65MLVD200A - 100 Mbps, Clock Frequencies up to 50 MHz
- SN65MLVD204A - Multipoint-LVDS Line Driver and Receiver (100 Mbps / 50 MHz)
- SN65MLVD204B - 100 Mbps, Clock Frequencies up to 50 MHz, Type-2 Receiver Provides an Offset Threshold to Detect Open-Circuit and Idle-Bus Conditions
- SN65MLVD206B - 200 Mbps, clock frequencies up to 100 MHz
- NB3N200S - 3.3 V Differential Multipoint Low Voltage M-LVDS Driver Receiver (200 Mbps / 100 MHz)
- ADN4690E - 3.3 V, 100 Mbps (50 MHz), Half-Duplex, High Speed M-LVDS Transceiver
Pin/DP source side recep. plug — DP sink side plug recept.
1 ML0_P ML0_P ML3_N ML3_N (hi-speed lanes in cable are crossed over)
3 ML0_N ML0_N ML3_P ML3_P
4 ML1_P ML1_P ML2_N ML2_N
6 ML1_N ML1_N ML2_P ML2_P
7 ML2_P ML2_P ML1_N ML1_N
9 ML2_N ML2_N ML1_P ML1_P
10 ML3_P ML3_P ML0_N ML0_N
12 ML3_N ML3_N ML0_P ML0_P
15 AUX_P AUX_P AUX_P AUX_P (AUX ones aren't crossed here, but are in the USB-C cable)
17 AUX_N AUX_N AUX_N AUX_N
18 HPD HPD HPD HPD NOTE: unlike USB3, +/- can't be swapped in DP to make routing easier
DP_BUS{PC_AUX_P PC_AUX_N ML0_P ML0_N ML1_P ML1_N}
PC USB 3/SS DATA CONNECTION TO HEADSET + USB 2 PI PICO + BOARD POWER
J2
USB3_B_Micro
D1
SS14
PC_VBUS_5V
PC_USB2_N
PC_USB2_P
SS_BUS{SSTX_N SSTX_P SSRX_N SSRX_P}
swy: we need to cross over RX<-TX in the adapter board because we work as a third cable do it at this point, each cable at either side also does this, so we cancel it out keep in mind that +/- *can* be swapped to make routing easier
DFP_D Type-C receptacle pin assignments - DP_BR - 2-lane
[D] [D flipped] [F] [F flipped]
A11-A10 ML0 SSRX ML0 SSRX
A02-A03 SSTX ML1 SSTX ML1
B11-B10 SSRX ML0 SSRX ML0
B02-B03 ML1 SSTX ML1 SSTX
[D] [D flipped] [F] [F flipped]
A8 AUX_P AUX_N AUX_P AUX_N
B8 AUX_N AUX_P AUX_N AUX_P
VESA DisplayPort Alt Mode on USB Type-C Standard: https://cyborlid.blogspot.com
https://www.st.com/resource/en/technical_article/ta0356-usb-typec-and-power-delivery-displayport-alternate-mode-stmicroelectronics.pdf
HEADSET USB TYPE-C CONNECTION (12V POWER + USB 3/SS DATA + DP VIDEO)
J1
USB_C_Receptacle
HMD_VBUS
HMD_TYC_SDA
HMD_TYC_SCL
HMD_TYC_INT
FUSB302
U3
HMD_USB2_N
HMD_USB2_P
SSRX_N
SSRX_P
SSTX_N
SSTX_P
ML0_N
ML0_P
ML1_N
ML1_P
HMD_AUX_P
PC_AUX_P
PC_AUX_N
HMD_AUX_N
NOTE: ALL AUX COMMS PASS THROUGH THE PICO (UNLESS BYPASSED BY THE JUMPER)
CC2 400u C24
CC1 400u C23
D7 LED ORANGE LED R19 56K LED_CONN_WRONG_ORIENT
D8 LED WHITE LED R20 56K LED_HMD_IS_READY
D9 LED GREEN LED R21 86K +3.3V LIGHTS UP WHEN THE ADAPTER IS POWERED ON
PWR_FLAG
U5 AMS1117-3.3
PC_VBUS_5V
C22 1uF
C21 1uF
USB TYPE-C POWER DELIVERY INJECTION: FROM A 2.1mm CENTER-POSITIVE DC JACK WE FEED 12V TO HEADSET
J4
BarrelJack_Switch
SI2301CDS
DC_JACK_12V
R24 4K7
R25 750
DC_JACK_VOLT_SENSE
Q6 Si2301CDS
Q7 PN2222A
R37 10K
R38 1K
R36 1K
R29 10K
HMD_ENABLE_VBUS_12V
VBUS_12V
High-side switch; 12V is off by default, only letting 5V through until our headset asks for more via Power Delivery.
HEADSET VBUS POWER CONTROL TO SWITCH BETWEEN OFF/5V/12V AS NEEDED DURING USB POWER DELIVERY NEGOTIATION
Q4 Si2301CDS
Q5 Si2301CDS
R23 100K
R26 100K
PC_VBUS_5V
Q1 Si2301CDS
Q2 PN2222A
R35 1K
R33 1K
R34 10K
R28 10K
HMD_ENABLE_VBUS
HMD_VBUS
High-side switch: VBUS is off by default until USB is plugged-in.
RP2040 microcontroller
U1
R1 27R
R2 27R
PC_USB2_P
PC_USB2_N
Conn_01x02_Pin
J5
R22 100
Y1
12MHz 20pF (X322512MSB4SI for JLC)
C19 33pF
C20 33pF
SW1 SW_Push
R18 10K
R17 1K
QSPI flash memory
U2
W25Q128JVS
DISPLAYPORT AUX CHANNEL M-LVDS TO TTL CONVERSION BY @JX35 (MAN-IN-THE-MIDDLE)
NOTE: KEEP IN MIND THAT THE RESISTANCE IS 1MΩ IN THE SINK/DOWNSTREAM SIDE AND 100MΩ IN THE SOURCE/CARD SIDE. -> SOURCE: PULL DOWN THE (+) AND PULL UP THE (-) WITH 100MΩ. SINK: PULL UP THE (+) AND PULL DOWN THE (-) WITH 1MΩ.
THESE EXIST FOR CABLE/CONNECTION DETECTION. https://glenwing.github.io/docs/DP-1.1a.pdf (Figure 3-2)
PC_AUX_TERMINATION_P
PC_AUX_TERMINATION_N
R12 1M
C6 100n
R11 1M
C5 100n
R3 3K3
R4 1K5
C2 1n
C1 100n
V_BIAS_PC
IC2 SN65MLVD200D
R9 51K
R10 51K
R7 10K
IC1 SN65MLVD200D
HMD_AUX_TERMINATION_P
HMD_AUX_TERMINATION_N
HMD_AUX_P
HMD_AUX_N
C7 100n
R13 100M
R14 100M
C8 100n
R15 51K
R16 51K
V_BIAS_HMD
R8 10K
R5 3K3
R6 1K5
C3 100n
C4 1n
Swyter
Sheet: /
File: psvr2-pc.kicad_sch
Title: PS®VR2 to PC adaptor with DP-AUX MitM and power injection
Size: A3
Date: 2024-02-01
Rev:
KiCad E.D.A. kicad 7.0.10
Id: 1/1