

LAB 07

CONDITIONAL PROCESSING



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Version: 1.0

Date: 27th Oct 2021

Lab Session 07: CONDITIONAL PROCESSING

Objectives:

- Boolean Instructions
- Set Operations
- CMP Instruction
- Conditional Jumps

Boolean Instructions

• AND

Boolean AND operation between a source operand and destination operand.

Syntax: *AND reg, reg*
 AND reg, mem
 AND reg, imm
 AND mem, reg
 AND mem, imm

• OR

Boolean OR operation between a source operand and destination operand.

Syntax: *OR reg, reg*
 OR reg, mem
 OR reg, imm
 OR mem, reg
 OR mem, imm

• XOR

Boolean XOR operation between a source operand and destination operand.



Syntax: *XOR reg, reg*
 XOR reg, mem
 XOR reg, imm
 XOR mem, reg
 XOR mem, imm

- **NOT**

Boolean NOT operation on a destination operand.

Syntax: *NOT reg*
 NOT mem

- **TEST**

Similar to AND operation, except that instead of affecting any operands it sets the FLAGS appropriately.

Syntax: *TEST reg, reg*
 TEST reg, mem
 TEST reg, imm
 TEST mem, reg
 TEST mem, imm

Example 01:

```
Include Irvine32.inc
.code main
proc
    mov     al, 10101110b      ; Clear only bit 3
    and     al, 11110110b      ; AL = 10100110

    mov     al, 11100011b      ; set bit 2
```



```

    or      al, 00000100b      ; AL = 11100111

    mov     al, 10110101b      ; 5 bits means odd parity
    xor     al, 0               ; PF = 0 (PO)

    mov     al, 10100101b      ; 4 bits means even parity
    xor     al, 0               ; PF = 1 (PE)

    mov     al, 11110000b
    not     al                  ; AL = 00001111b

    mov     al, 00100101b
    test    al, 00001001b      ; ZF = 0

    mov     al, 00100101b
    test    al, 00001000b      ; ZF = 1
    call    DumpRegs

exit
main ENDP
END main

```

Set Operations (using Boolean instructions)

- **Set Complement**

The complement of a set can be achieved through NOT instruction.

- **Set Intersection**

The intersection of two sets can be achieved through AND instruction.

- **Set Union**

The union of two sets can be achieved through OR instruction.

Example 02:

```

Include Irvine32.inc
.data
    A DWORD 1000000000000000000000000000111b
    B DWORD 10000001010100000000011101100011b

```



```

        msg1 BYTE "A intersection B is: ", 0      msg2
        BYTE "A union B is: ", 0
        msg3 BYTE "Complement of A is: ", 0
.code main
proc
    mov     eax,A
    and     eax, B          ; A intersection B
    mov     edx, OFFSET msg1
    call    WriteString
    mov     ebx, TYPE DWORD
    call    WriteBinB
    call    Crlf
    mov     eax, A
    or      eax, B          ; A union B
    mov     edx, OFFSET msg2
    call    WriteString
    mov     ebx, TYPE DWORD
    call    WriteBinB
    call    Crlf
    mov     eax, A
    not     eax             ; A complement
    mov     edx, OFFSET msg3
    call    WriteString
    mov     ebx, TYPE DWORD
    call    WriteBinB
call     DumpRegs
exit
main ENDP END
main

```

CMP instruction

CMP (compare) instruction performs an implied subtraction of a source operand from a destination operand for comparison.
For unsigned operands:

- Destination < source ZF = 0 CF = 1
- Destination > source ZF = 0 CF = 0



- Destination = source ZF = 1 CF = 0

For signed operands:

- Destination < source SF != OF
- Destination > source SF = OF
- Destination = source ZF = 1

Example 03:

Include Irvine32.inc

.code

main proc

 mov ax, 5

 cmp ax, 10 ; ZF = 0 and CF = 1

 mov ax, 1000

 cmp ax, 1000 ; ZF = 1 and CF = 0

 mov si, 106

 cmp si, 0 ; ZF = 0 and CF = 0

call DumpRegs

exit

main ENDP

END main



Conditional Jumps

- Jumps based on Flag values

Mnemonic	Description	Flags / Registers
JZ	Jump if zero	ZF = 1
JNZ	Jump if not zero	ZF = 0
JC	Jump if carry	CF = 1
JNC	Jump if not carry	CF = 0
JO	Jump if overflow	OF = 1
JNO	Jump if not overflow	OF = 0
JS	Jump if signed	SF = 1
JNS	Jump if not signed	SF = 0
JP	Jump if parity (even)	PF = 1
JNP	Jump if not parity (odd)	PF = 0

- Jumps based on Equality

Mnemonic	Description
JE	Jump if equal (<i>leftOp</i> = <i>rightOp</i>)
JNE	Jump if not equal (<i>leftOp</i> ≠ <i>rightOp</i>)
JCXZ	Jump if CX = 0
JECXZ	Jump if ECX = 0

- Jumps based on unsigned comparisons

Mnemonic	Description
JA	Jump if above (if $leftOp > rightOp$)
JNBE	Jump if not below or equal (same as JA)
JAЕ	Jump if above or equal (if $leftOp \geq rightOp$)
JNB	Jump if not below (same as JAЕ)
JB	Jump if below (if $leftOp < rightOp$)
JNAЕ	Jump if not above or equal (same as JB)
JBE	Jump if below or equal (if $leftOp \leq rightOp$)
JNA	Jump if not above (same as JBE)

- Jumps based on signed comparisons

Mnemonic	Description
JG	Jump if greater (if $leftOp > rightOp$)
JNLE	Jump if not less than or equal (same as JG)
JGE	Jump if greater than or equal (if $leftOp \geq rightOp$)
JNL	Jump if not less (same as JGE)
JL	Jump if less (if $leftOp < rightOp$)
JNGE	Jump if not greater than or equal (same as JL)
JLE	Jump if less than or equal (if $leftOp \leq rightOp$)
JNG	Jump if not greater (same as JLE)

Example 04:

```

Include Irvine32.inc
.data
    var1 DWORD 250
    var2 DWORD 125
    larger DWORD ?
.code main
proc
    mov     eax, var1
    mov     larger, eax
    mov     ebx, var2

```



```
        cmp    eax, ebx
        jae    L1
        mov    larger, ebx
L1: call    DumpRegs exit
main ENDP
END main
```

Example 05:

```
Include Irvine32.inc
.data
    var1    DWORD 50
    var2    DWORD 25
    var3    DWORD 103
    msg     BYTE "The smallest integer is: ", 0
.code main
proc mov
    eax, var1
    cmp eax, var2 jbe L1
        mov    eax, var2
    L1:

        cmp    eax, var3
jbe L2 mov eax, var3

    L2:
        mov    edx, OFFSET msg
```

```
        call    WriteString
        call    WriteDec
    call    DumpRegs
    exit
main ENDP
END main
```

Example 06:

```
Include Irvine32.inc
.data
char BYTE ?
.code main
proc L1:

    mov  eax, 10    call    Delay    ; create 10ms delay
    call    ReadKey    ; reads a key input
    jz     L1        ; repeat if no key is pressed
    mov  char, al    call    DumpRegs ; saves the character
    exit
main ENDP
END main
```

Lab Task(s):

1. Translate the following pseudo-code to Assembly Language:



```

var = 5
if ( var<ecx ) AND      (ecx>=edx)
then      x = 0  else      x = 1

```

2. Use cmp and jumps to find the first non-zero value in the given array:

```
intArr      SWORD      0, 0, 0, 0, 1, 20, 35, -12, 66, 4, 0
```

3. Write a program that takes four input integers from the user. Then compare and display a message whether these integers are equal or not.
4. Write a program for sequential search. Take an input from the user and find if it occurs in the following array:

```
arr  WORD      10, 4, 7, 14, 299, 156, 3, 19, 29, 300, 20
```

5. Translate the following pseudo-code to Assembly Language:

```

Swap_Count = 0
for all elements of list
    if list[i] > list[i+1]
swap(list[i], list[i+1])      Swap_Count =
Swap_Count + 1
    end if
end for      Print
Swap_Count

```

Task 1:

```
1 include irvine32.inc
2 .model small
3 .stack 100h
4 .data
5 var dword 5
6 x byte 5
7 .code
8 main proc
9 ;mov ecx,2
10 ;mov edx,3
11 cmp var,ecx
12 JB L1
13 L1:
14 cmp ecx,edx
15 JAE L2
16 mov x,0
17 mov eax,0
18 mov al,x
19 call WriteDec
20 invoke exitprocess,0
21 L2:
22 mov x,1
23 mov eax,0
24 mov al,x
25 call WriteDec
26 exit
27 main endp
28 end main
29
```

Microsoft Visual Studio Debug Console

```
1
C:\Users\Faheem\source\repos\Lab 1\Debug\Lab 1.exe (process 25608) exited with code 0.
Press any key to close this window . . .
```



Task 2:

```
1  include irvine32.inc
2  .model small
3  .stack 100h
4  .data
5  intArr sword 0,0,0,0,1,20,35,-12,66,4,0
6  msg1 byte "The first non-zero value is: ",0
7  msg2 byte "The non-zero value is at index: ",0
8  index_count byte -1
9  .code
10 main proc
11 mov esi,0
12 L1:
13 movzx eax,intArr[esi]
14 add esi,2
15 inc index_count
16 cmp eax,0
17 JE L1
18 mov edx,offset msg1
19 call WriteString
20 call WriteInt
21 call Crlf
22 mov edx,offset msg2
23 call WriteString
24 movzx eax,index_count
25 call WriteInt
26 exit
27 main endp
28 end main
29
```

Microsoft Visual Studio Debug Console

The first non-zero value is: +1
The non-zero value is at index: +4
C:\Users\Faheem\source\repos\Lab 1\Debug\Lab 1.exe (process 2520)
Press any key to close this window . . .

Task 3:

```
1  include irvine32.inc
2  .model small
3  .stack 100h
4  .data
5  var1 dword ?
6  var2 dword ?
7  var3 dword ?
8  var4 dword ?
9  msg1 byte "Enter the integer: ",0
10 msg2 byte "The inserted integers entered are equal! ",0
11 msg3 byte "The inserted integers entered are not equal! ",0
12 .code
13 main proc
14 mov edx,offset msg1
15 call WriteString
16 call ReadInt
17 mov var1,eax
18 mov edx,offset msg1
19 call WriteString
20 call ReadInt
21 mov var2,eax
22 mov edx,offset msg1
23 call WriteString
24 call ReadInt
25 mov var3,eax
26 mov edx,offset msg1
27 call WriteString
28 call ReadInt
29 mov var4,eax
30 mov ebx,var1
31 mov ebx,var2
32 mov ebx,var3
33 mov ebx,var4
34 mov ecx,4
35 L1:
36 sub ebx,var1
37 L2:
38 cmp ebx,0
39 JE L3
40 mov edx,offset msg3
41 call WriteString
42 exit
43 L3:
44 mov edx,offset msg2
45 call WriteString
46 exit
47 main endp
```

Microsoft Visual Studio Debug Console

Enter the integer: 5
Enter the integer: 5
Enter the integer: 5
Enter the integer: 5
The inserted integers entered are equal!
C:\Users\Faheem\source\repos\Lab 1\Debug\Lab 1.exe (process 10112) exited with code 0.
Press any key to close this window . . .

Task 4:

```
1  include irvine32.inc
2  .model small
3  .stack 100h
4  .data
5  arr word 10,4,7,14,299,156,3,19,29,300,20
6  var word ?
7  msg1 byte "Enter the integer for Sequential Search: ",0
8  msg2 byte "The Searched integer was found!",0
9  msg3 byte "The Searched integer was not found!",0
10 .code
11 main proc
12 mov esi,offset arr
13 mov edx,offset msg1
14 call WriteString
15 mov eax,0
16 call ReadInt
17 mov var,ax
18 mov ecx,lengthof arr
19 L1:
20 mov eax,0
21 mov ax,[esi]
22 add esi,2
23 cmp ax,var
24 JE L2
25 loop L1
26 mov edx,offset msg3
27 call WriteString
28 exit
29 L2:
30 mov edx,offset msg2
31 call WriteString
32 call WriteInt
33 exit
34 main endp
35 end main
```

Microsoft Visual Studio Debug Console

Enter the integer for Sequential Search: 19
The Searched integer was found!+19
C:\Users\Faheem\source\repos\Lab 1\Debug\Lab 1.exe (process 22620) exited with code 0.
Press any key to close this window . . .

Task 5:

```
1  include irvine32.inc
2  .model small
3  .stack 100h
4  .data
5  arr dword 5,4,3,2,1
6  swap_count dword 0
7  msg1 byte "The Swap Count of the whole array is: ",0
8  .code
9  main proc
10 mov esi,0
11 mov edi,4
12 mov ecx,(lengthof arr-1)
13 L1:
14 mov eax,arr[esi]
15 mov ebx,arr[edi]
16 cmp eax,ebx
17 JA L2
18 add esi,4
19 add edi,4
20 loop L1
21 L2:
22 inc swap_count
23 mov arr[esi],ebx
24 mov arr[edi],eax
25 dec ecx
26 cmp ecx,0
27 JECXZ L3
28 add esi,4
29 add edi,4
30 JMP L1
31 L3:
32 mov edx,offset msg1
33 call WriteString
34 mov eax,swap_count
35 call WriteInt
36 call Crlf
37 mov ecx,lengthof arr
38 mov esi,0
39 L4:
40 mov eax,arr[esi]
41 call WriteInt
42 add esi,4
43 loop L4
44 exit
45 main endp
46 end main
```

Microsoft Visual Studio Debug Console

```
The Swap Count of the whole array is: +4
+4+3+2+1+5
C:\Users\Faheem\source\repos\Lab 1\Debug\Lab 1.exe (process 29632) exited with code 0.
Press any key to close this window . . .
```

