

Components and Their Details

- 1. **8254 Programmable Interval Timer (U2)**
 - **Pins:**
 - `CLK0, CLK1, CLK2`: Clock inputs for the three timers.
 - `OUT0, OUT1, OUT2`: Outputs of the three timers.
 - `G0, G1, G2`: Gate inputs for enabling/disabling timers.
 - `D0–D7`: Data bus for communication with the microprocessor.
 - `CS`, `RD`, `WR`, `A0`, `A1`: Control signals for accessing internal registers.
 - **Purpose:**
- Used to generate precise time delays and pulse widths for controlling the speed of the motor.
 - Timer outputs (OUT0, OUT1) are used for motor control signals.

- 2. **Latch IC (U1 74LS373)**
 - **Purpose:**
 - Stores the address inputs (`A2–A15`) from the microprocessor.
 - Decodes addresses to enable 8254's operation.
 - **Connections:**
 - Receives address lines from the CPU.
 - Sends address information to 8254 via `A0` and `A1`.
- 3. **Flip-Flop (U3A 74ALS112)**
 - **Purpose:**
 - Handles the direction control of the motor.
 - `Q` and `Q'` outputs control which H-bridge transistors are activated.
 - **Connections:**
 - Triggered by the clock signals generated by the 8254.
- 4. **Open-Collector Inverters (U4A-U4D 7406)**
 - **Purpose:**
 - Buffer and amplify control signals from the 8254.
 - Drive the base of the transistors in the H-bridge.
 - **Connections:**
 - Inputs receive signals from the flip-flop and timer outputs.
 - Outputs are connected to the H-bridge transistors.
- 5. **H-Bridge (Transistors Q1, Q2, Q3, Q4)**
 - **Purpose:**

- Drives the DC motor.
- Controls both the speed (via PWM signals) and direction (via flip-flop outputs).
 - **Connections:**
 - The bases of the transistors are driven by the inverters (U4A–U4D).
 - The motor is connected between the diagonal pairs of transistors.
- 6. **DC Motor**
 - **Purpose:**
 - Converts electrical energy into mechanical motion.
 - **Connections:**
 - Connected to the H-bridge output.
- 7. **Clock Signal**
 - **Purpose:**
 - Provides a consistent frequency (8 MHz) for 8254's internal operations.

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### **How the Circuit Works**
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#### **1. Speed Control**
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- **PWM Generation:**
- The 8254 is configured to operate in a mode (e.g., Mode 2 or Mode 3) that generates Pulse Width Modulated (PWM) signals at its `OUT0` or `OUT1` outputs.
- The duty cycle of the PWM determines the average voltage applied to the motor, which controls its speed.

- The microprocessor programs the 8254 with appropriate values to adjust the timer's reload value, thus changing the PWM frequency or duty cycle.
- **Connection:**
- `OUT0` and `OUT1` signals from the 8254 are routed through the inverters (U4A, U4B) and fed into the H-bridge transistors.
- The varying duty cycle controls the motor speed proportionally.

2. Direction Control

- **Flip-Flop Operation:**
- The flip-flop (U3A) receives control signals from the microprocessor via the 8254 and toggles its outputs (`Q` and `Q'`) based on the clock input.
- The outputs of the flip-flop determine which diagonal pair of transistors in the H-bridge is activated.
- This setup reverses the polarity of the voltage applied to the motor, thus changing its direction.
- **Connection:**
- `Q` and `Q'` outputs of the flip-flop are fed into the inverters (U4C, U4D), which drive the H-bridge transistors.

Sequence of Operations

1. **Initialization**:

- The microprocessor configures the 8254 via its data and control signals (`CS`, `RD`, `WR`, `A0`, `A1`).
 - PWM parameters (frequency and duty cycle) are loaded into the timer registers.

2. **Speed Adjustment**:

- The 8254 generates a PWM signal at `OUT0` or `OUT1`.
- The duty cycle of this PWM signal determines the speed of the motor.

3. **Direction Control**:

- The flip-flop (U3A) toggles its outputs based on the control signal from the microprocessor.
- Depending on the state of `Q` and `Q'`, the H-bridge changes the direction of the current through the motor, controlling its rotation direction.

4. **Motor Operation**:

- The H-bridge transistors apply the required voltage and polarity to the motor terminals.
 - The motor operates at the desired speed and direction.