Chardraschar, m@svuca.edu OCH40 Bock; Data & Communication, With Ed. William Stallings Digital signal has defined levels (F3.1) Smrak V(+) = A sin (2Tf+ + p) A: complitude, max value of amplitude the v(+)  $v(t) = Asin \left(2\pi t 2\pi f t + \phi\right)$  $= A \sin \left(2\pi f \left(+ + \frac{1}{f}\right) + \phi\right)$ = Asin (2TJ(t+T)+0) = Asin (wt +  $\phi$ )  $w = 2\pi f$ 

Digital dranceiver: converts from I digital logic to another C440 logic (voltage, etr.), or act as a repeater. May 28th (Fig 3.13) 2015 Analog Synal. Analog data (1) singing thrownic in the concert
(2) He radio tower mod/demod. LPF:Hruncate the Sound > Mic > Mad > And enner... 20Hy Jx. 2khg Dumanky herable small antenra. J=3,3KHz Amphrade, same freg. same france. Merhod 1 change f), some amplitude, some phase

phase is changed, same amplitude, same freq Method 3 Asynchronos: example: internet. Syndronions example: TCP/IP Attenuation: Prob - low does not reach the Rx ar lower than

noise signal. Sol Use amplifier/equalizer (testing 123) frey got more
got less amplification. symplification

	White naise: multiple filgs combined.
	Stop at Categories of Noise.
	pmp 2 w/g
	clarrede poiznzc.
	quiz nest of next week
Zun M	the state of the s
	V data
Sen	le ( n-kbit ( F= f(data))  - generate checksum.
	_ generate checksum.
	Reciper.
	regeneral Ed compare with
	the sender's E
	D :
	Parity Check:
	Data Parity(even) counting number of 1s in the data
	, A
	odd - cdd; O other wise: 1
	0011 0.
	Can't detet if 2-4-6-5 bit are inverted.
	The state of the s
	Data K-bit



