Unit 11 Parallel instruction = ILP (using datapath)

2 ways to implement; last week is pipeline: 3 stages,

(Softwork) Sombaine multiple instructions, together to execute, parallel. - take a duratinger of multiple hardware.

Together to execute, parallel. - take a duratinger of multiple hardware.

The improve CPI

Will also encouter data hazard prob & branch prob.

M. I. ... Pata path; data hazard of forward.

reorder the instruction control hazard of predict first in Ird method,

Multiple issues (alterrative of pipeline) 2 ways: software (static), hardware (dyramic) Speculation only occurs or branch command Software way

Software way

Dynamic (basdware way)

Reservation Buffer: hold registers until data

The parder cy resolved & pass to commir.

Several sets

In struction A in struction buffer reservation buffer pass to

Sommit

Lingit buffer)

