Question 3

€ 5	Summary of que	•	N	Instr	Atlevel 1:			
			6	3				
			5	2	- Instr type A _ 25% - Instr type C _ 30%			
			4	4	- 44 ns - 20ns			
			3	2	- Instr type 6 _ 45%			
			2	5	<u>-</u>			
			1	-	_ 40 ns			
					#1			

#Instr: 500,000

a) Number of level-1 instr, for each level-6 instr: $3 \times 2 \times 4 \times 2 \times 3 = \boxed{144}$ instr,

b) Average instruction time at level $4:t_1=\frac{25\times44+45\times40+50\times20}{100}=35$ ns

c) Number of level-1 instr, for each level-4 instr: 4x2x3 = 24 instrs

Average instrexecution time for each level-4 instr: $t_4 = 24 \times 35 = 840$ (ns)

d) Average instressecution time for each level-6 instret to = $I_6 \times t_1 = 144 \times 35 = 5040$ ms

e) Program completion time: Tprog = tp x M = 500,000 x 5040 = 252 x 107 ns

f) Recalculation for new program:

Number of level 1 instr, for each level G instr: $3\times1\times4\times2\times2=96$ instr Average instress execution time for each level-G instr: $96\times35=3360$ ns Program completion time: $T_{prog.N}=3360\times500,000$ = 168×10^7 ns

hatio of new program completion time, compare to old: $\frac{T_{progn}}{T_{prog}} = \frac{168 \times 10^{7}}{252 \times 10^{7}} = \boxed{\frac{2}{3}}$

Question 4

- +) Let t be the time it takes to execute a program in level 1.
- +) An instruction at level n is translated into S instructions at level n-1
- -> Each level is 3 times as powerful as the level below it

But as optimal translation from a level to one below is hard to achieved \rightarrow each additional level of translation slow the machine down.

- > Each level runs S times jaster than the level above it.
- +) Given the above conclusion:

Runtime at level
$$2: \frac{t}{5}$$

level 5:
$$\frac{1}{5} = \frac{1}{5}$$

$$\frac{t}{S^5}: t = \boxed{\frac{1}{S^5}}$$

Question 6

a) Number of transistors on 12A size chip, year 0: 8000 x 12= 96,000

Hear	i							
Doubling pe.	0	4	8 -	12	16-	20		
d years	1000	Nox 24/4 = 584,000	No x 28,2 =1,536,000	No x 212/2 = 6,144,000	No x 216/2 = 24,576,000	No x 2 ^{20/2} = 98,504,000		
1.5 years C18 months)	36000 (N)				No = 216/1.5	No x 2 ²⁰ 11.5 = 990,842,231		

(graph attached below)

b, (Since the question didn't mention the chip size, assuming this with area A)

Length of one side of the chip: VA

Number of transistors on one siden: $\sqrt{8000} = 40\sqrt{5}$

-> Length of 1 side of transistor, year 0: VA (lold)

Since we have $l_{new} = \frac{loid}{\sqrt{2n}}$ -> $\frac{l_{new}}{loid} = \frac{1}{\sqrt{2n}}$, with n is the number of doubling period.

+> For 1.5 years (18 months) doubling period

