Reactive Jamming

Lab Report

Daniel May
Simon Schmitt
Technische Universtität Darmstadt
daniel_nicolas.may@stud.tu-darmstadt.de
simon_johannes.schmitt@stud.tu-darmstadt.de

ABSTRACT

This lab report presents the creation of an reactive jammer. We will describe how the frame handling works on the WARP and how it can be used to suppress individual targeted devices or communications respectively. At the end of this report we evaluate the performance of our jammer and discuss possible improvements.

1 INTRODUCTION

Wireless signals, as they are used in most of today's analog or digital communications, are very sensitive and affectable by the environment. Signals with the same frequency can interfere and suppress each other. This effect is typically used by jammers to prevent a certain receiver from decoding a signal. While jamming is typically associated with malicious behaviour or within military conflicts to hinder an opposing party from exchanging information, there also exists other jamming schemes, so called friendly jamming. Friendly jamming can be used to protect vulnerable systems from adversarial actions, e.g., pacemakers that can be wirelessly reprogrammed. More recent work also demonstrated that secrete key-exchanges can be realized at the physical layer utilizing a jammer.

The objective of this lab was to create a reactive WiFi jammer using the Wireless Open-Access Research Platform (WARP). WARP is a programmable Software-Defined Radio (SDR) which provides a basic implementation of the 802.11g WiFi standard. The architecture of the WARP allows to transmit frames while still receiving a signal. Thus WiFi transmissions with a certain Medium Access Control (MAC) address can be analyzed and jammed if they are matching a target address.

In comparison with existing jammers this approach is more precise as it only suppresses the signals of a certain target, while still allowing the communication of other devices. This also results in much lower power-consumptions, due to the smaller amount of frames that have to be jammed.

2 BACKGROUND ON THE WARP

The WARP is a transceiver, which means it can be used to send and receive frames respectively. This is realized with two independent paths of circuits that are connected to a single antenna (1). The antenna is followed by a switch (2) to either connect to the transmit or receiver path. At the receiving path the incoming frames are directly forwarded to the transceiver module (4), while the signals leaving this module are amplified to a fixed gain (3). The transceiver module controls the conversion between the complex baseband

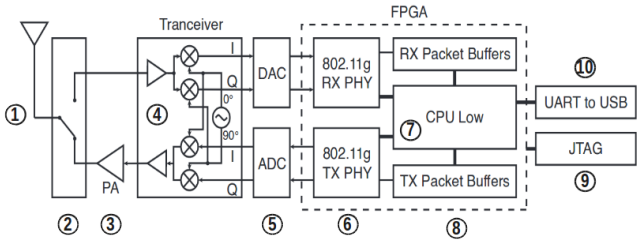

Figure 1: Block diagram of the 802.11 WARP

signal and the Radio Frequency (RF) using a quadrature modulator. The next layer (5) contains the Digital-to-Analog and Analog-to-Digital Converter, which are connected to the chips that implement the 802.11 physical layer (6). Incoming frames are written into a RX Packet Buffer, while outgoing frames are read from the TX Packet Buffer. Both buffers represent shared memory that is also accessible by the MicroBlaze processor (7), which handles the MAC layer of the network interface. Whats special about the WARP, is the fact that the processor allows to start processing while incoming frames are still being received. This allows us to implement a reactive jammer. It is only necessary to prepare the frames used for the jamming signal. Those frames are stored in the TX Packet Buffer and can be send as soon as a condition matches to the incoming frame.

The JTAG port (9) is used to flash the firmware of the processor and to upload the implementation of the reactive jammer. Any debugging messages that are written to the standard output can be observed in a terminal that is connected to the UART to USB port (10).

3 IMPLEMENTATION

4 CONCLUSION AND TAKE-AWAY

5 FUTURE WORK

REFERENCES

SEEMOO PhySec (Lab RDS) 2017/18, February 2018, Darmstadt, Germany