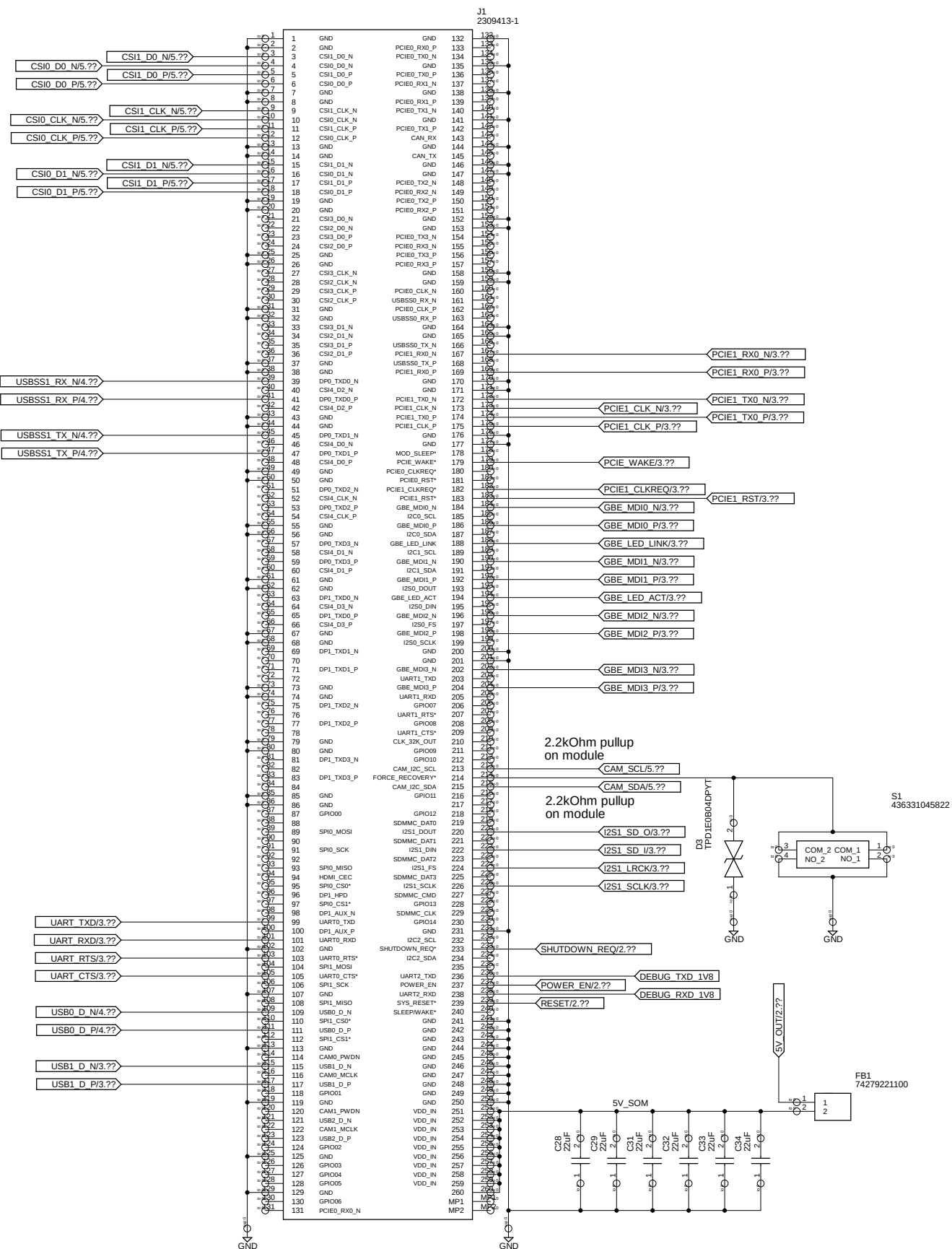


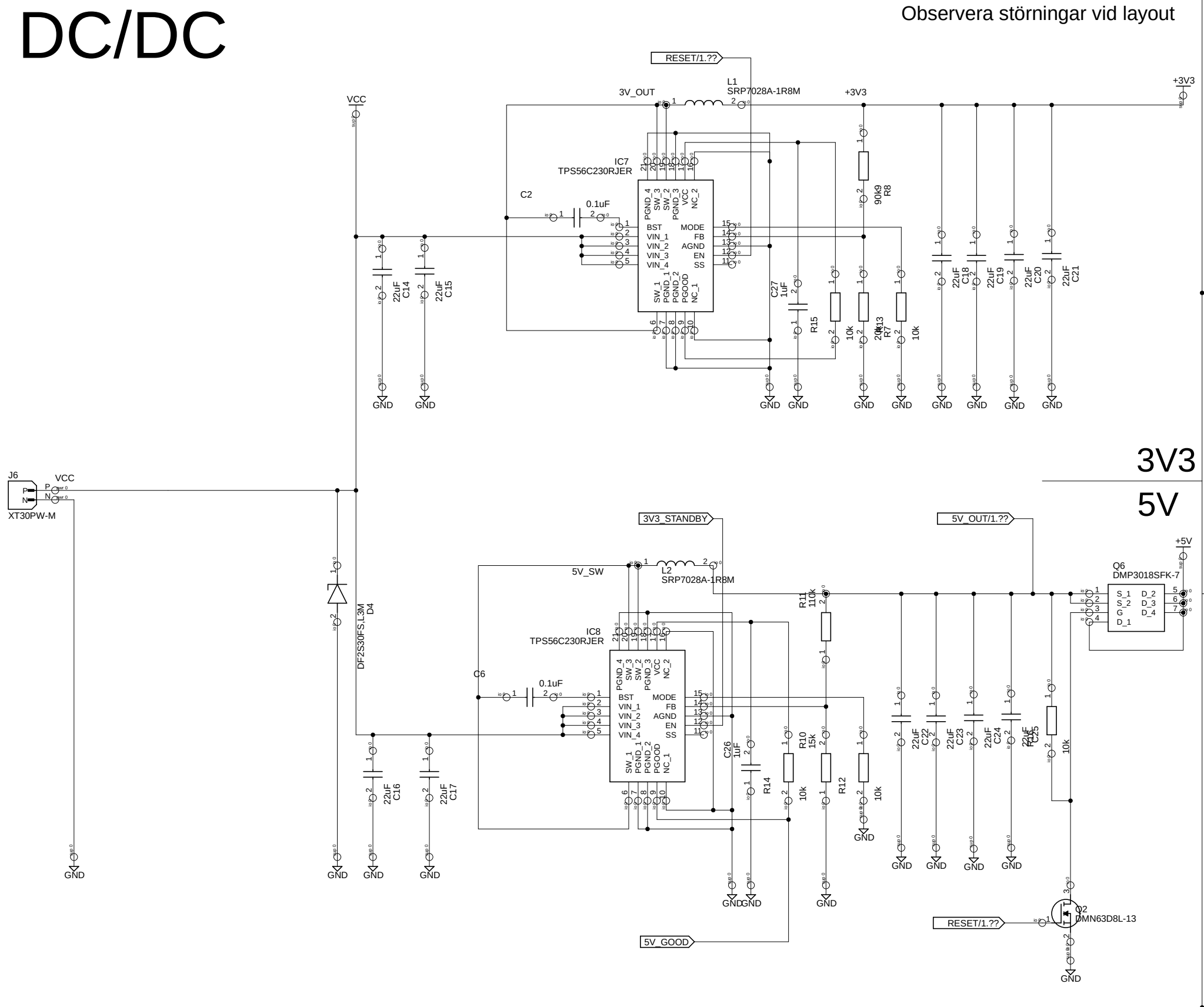
SOM



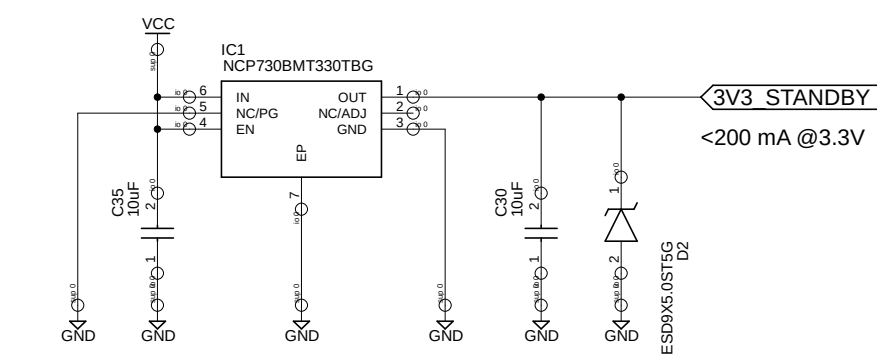
RESET-knapp?

POWER

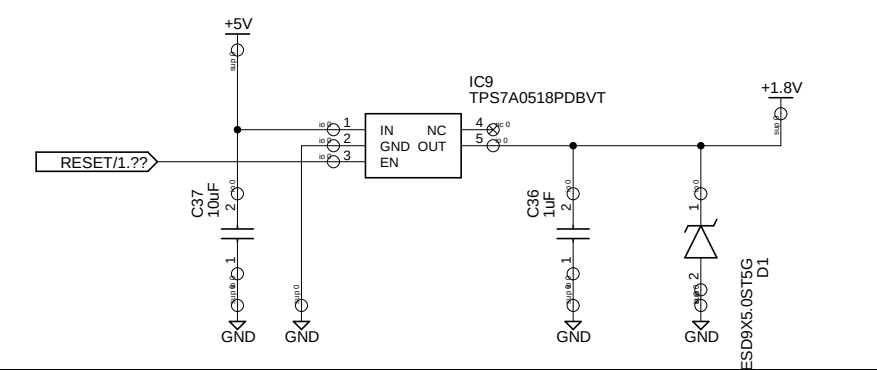
DC/DC



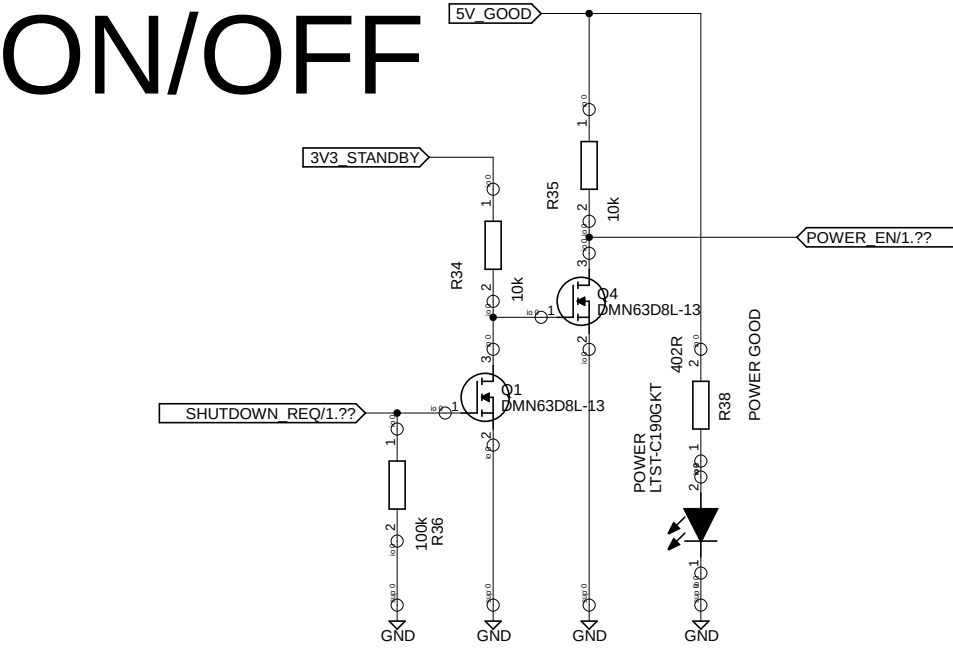
3V3 LDO



1.8V LDO

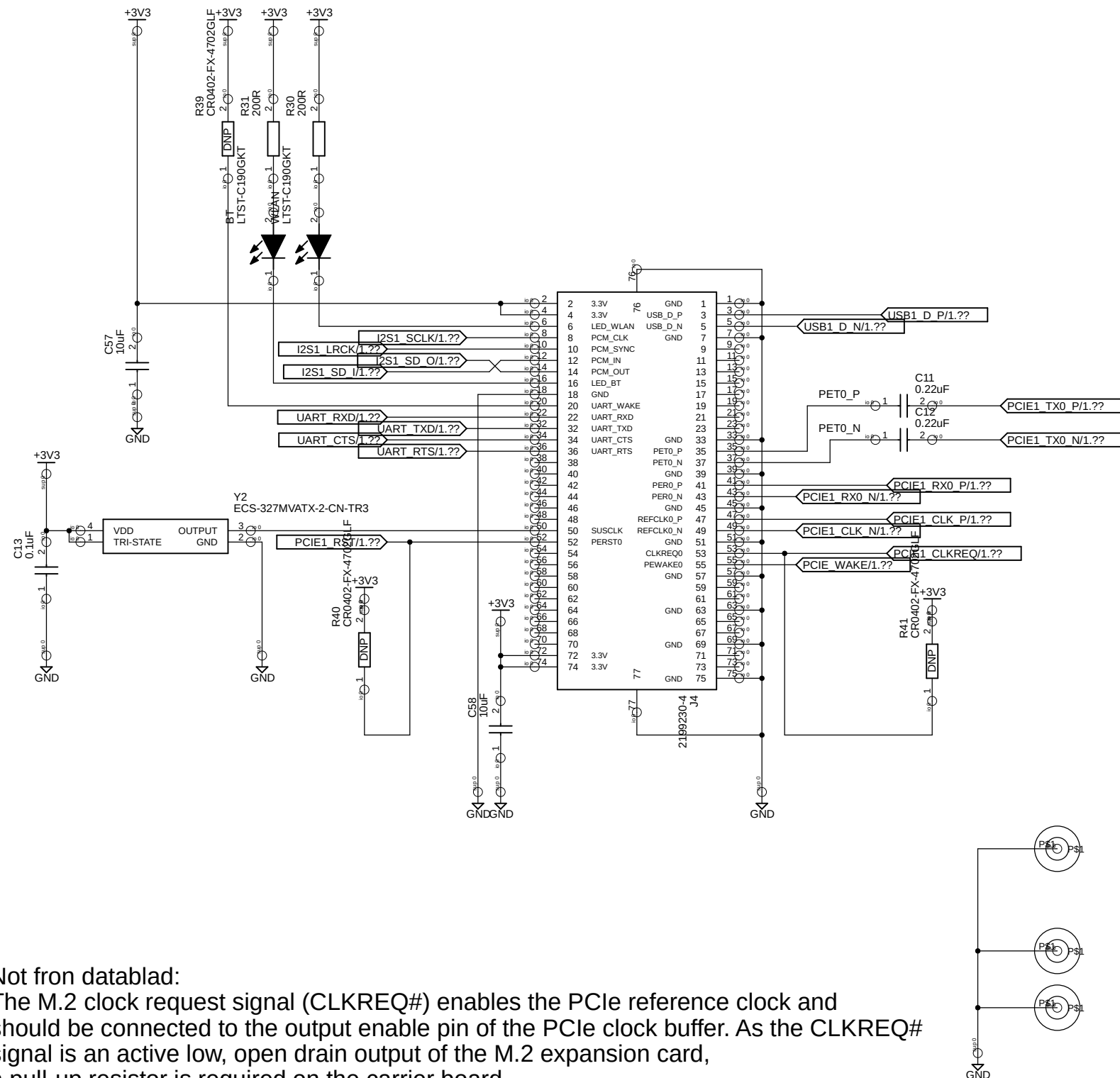


ON/OFF



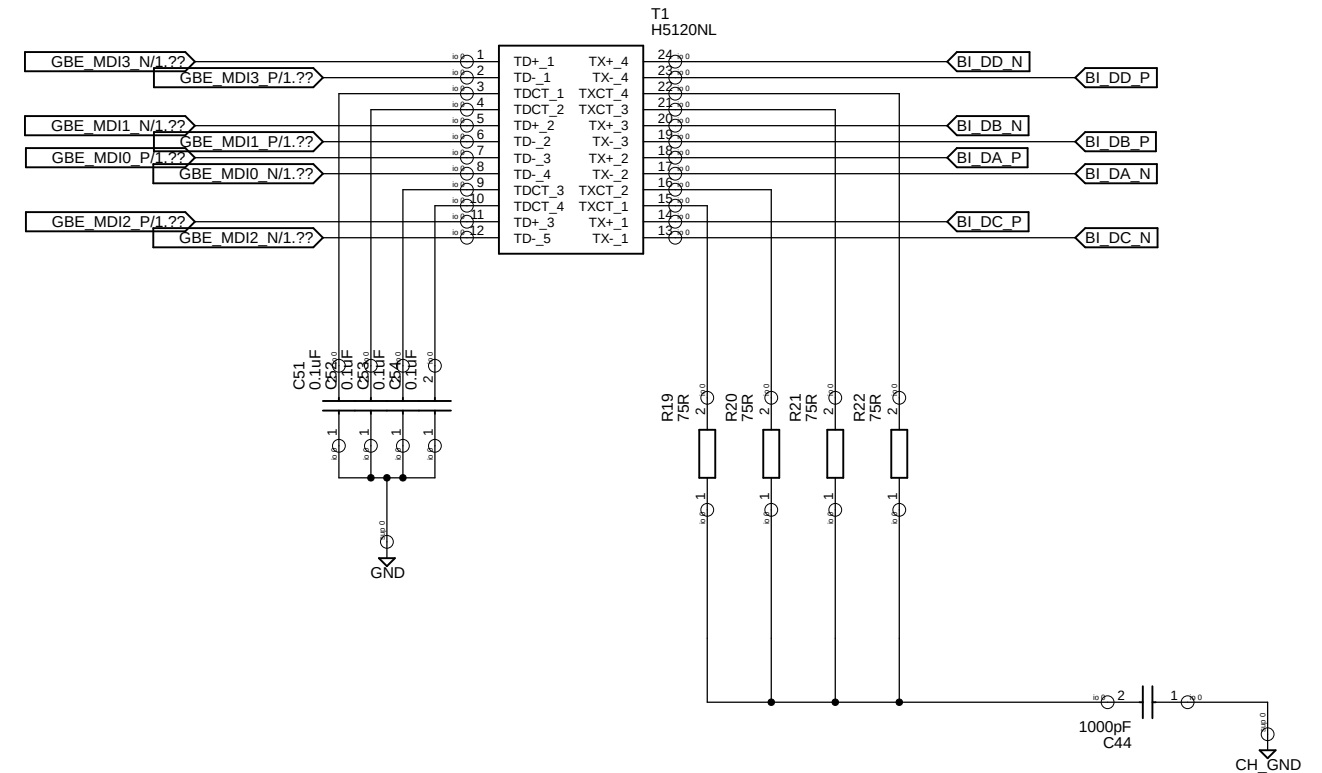
PCIE & ETHERNET

EV pullup på vissa pinnar, se DNP

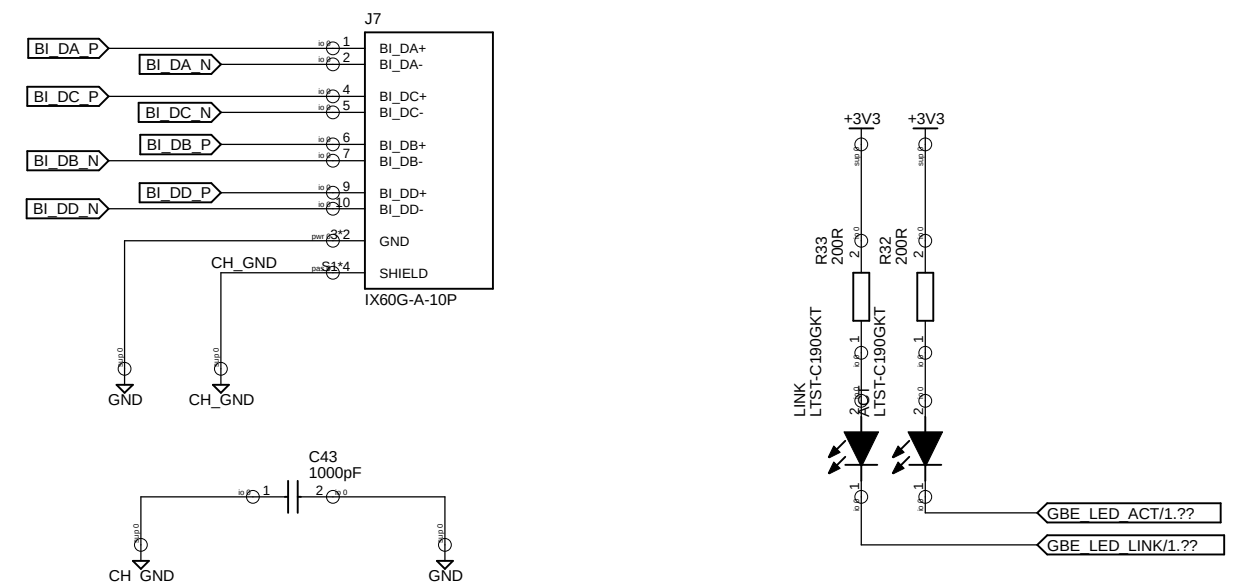


Not from datablad:
The M.2 clock request signal (CLKREQ#) enables the PCIe reference clock and should be connected to the output enable pin of the PCIe clock buffer. As the CLKREQ# signal is an active low, open drain output of the M.2 expansion card, a pull-up resistor is required on the carrier board.

ETHERNET ISOLATION

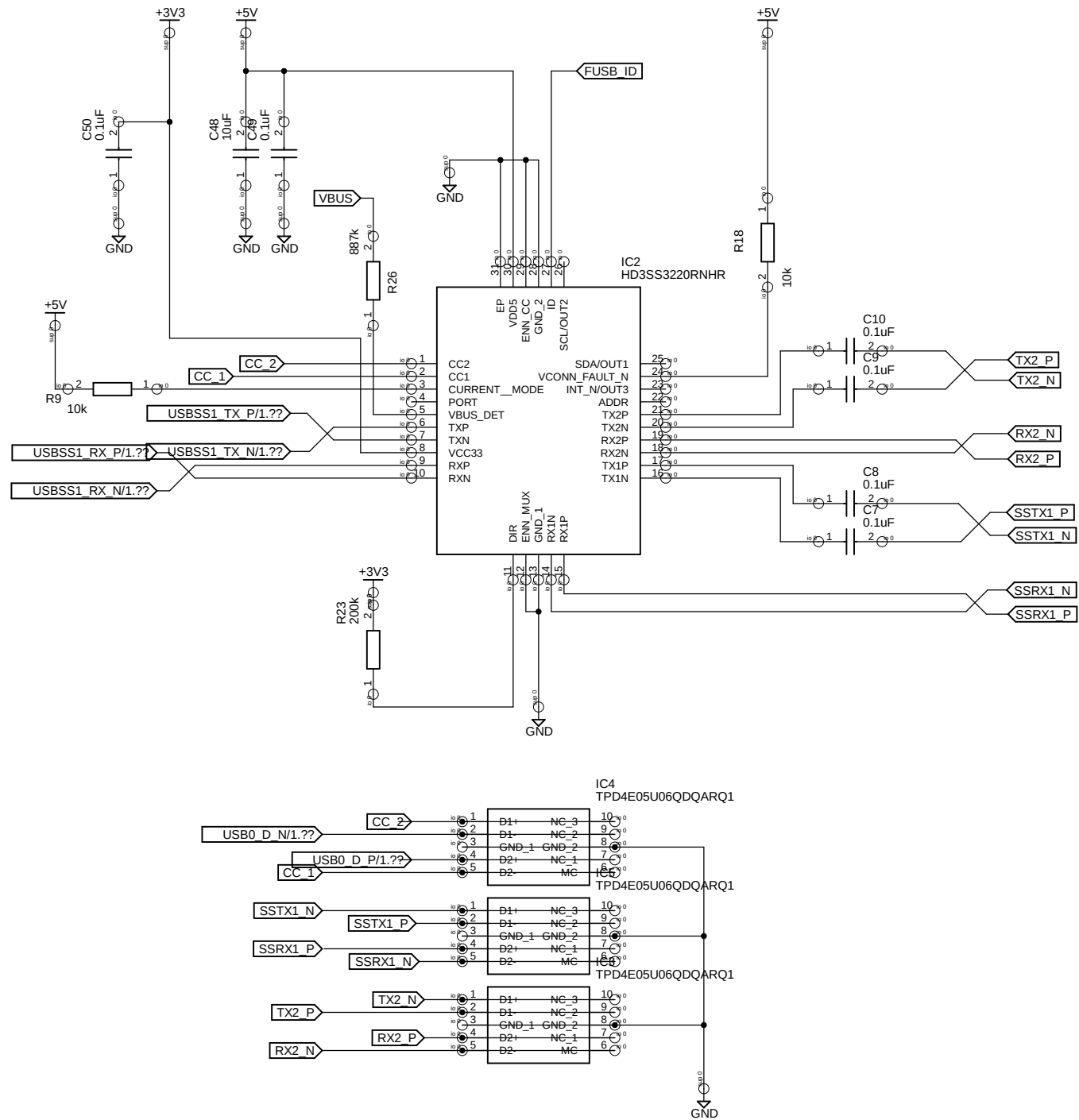


ETHERNET

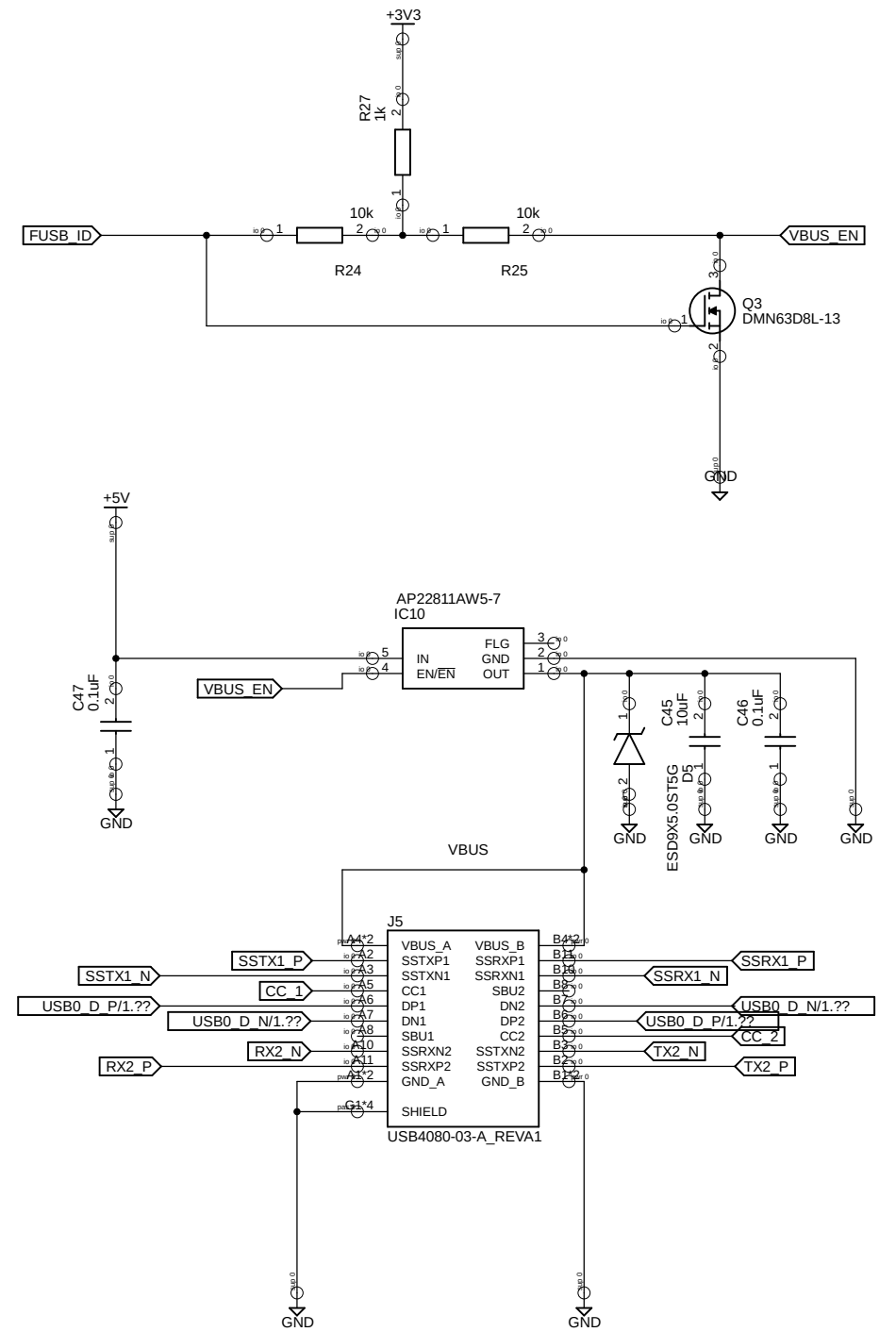


USB C

MUX pga DRP och vändbar



TRIPPLA TVS-DIODER?



[illegible][illegible]