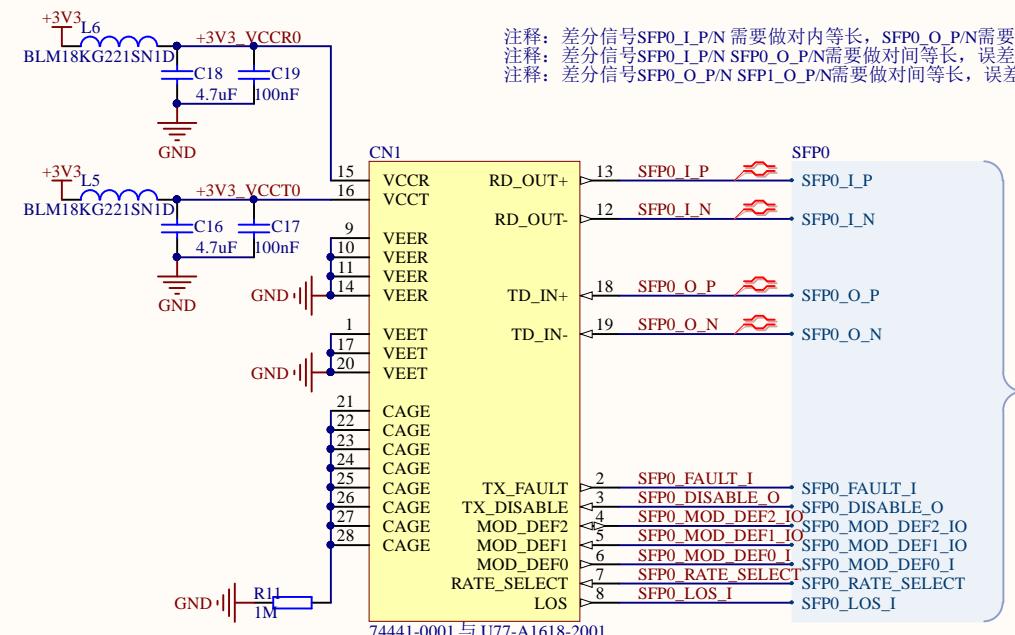
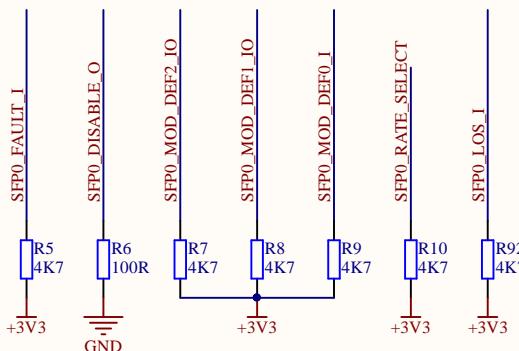


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Title

Size	Number	Revision
A4		
Date:	2022/8/19	Sheet of
File:	D:\WorkDir\..\CON_SFP0.SchDoc	Drawn By:

A

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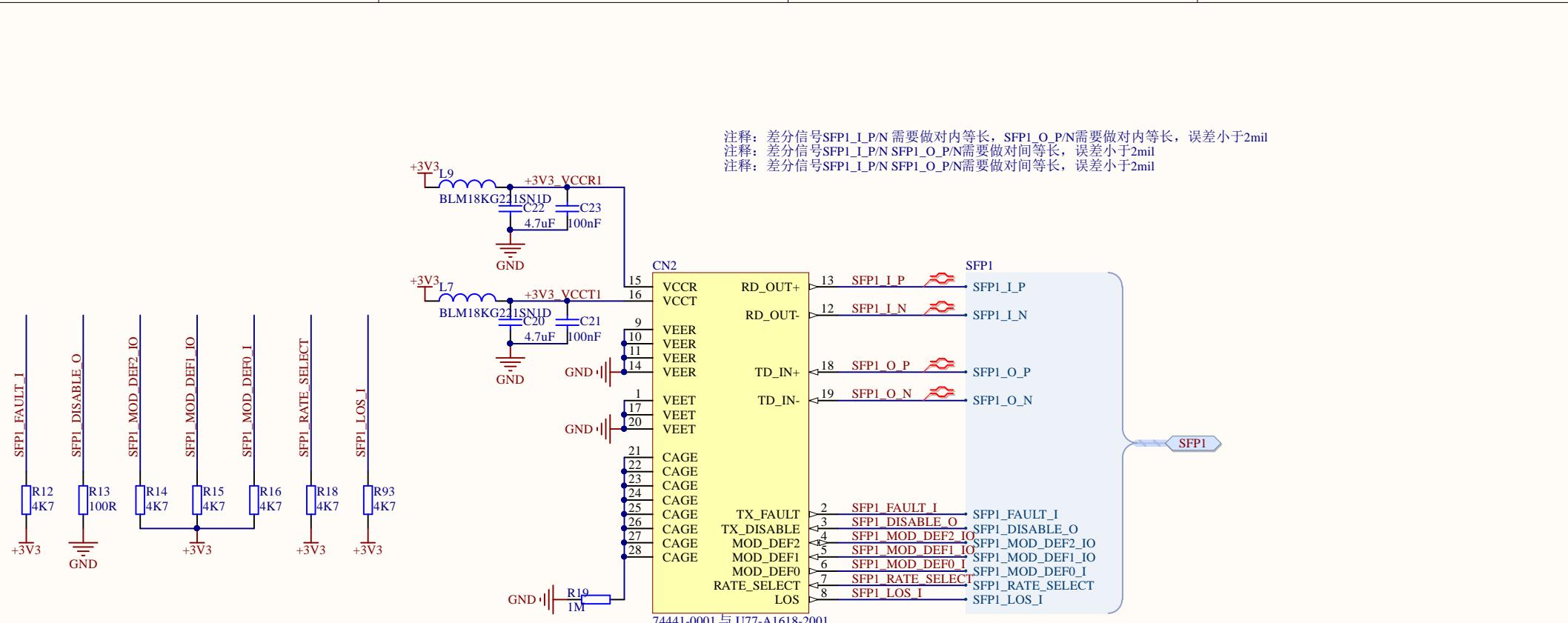
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Title

Size	Number	Revision
A4		
Date:	2022/8/19	Sheet of
File:	D:\WorkDir\..\CON_SFPI.SchDoc	Drawn By:

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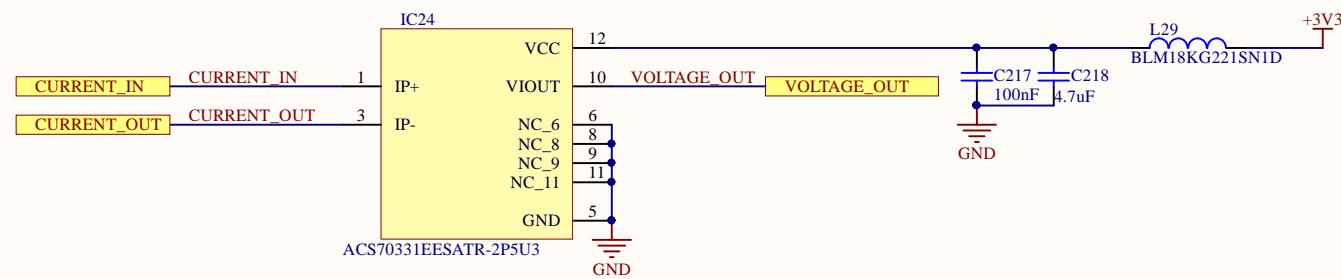
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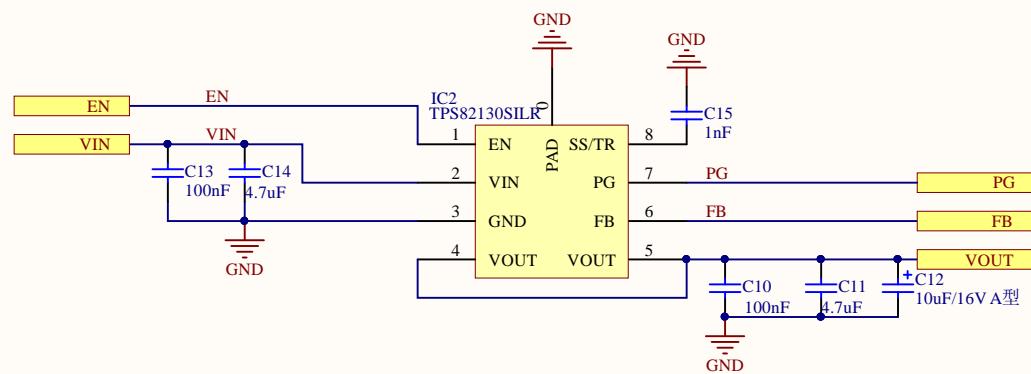
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Title		
Size A4	Number	Revision
Date: 2022/8/19	Sheet	of
File: D:\WorkDir\..\CURRENT_SENSOR.Sch	Drawn By:	

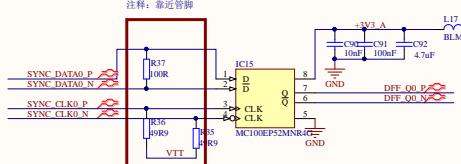


注释：电源线请加粗 不低于15mil

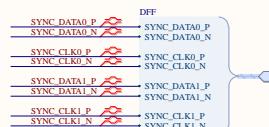
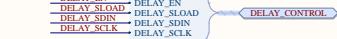
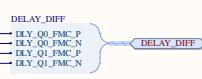
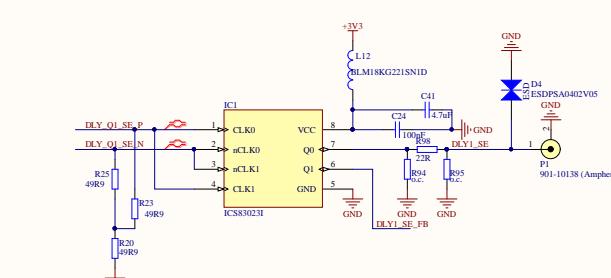
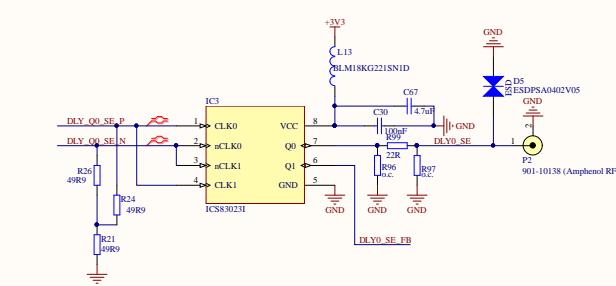
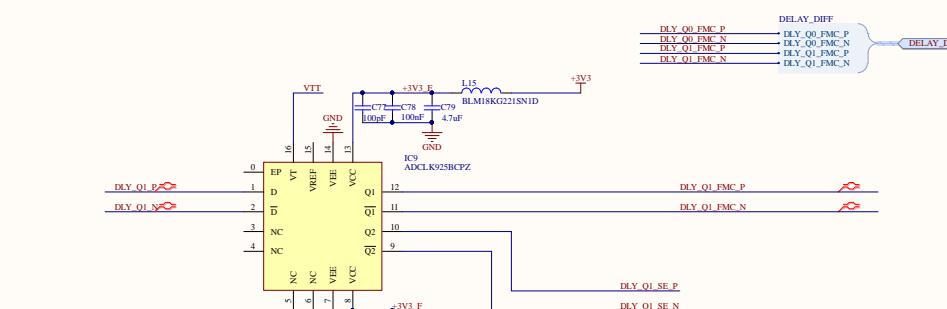
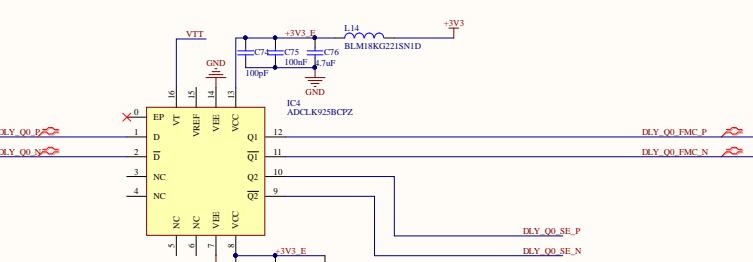
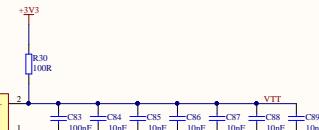
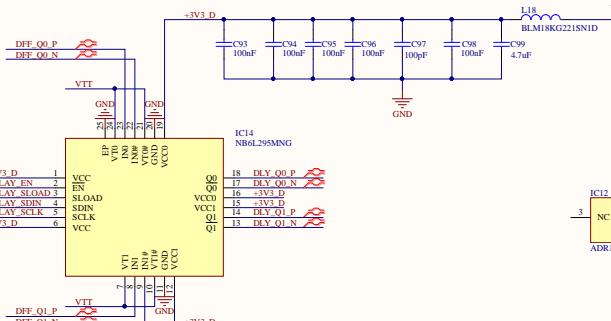
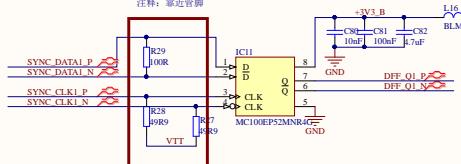
Title		
Size A4	Number	Revision
Date: 2022/8/19	Sheet of	
File: D:\WorkDir\..\DCDC_3A.SchDoc		Drawn By:

注释：差分信号内部需要做对内等长，误差小于 2mil
注释：此图尾端为 0 的差分对需要做对称等长，误差小于 2mil

注释：靠近管脚



注释：靠近管脚



Title		
Size	Number	Revision
A2		
Date:	2022/8/19	Sheet of
File:	D:\WorkDir\DFP_DELAY_FANOUT.SCH	Shown By:

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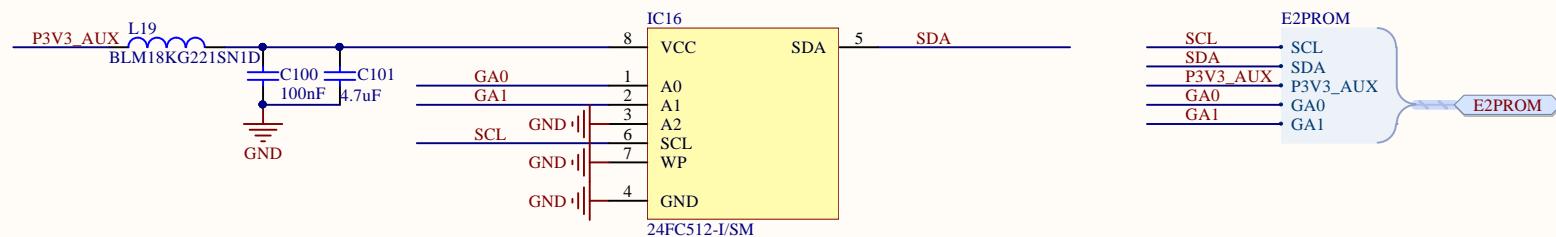
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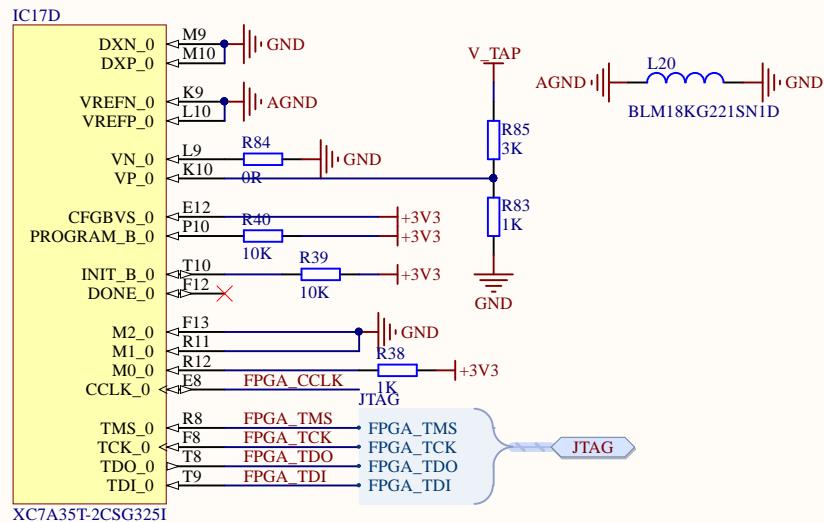
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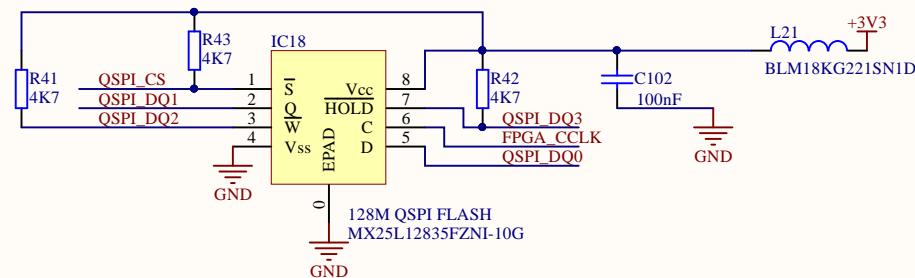
Title		
Size	Number	Revision
A4		
Date: File:	2022/8/19 D:\WorkDir\..\E2PROM.SchDoc	Sheet of Drawn By:

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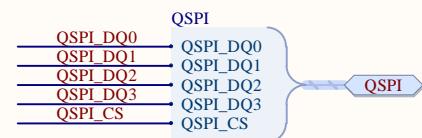
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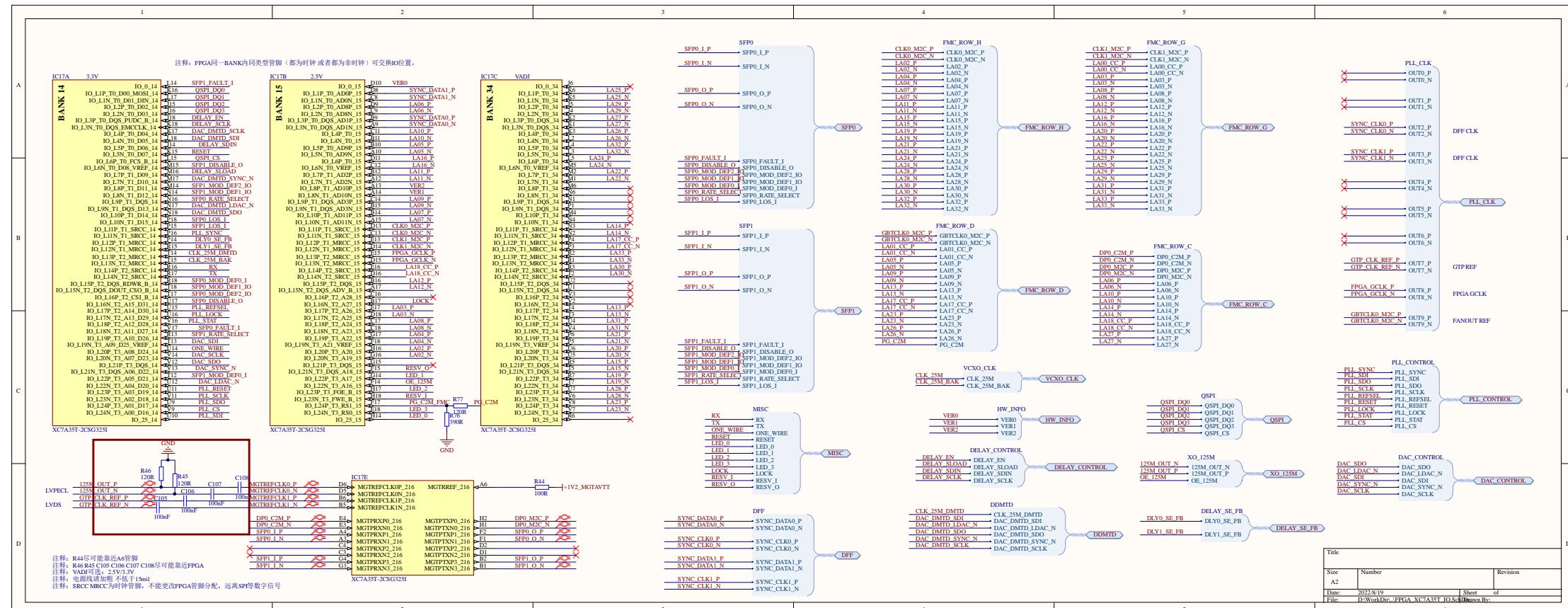
C

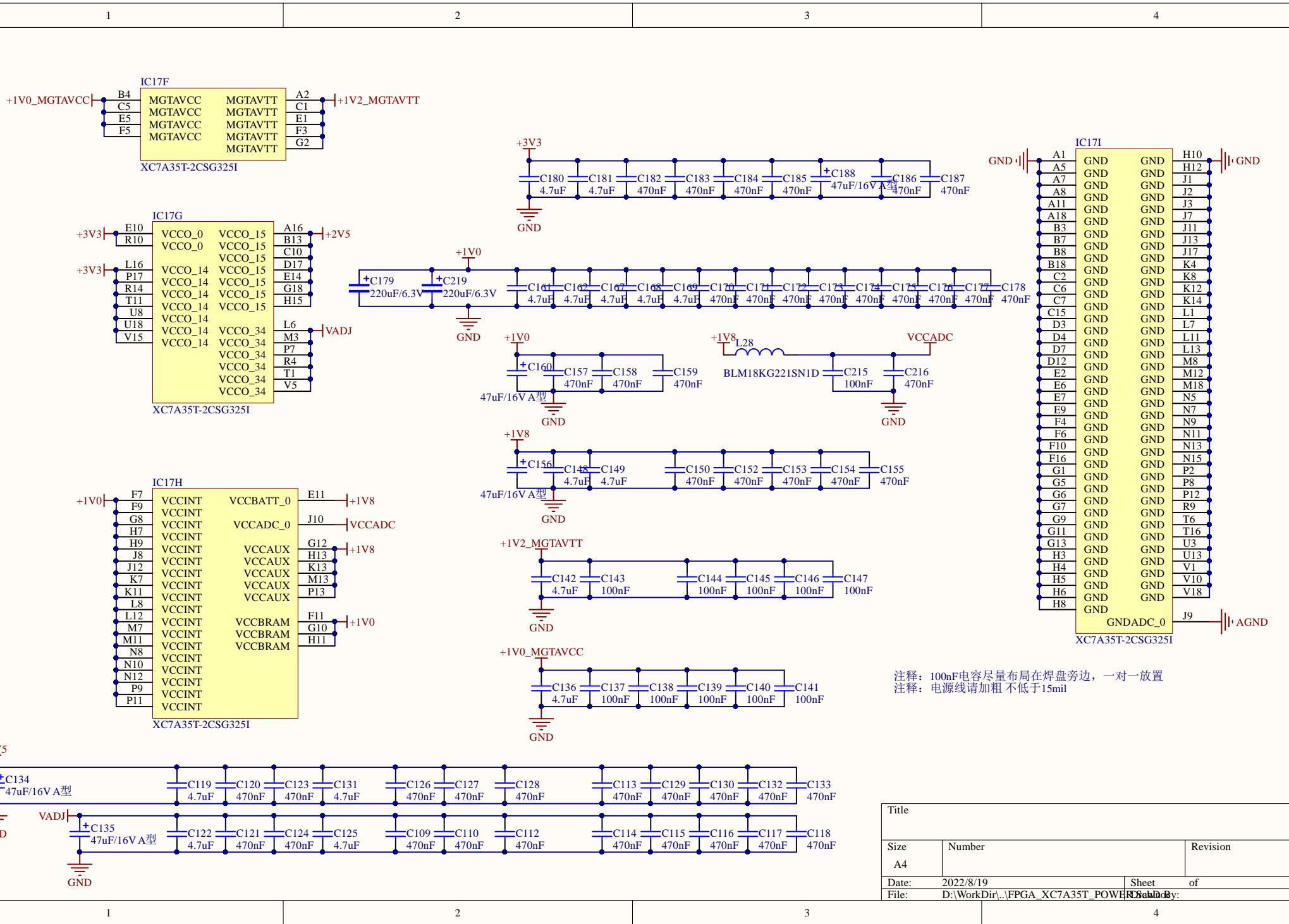


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Title		
Size	Number	Revision
A4		
Date:	2022/8/19	Sheet of
File:	D:\WorkDir\..\FPGA_XC7A35T_CONF\	Block Diagram





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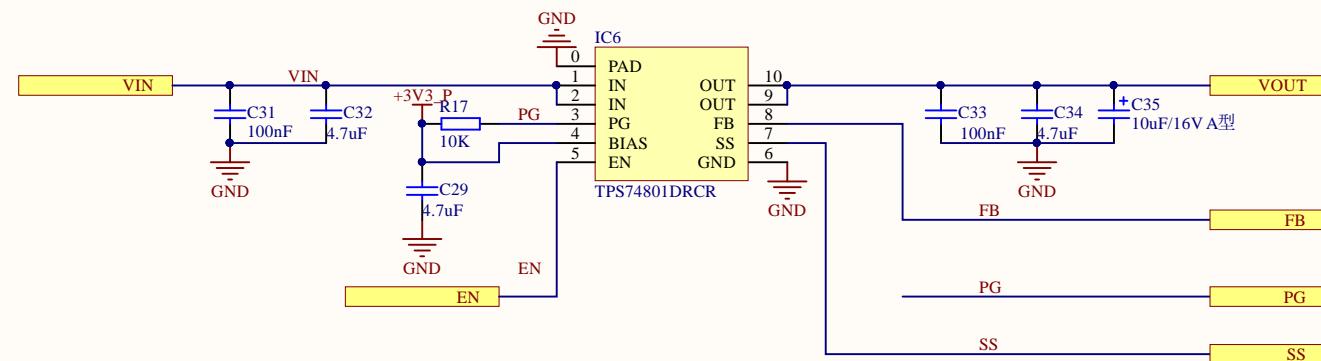
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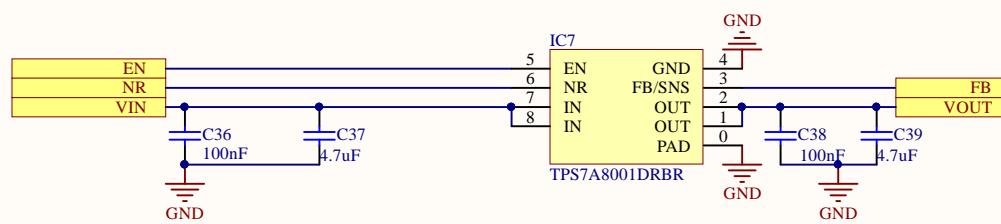
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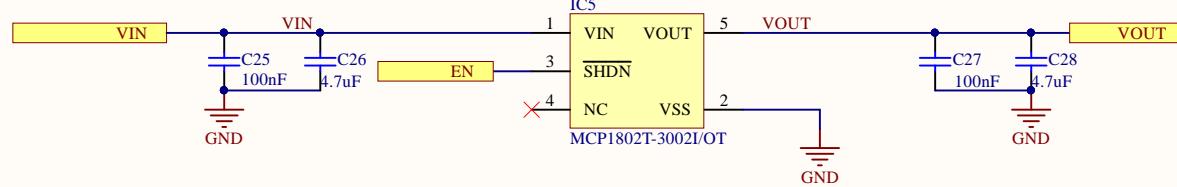
注释：电源线请加粗 不低于15mil

Title		
Size A4	Number	Revision
Date: 2022/8/19	Sheet of	
File: D:\WorkDir\..\LDO_GTP_1.5A.SchDoc		Drawn By:



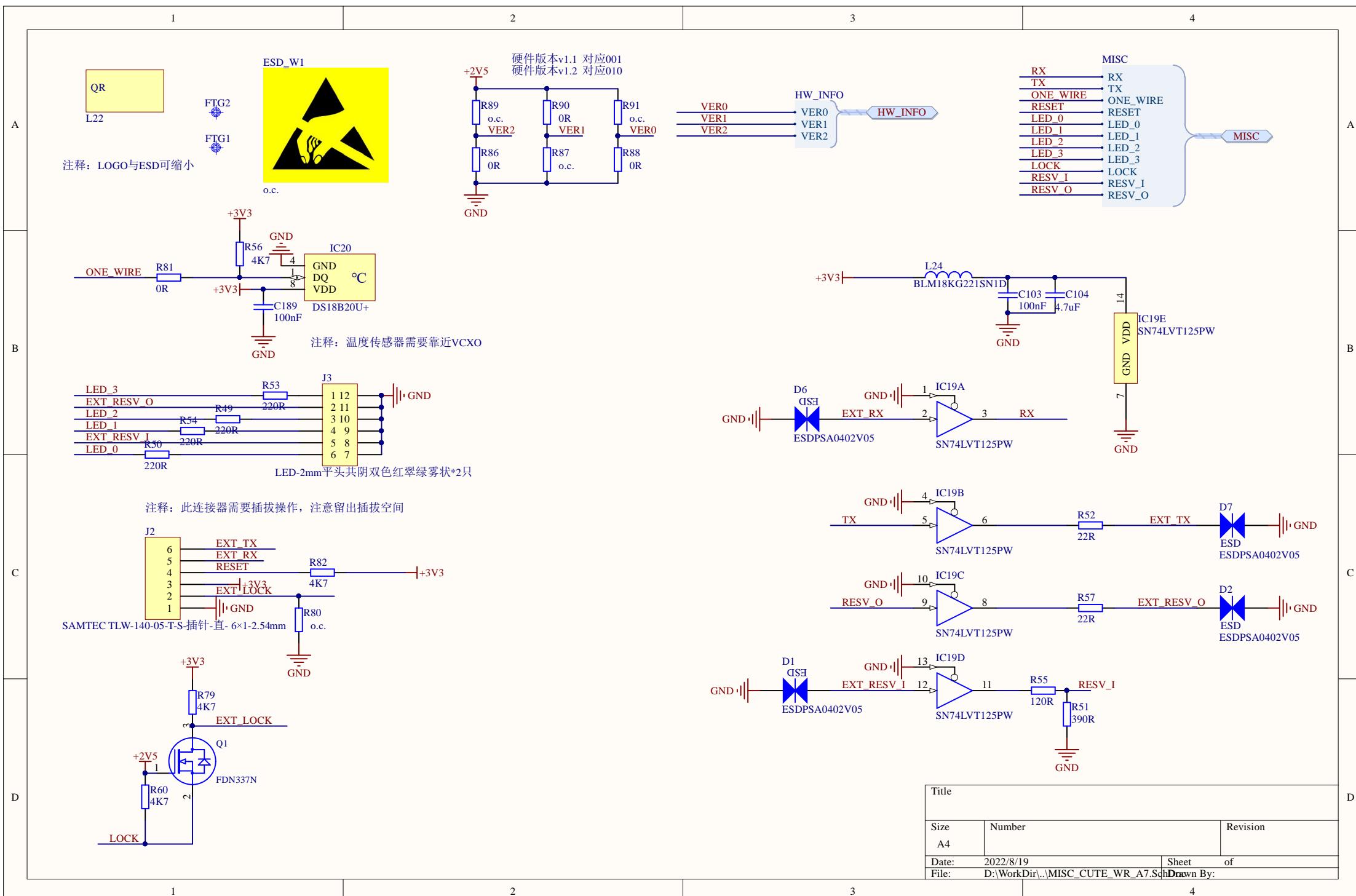
注释：电源线请加粗 不低于15mil

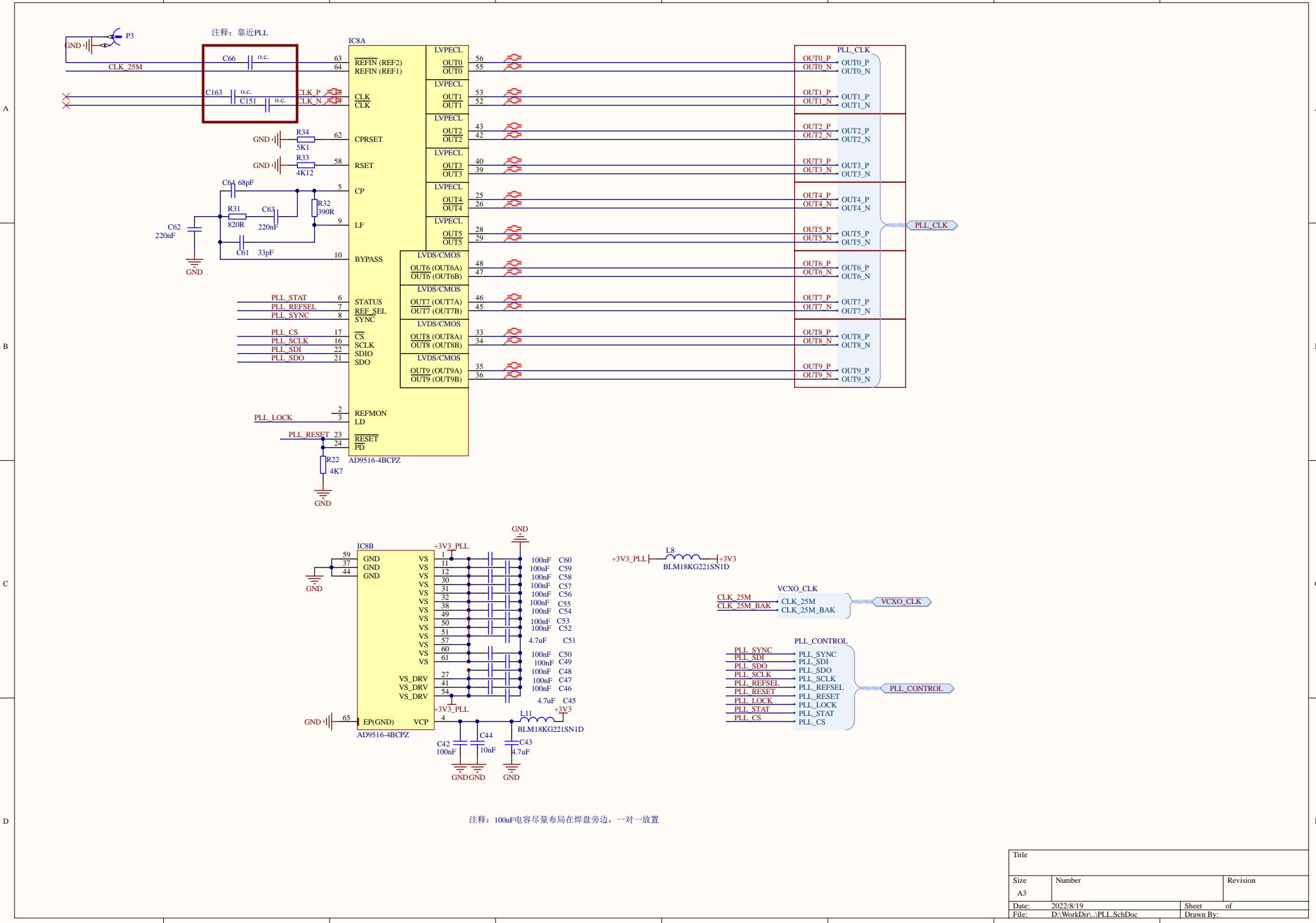
Title		
Size	Number	Revision
A4		
Date: File:	2022/8/19 D:\WorkDir\..\LDO_IO_1A.SchDoc	Sheet of Drawn By:

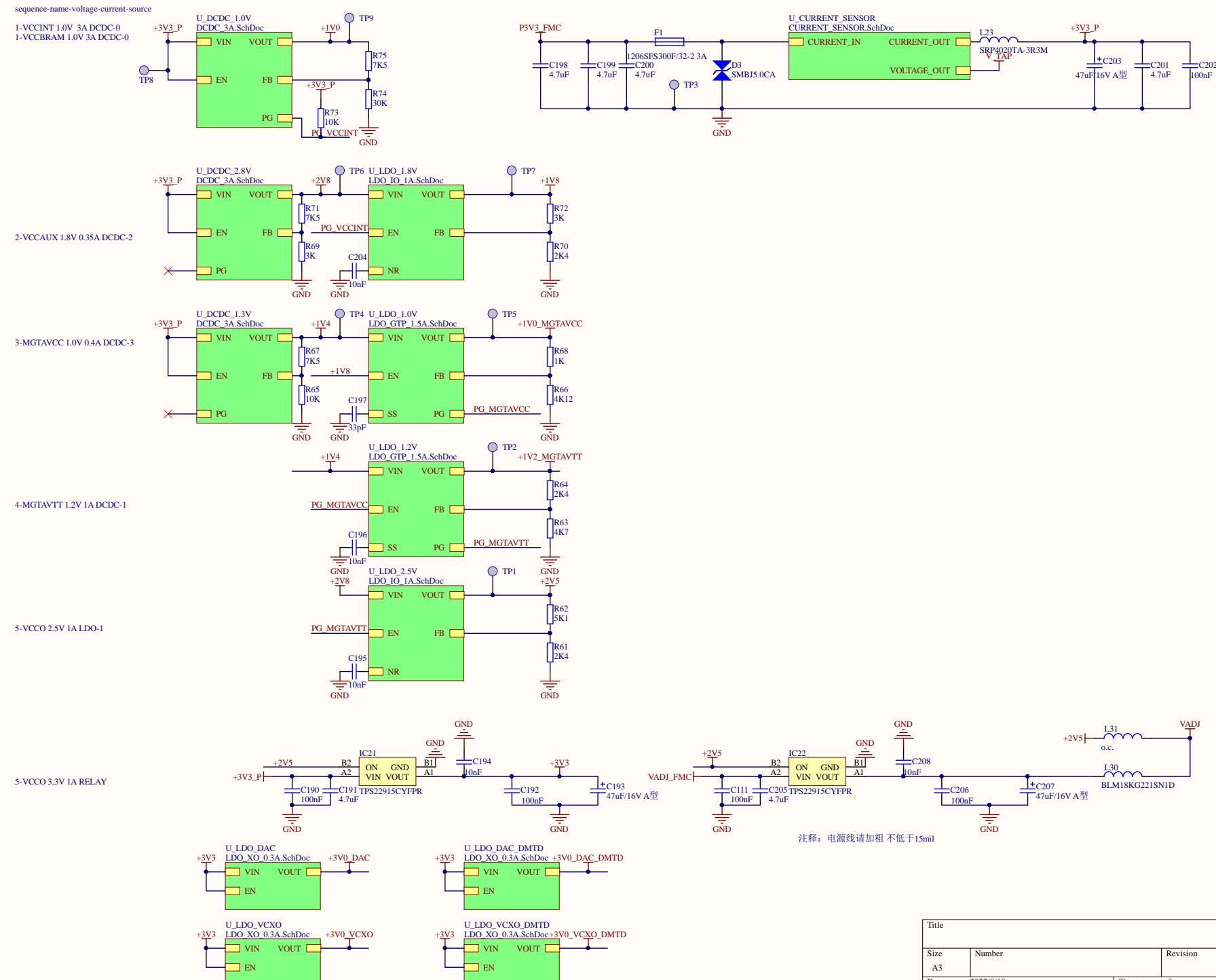


注释：电源线请加粗 不低于15mil

Title		
Size A4	Number	Revision
Date: 2022/8/19	Sheet of	
File: D:\WorkDir\..\LDO_XO_0.3A.SchDoc		Drawn By:

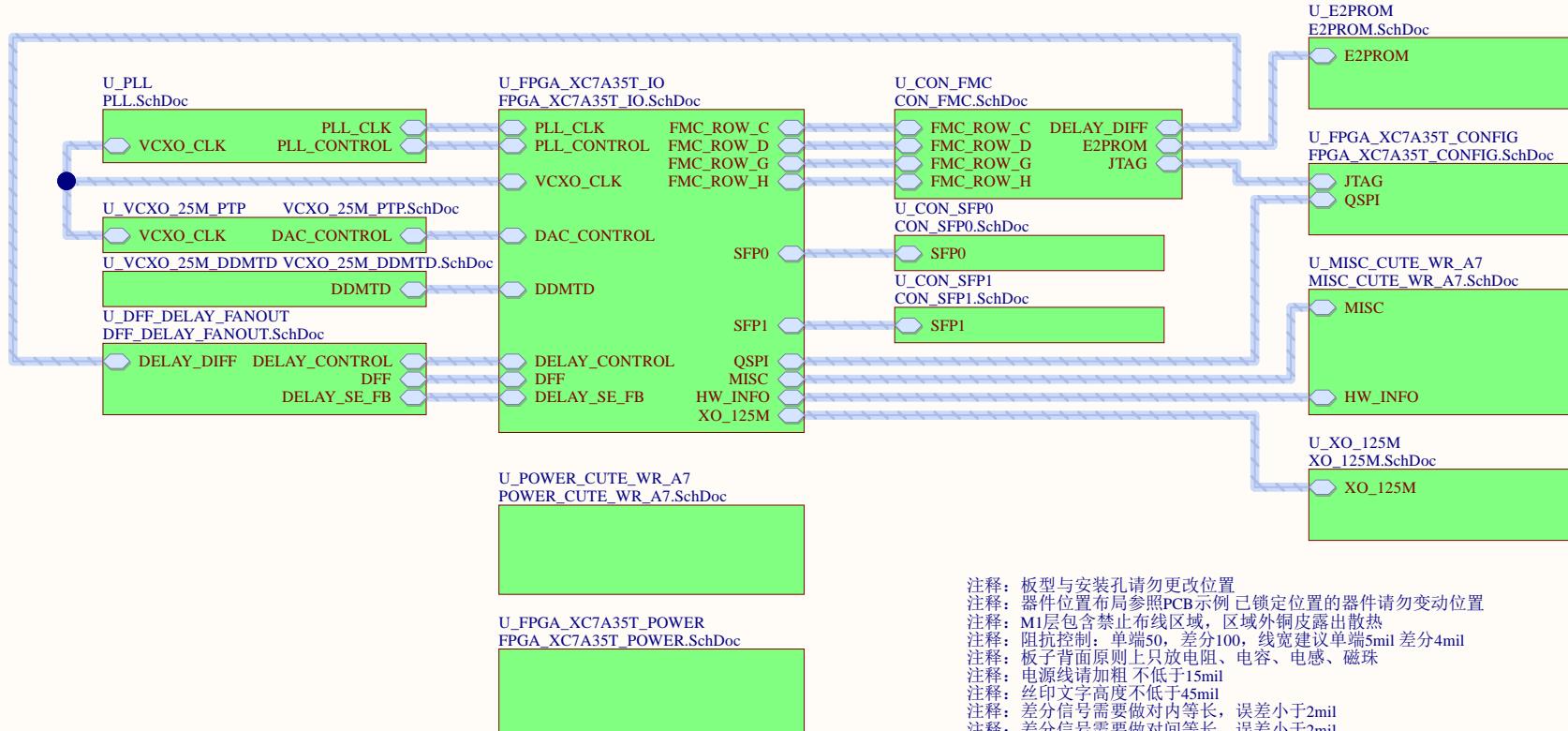






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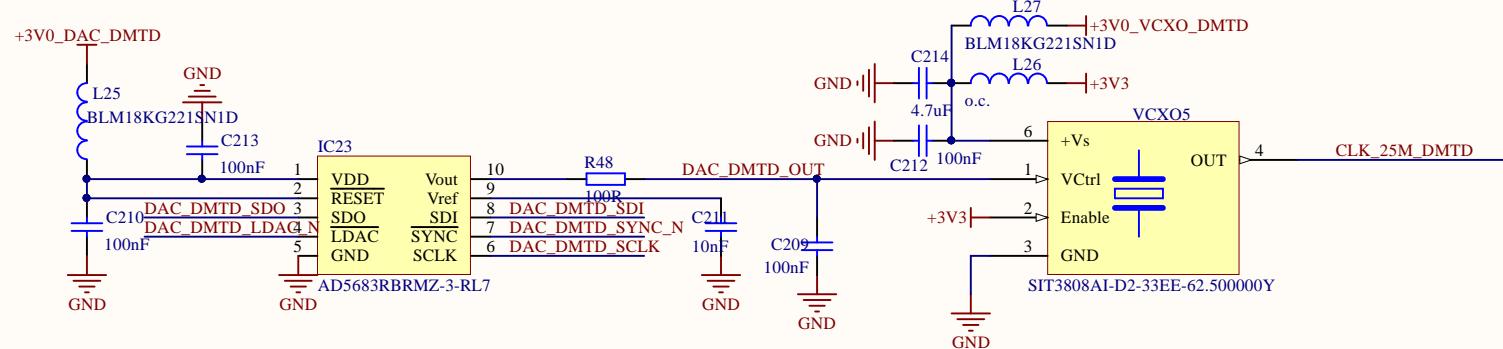
注释：板型与安装孔请勿更改位置
注释：器件位置布局参照PCB示例 已锁定位置的器件请勿变动位置
注释：M1层包含禁止布线区域，区域外铜皮露出散热
注释：阻抗控制：单端50，差分100，线宽建议单端5mil 差分4mil
注释：板子背面原则上只放电阻、电容、电感、磁珠
注释：电源线请加粗 不低于15mil
注释：丝印文字高度不低于45mil
注释：差分信号需要做对内等长，误差小于2mil
注释：差分信号需要做对间等长，误差小于2mil

Title		
Size	Number	Revision
A4		
Date: File:	2022/8/19 D:\WorkDir..\TOP_CUTE_WR_A7.SchDoc	Sheet of 1 of 1

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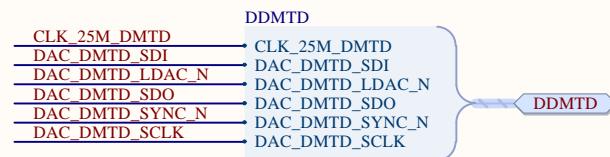
B

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注释: DAC输出单端走线尽可能短, 地保护
注释: CLK_25M_DMTD单端走线尽可能短, 地保护

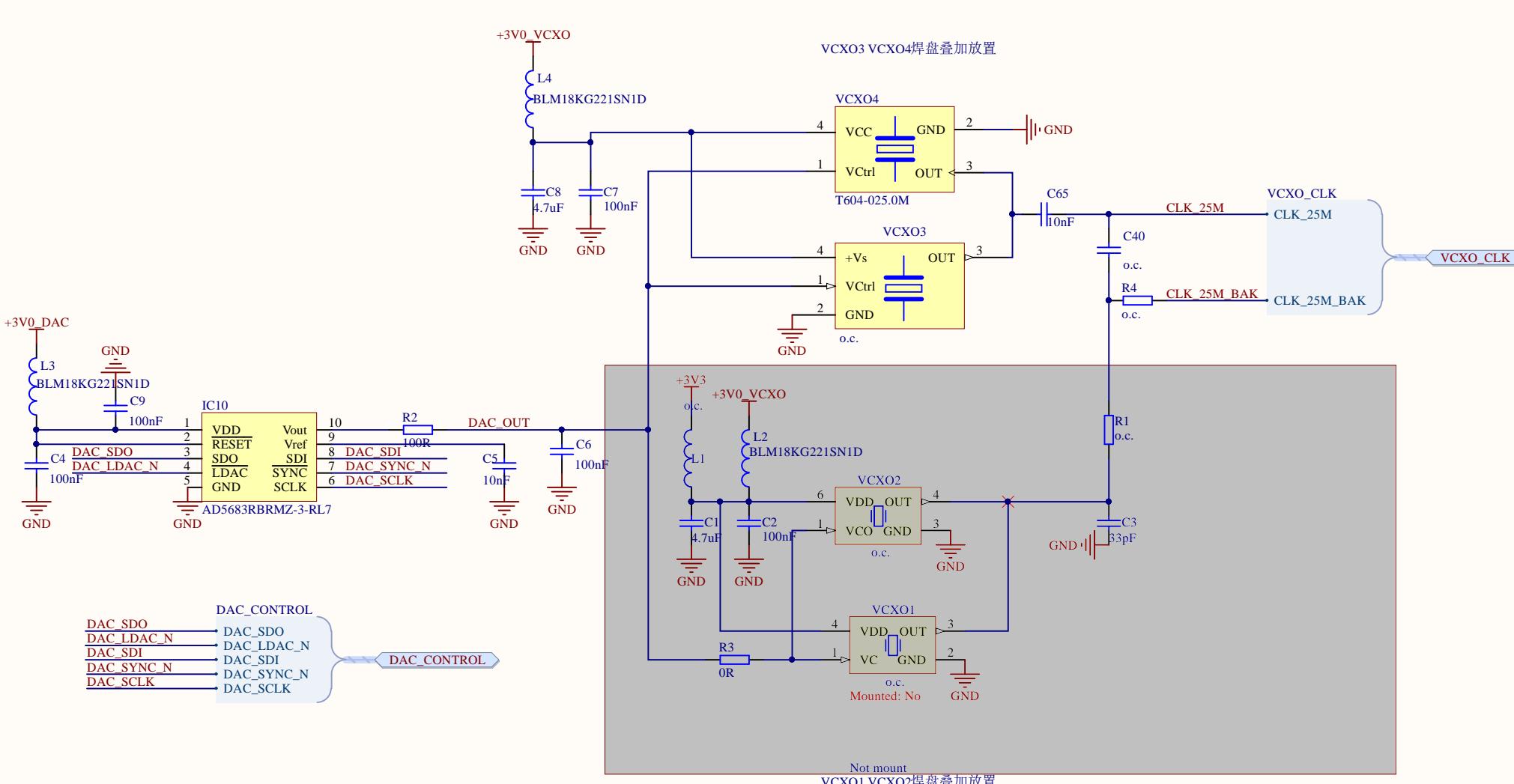


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Title		
Size	Number	Revision
A4		
Date:	2022/8/19	Sheet of
File:	D:\WorkDir\..\VCXO_25M_DDMTD.Sch	Drawn By:

1 2 3 4



Title		
Size A4	Number	Revision
Date: 2022/8/19	Sheet of	
File: D:\WorkDir\..\VCXO_25M_PTP.SchDoc		Drawn By:

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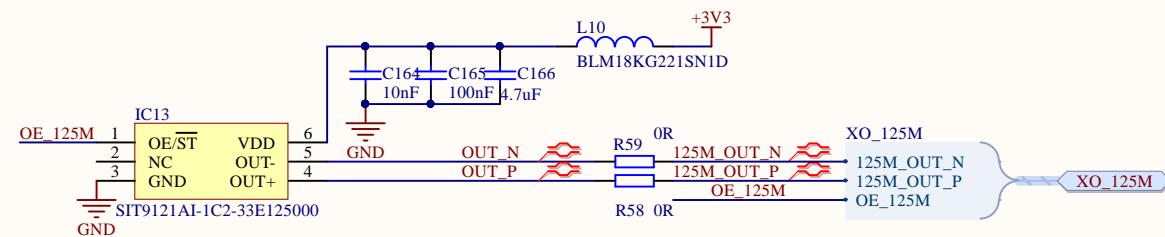
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Title		
Size	Number	Revision
A4		
Date: File:	2022/8/19 D:\WorkDir\..\XO_125M.SchDoc	Sheet of Drawn By:

