



open source  
hardware  
Synthesis

\*TEST SAMPLE\*

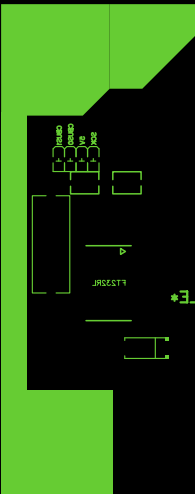
PCIe\_Arduino  
Alpha 0.5 ES 2021.2



2x0 1x8

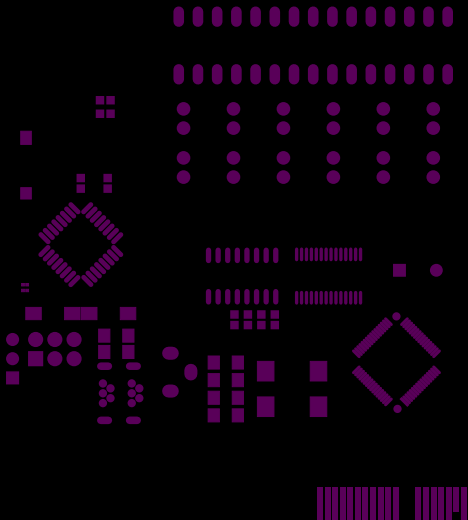


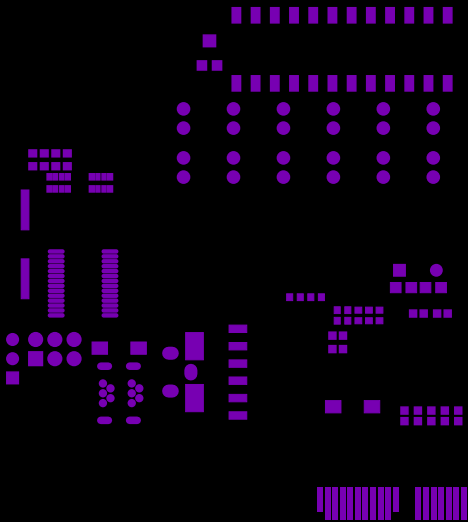
FL3525R





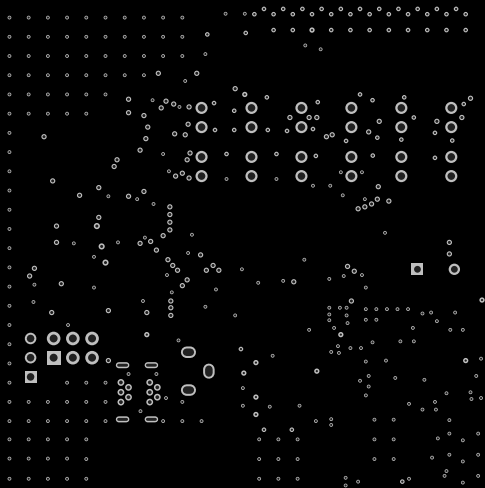














ARDUINO



Synthesis



open source  
hardware



/SynthesisDu

PCIE\_Arduino  
Alpha0.2 ES 2021.2  
JLCJLCJLCJLC

65.000mm

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14.986mm

30.099mm

[https://github.com/SynthesisDu/PCIE\\_Arduino](https://github.com/SynthesisDu/PCIE_Arduino)

