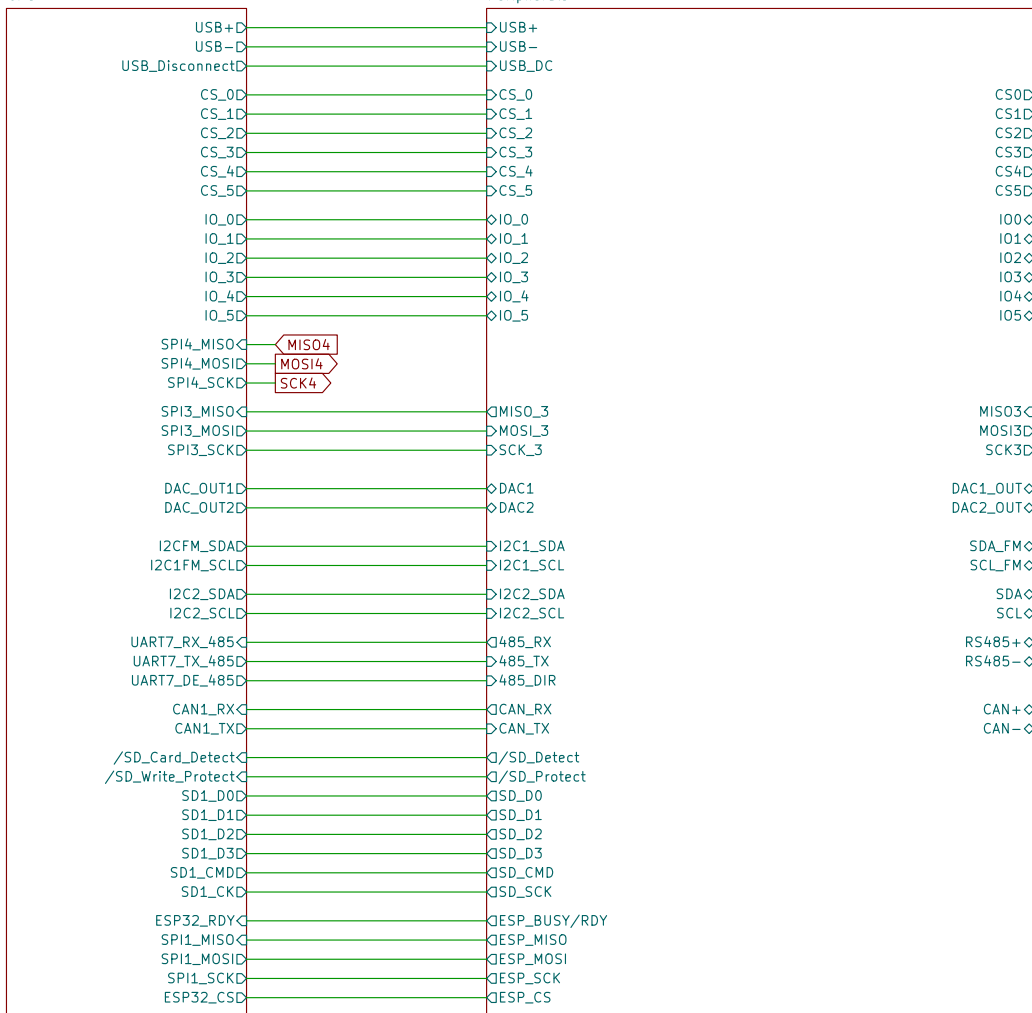


Power

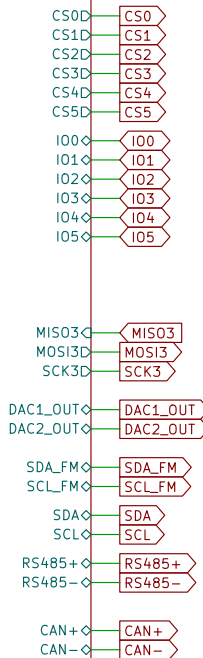
File: power.kicad_sch

CPU



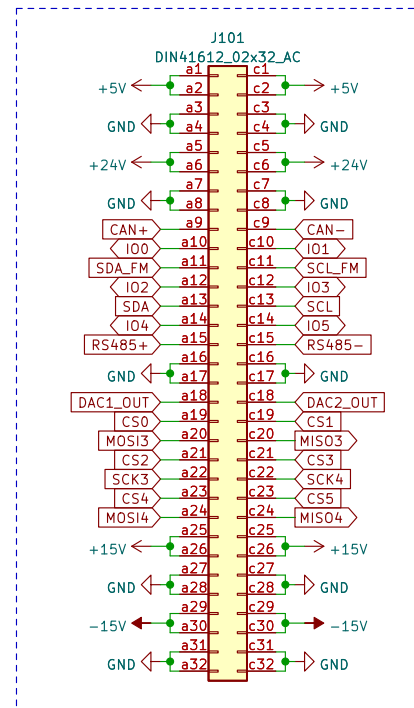
File: cpu.kicad_sch

Peripherals



File: peri.kicad_sch

BACKPLANE



Synthon

Sheet: /
File: Mainboard_767.kicad_sch

Title: Master-Sheet and Bus Connector

Size: A4 Date: 2022-11-19 Rev: V3 R1.0
KiCad E.D.A. kicad (6.0.7) Id: 1/4

.....

SD1 = SD Interface

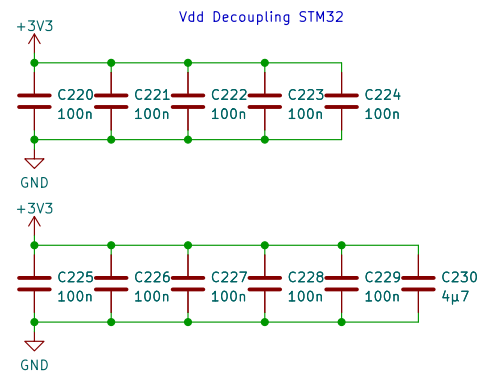
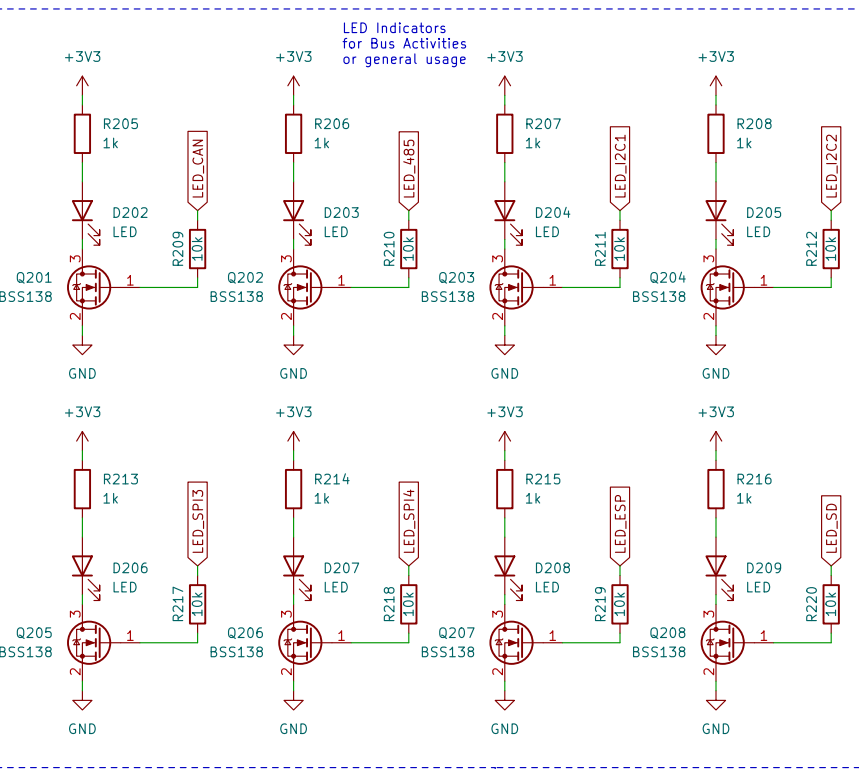
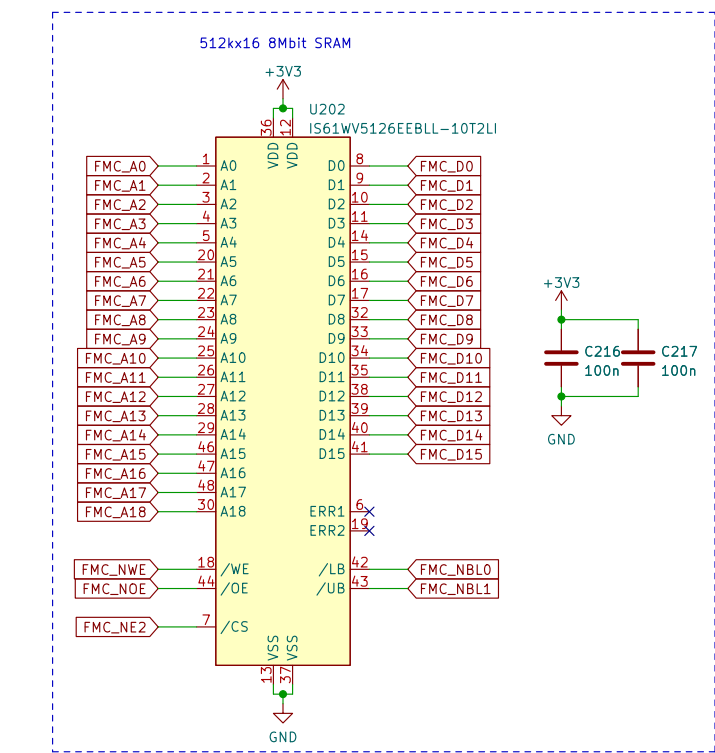
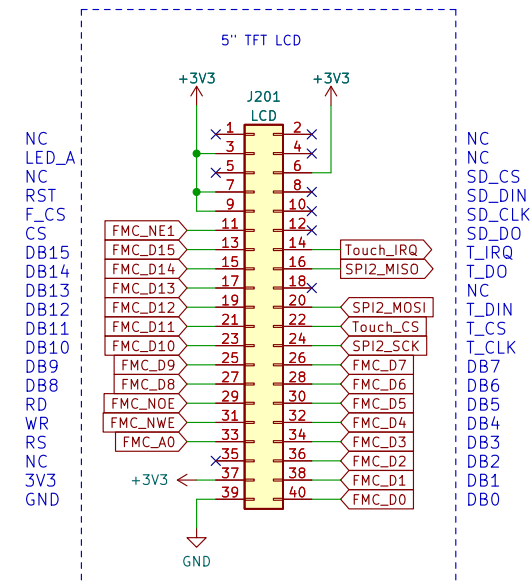
CAN1 = CAN Bus Backplane

UART7 = RS485 Backplane

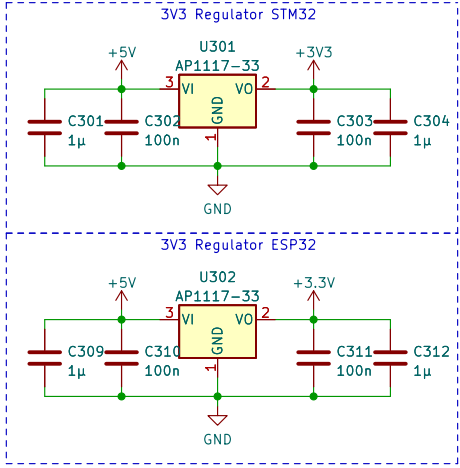
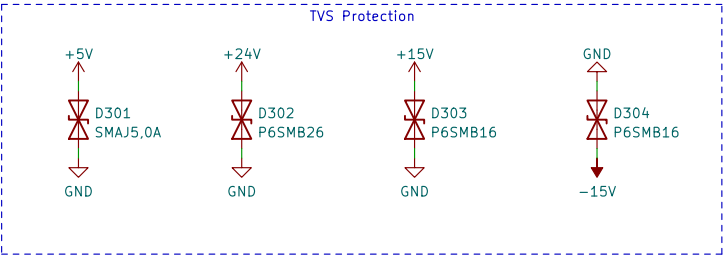
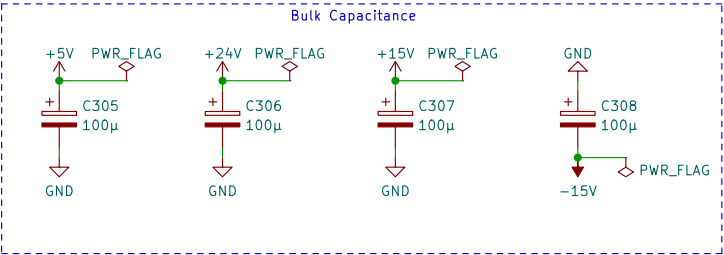
USB_FS = USB Fast Mode Device

- 6 General ChipSelects
- 6 General IOs

8 LEDs for Bus-Activity or other stuff



Id: 2/4



Synthron

Sheet: /Power/
File: power.kicad_sch

Title: Power Section

Size: A4 Date: 2022-11-19
KiCad E.D.A. kicad (6.0.7)

Rev: V3 R1.0
Id: 3/4

