

# Computer Networks: Data Link Layer



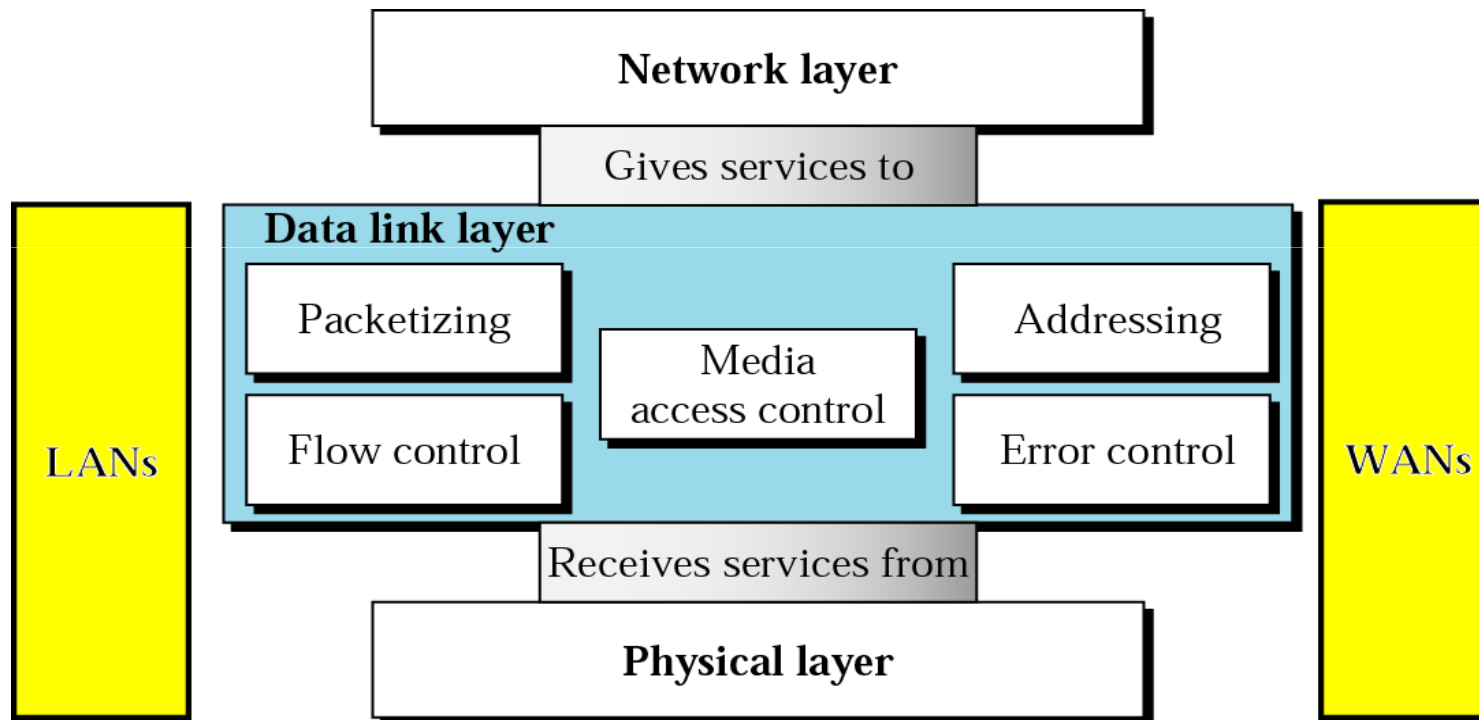
**By,**

Mr. Kumar Pudashine, (MEng, AIT)  
CCNP (Security), CEH, ITIL Expert, ISO 27001, CISA, AcitivIdentity Certified  
Information Technology Division,  
Agricultural Development Bank,  
Ramshahpath, Kathmandu  
Nepal

August 2016

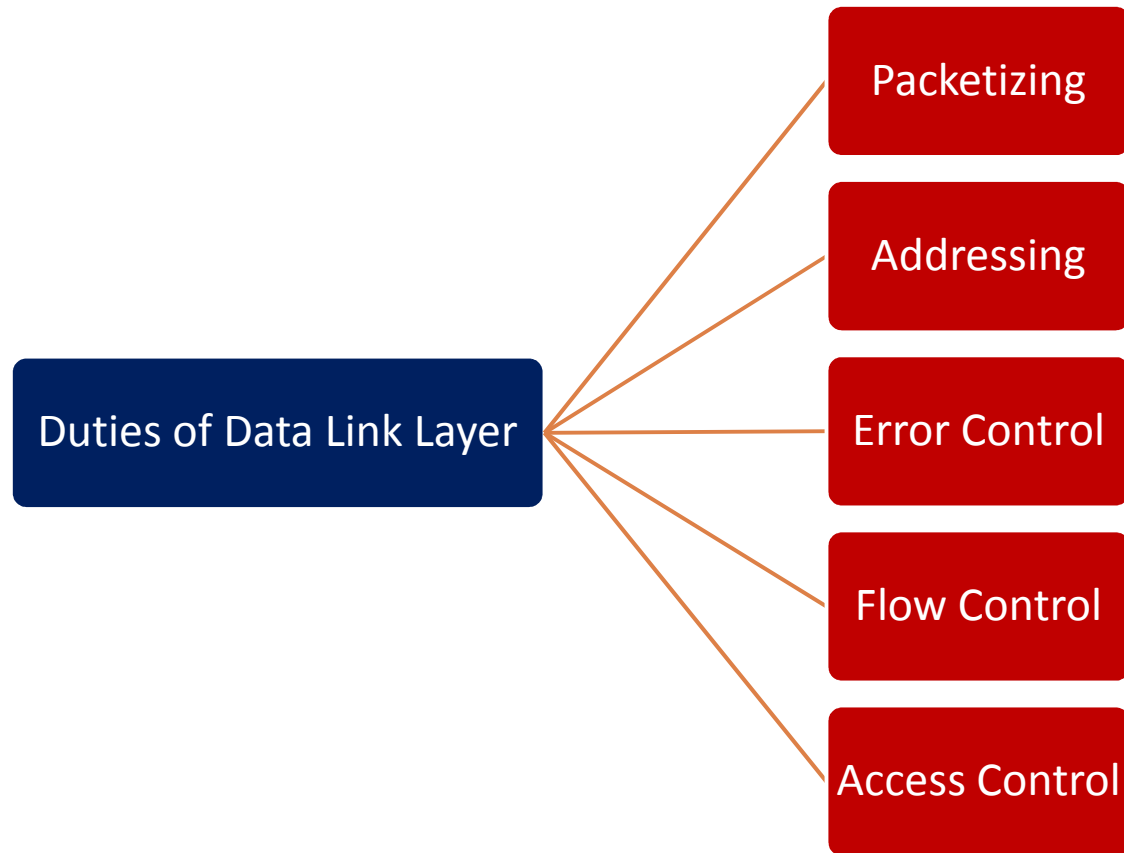
# OSI Layers : Position of Data Link Layer

2



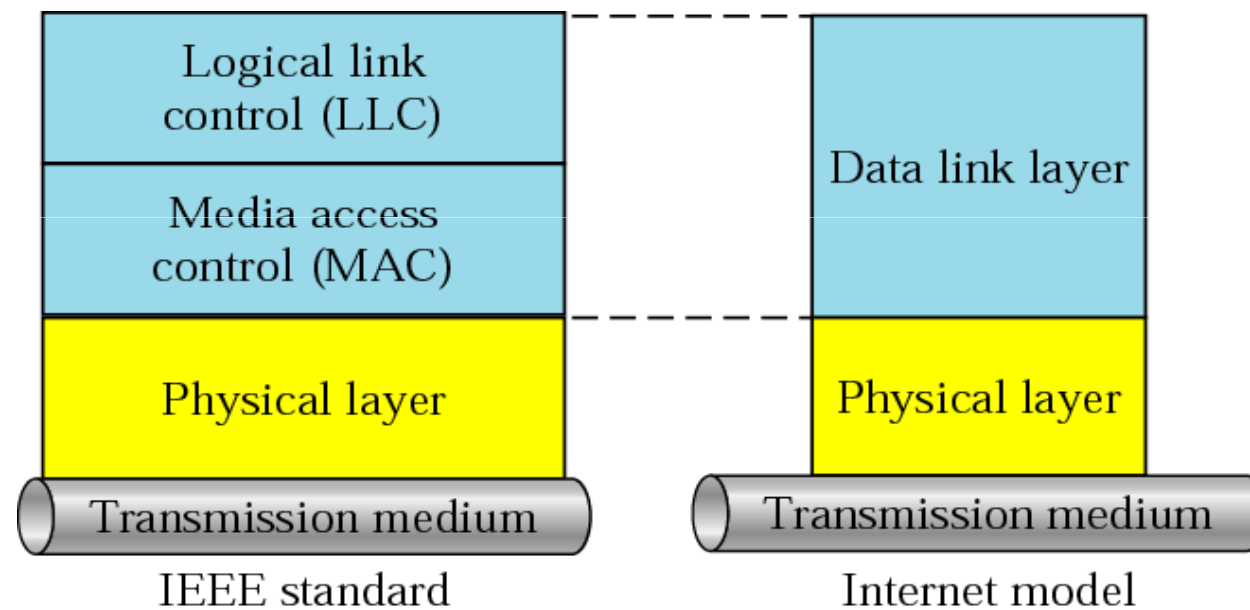
# Data Link Layer : Duties ??

3



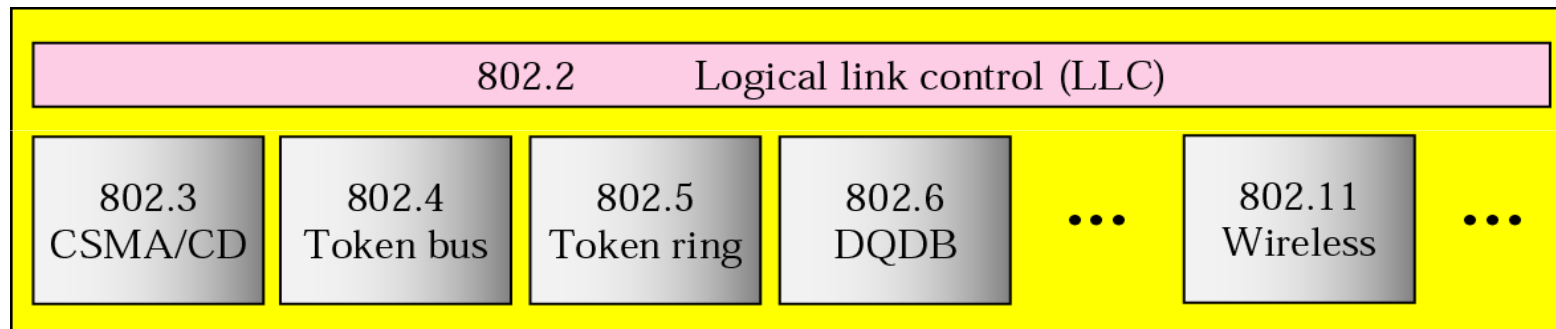
# Data Link Layer : Sub Layers

4



# Data Link Layer : IEEE Standards For LANs

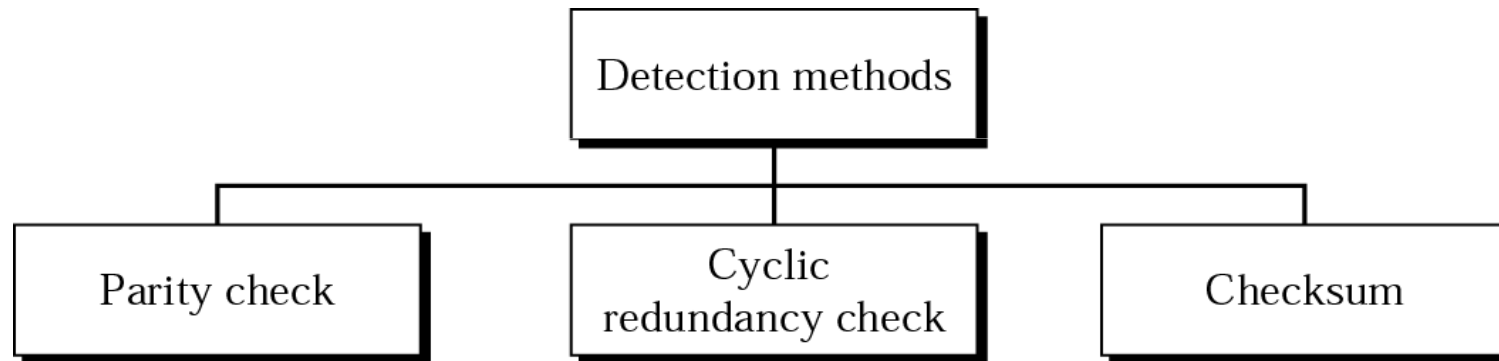
5



Project 802

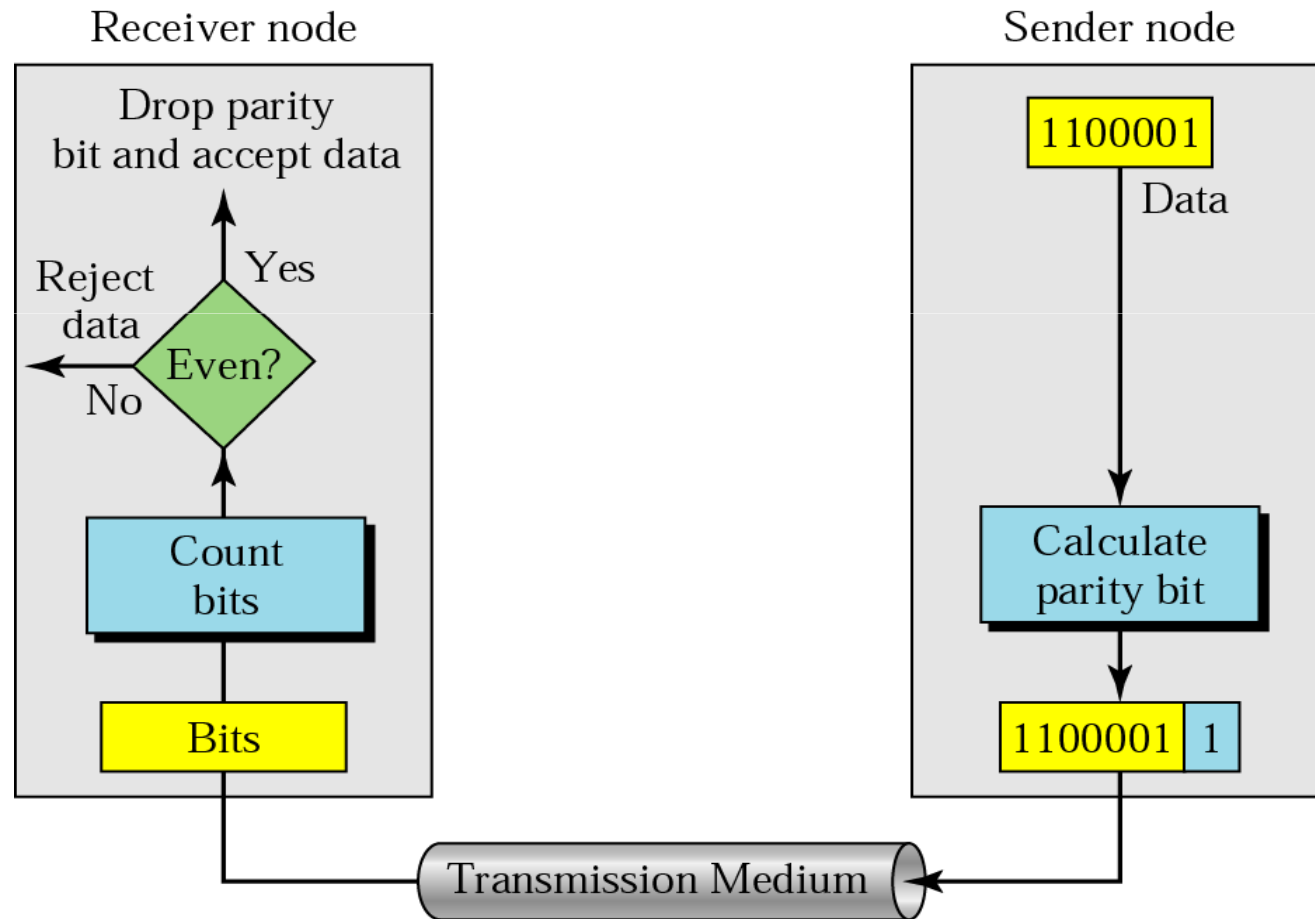
# Data Link Layer : Error Detection

6



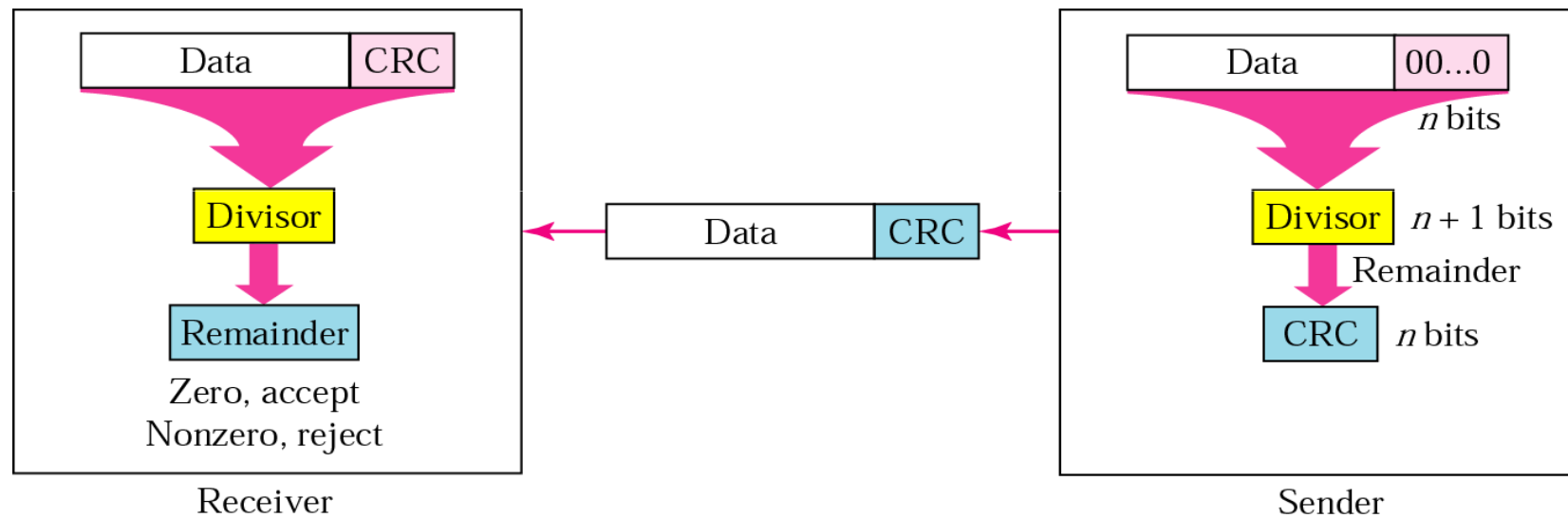
# Parity Check : Even Parity Concept

7



# CRC : Generator and Checker

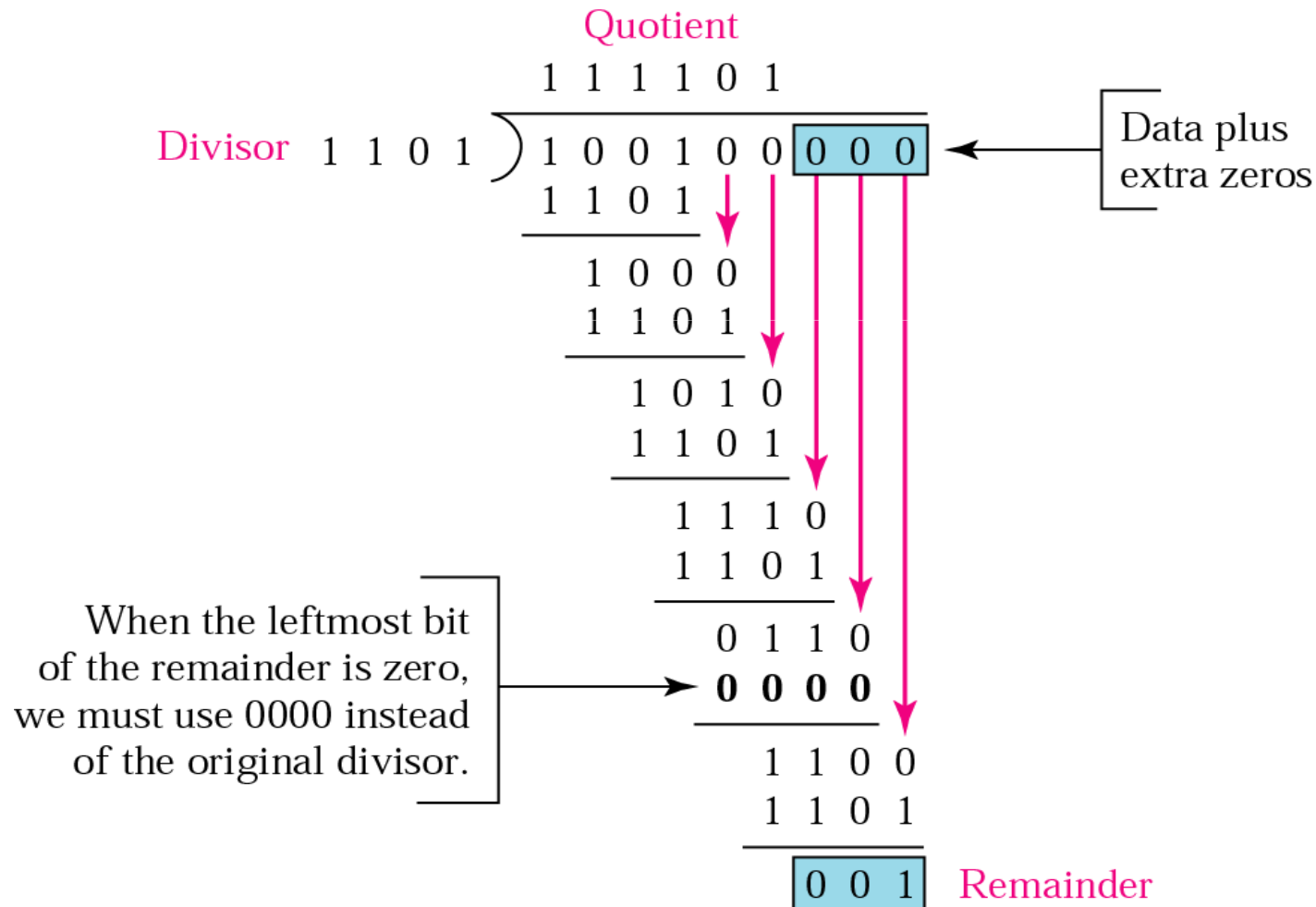
8





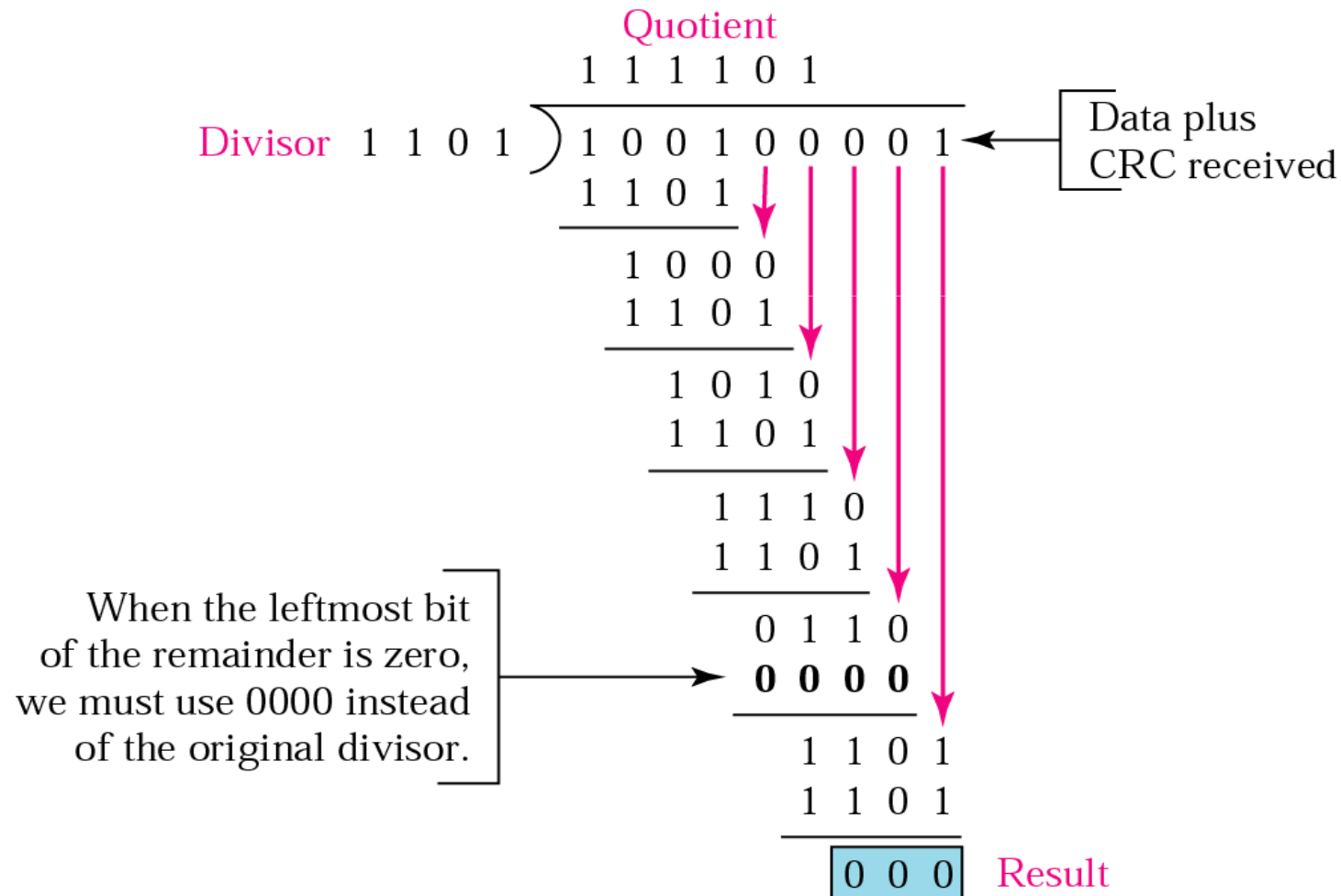
## CRC Generation : Sender Side

9



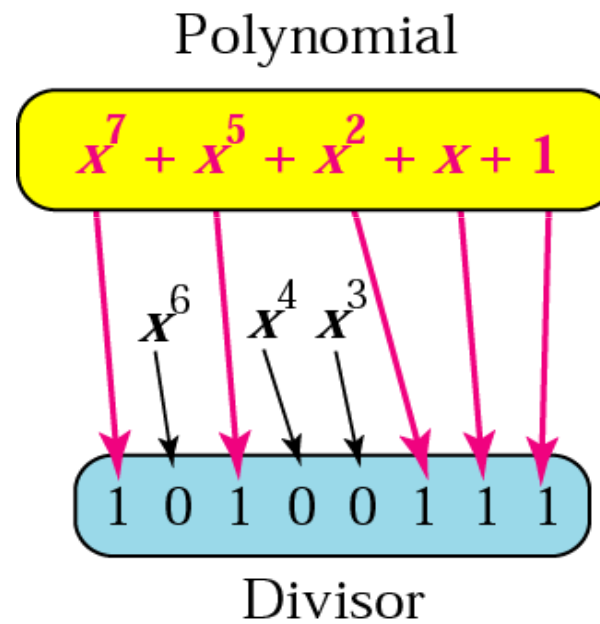
# CRC Check : Receiver Side

10



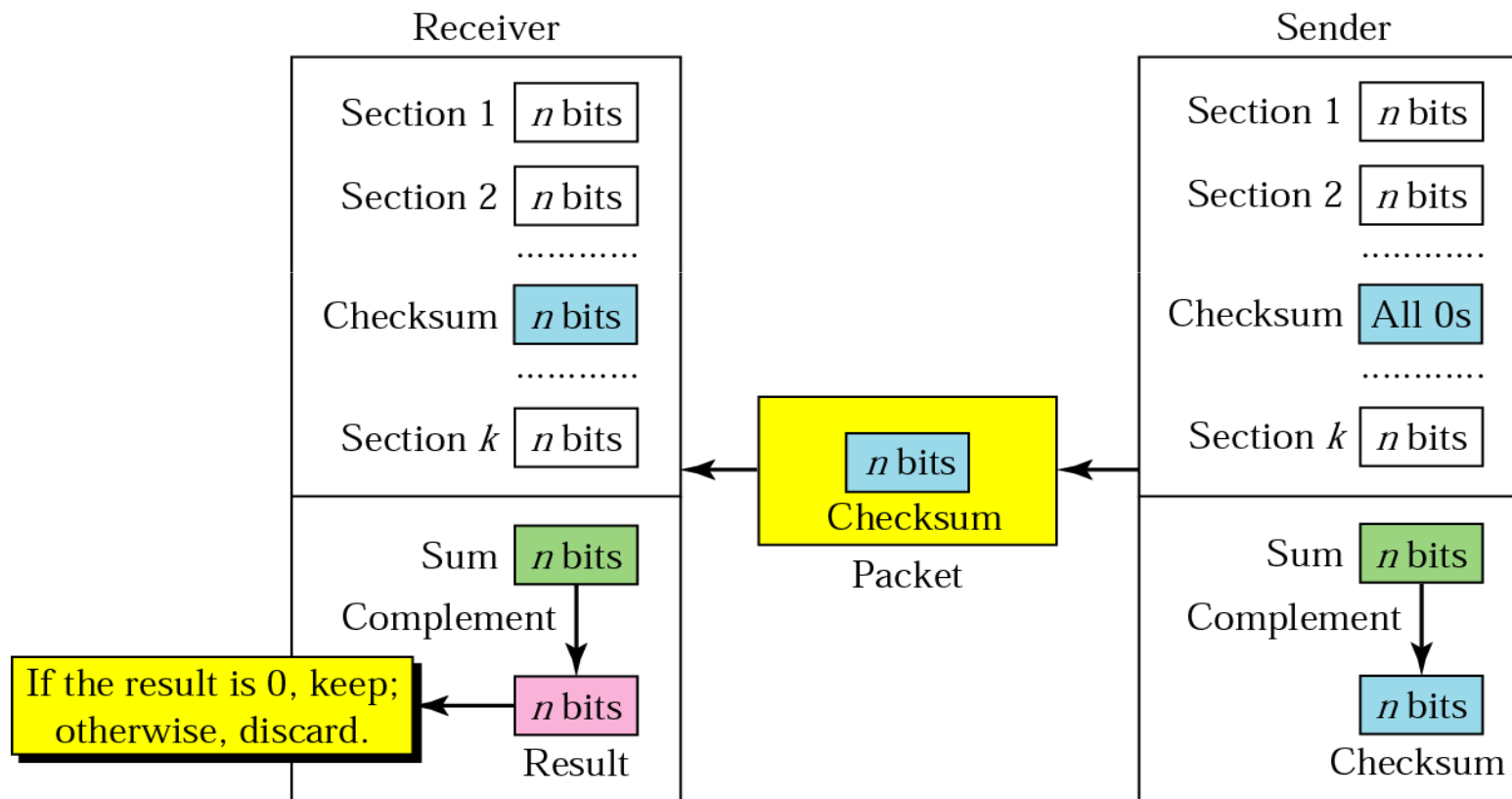
# CRC Polynomial

11



# Checksum

12



# Checksum Example: Sender Side

13

- Suppose the block of 16 bits is to be sent using a checksum of 8 bits. [ 10101001 00111001 ]
- Two 8 Bit Numbers are added.  
 $10101001 + 00111001 = 11100010$
- One's Complement of 11100010 = 00011101
- The Pattern Sent is  
10101001 00111001 00011101

# Checksum Example: Receiver Side

14

- The Received data along with checksum is added

10101001

00111001

00011101

-----

11111111

- Compute One's Complement of 11111111 = 00000000
- No Error in Transmission.

# Error Correction

15

- Error Correction By Retransmission
  - ✓ Stop AND Wait ARQ
  - ✓ Go-Back-N ARQ
  - ✓ Selective Repeat ARQ
  
- ARQ => Automatic Repeat Request
  
- Error Correction By Forward Error Control
  - ✓ Hamming Code

# Hamming Code: Data and Redundancy Bits

16

| Number of Data Bits (m) | Number of Redundancy Bits (r) | Total Bits (m + r) |
|-------------------------|-------------------------------|--------------------|
| 1                       | 2                             | 3                  |
| 2                       | 3                             | 5                  |
| 3                       | 3                             | 6                  |
| 4                       | 3                             | 7                  |
| 5                       | 4                             | 9                  |
| 6                       | 4                             | 10                 |
| 7                       | 4                             | 11                 |

$$2^r \geq m+r+1$$



# Hamming Code: Position of Redundancy Bits

17

|    |    |   |       |   |   |   |       |   |       |       |
|----|----|---|-------|---|---|---|-------|---|-------|-------|
| 11 | 10 | 9 | 8     | 7 | 6 | 5 | 4     | 3 | 2     | 1     |
| d  | d  | d | $r_8$ | d | d | d | $r_4$ | d | $r_2$ | $r_1$ |

# Hamming Code: Redundancy Bits

18

$r_1$  will take care of these bits.

|    |   |   |       |   |   |   |       |   |       |       |
|----|---|---|-------|---|---|---|-------|---|-------|-------|
| 11 |   | 9 |       | 7 |   | 5 |       | 3 |       | 1     |
| d  | d | d | $r_8$ | d | d | d | $r_4$ | d | $r_2$ | $r_1$ |

$r_2$  will take care of these bits.

|    |    |   |       |   |   |   |       |   |       |       |
|----|----|---|-------|---|---|---|-------|---|-------|-------|
| 11 | 10 |   |       | 7 | 6 |   |       | 3 | 2     |       |
| d  | d  | d | $r_8$ | d | d | d | $r_4$ | d | $r_2$ | $r_1$ |

$r_4$  will take care of these bits.

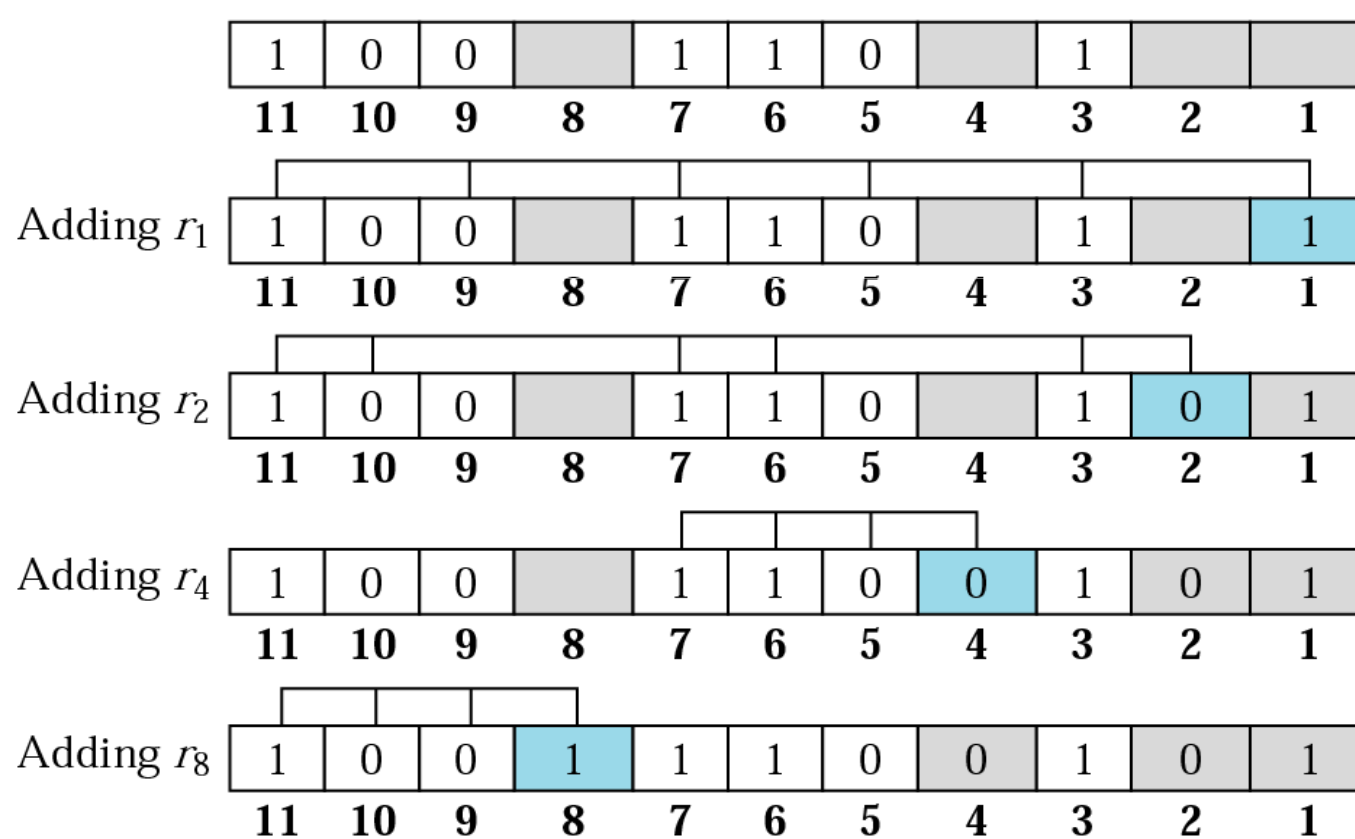
|   |   |   |       |   |   |   |       |   |       |       |
|---|---|---|-------|---|---|---|-------|---|-------|-------|
|   |   |   |       | 7 | 6 | 5 | 4     |   |       |       |
| d | d | d | $r_8$ | d | d | d | $r_4$ | d | $r_2$ | $r_1$ |

$r_8$  will take care of these bits.

|    |    |   |       |   |   |   |       |   |       |       |
|----|----|---|-------|---|---|---|-------|---|-------|-------|
| 11 | 10 | 9 | 8     |   |   |   |       |   |       |       |
| d  | d  | d | $r_8$ | d | d | d | $r_4$ | d | $r_2$ | $r_1$ |

# Hamming Code: Example of Redundancy Bit Calculation

19

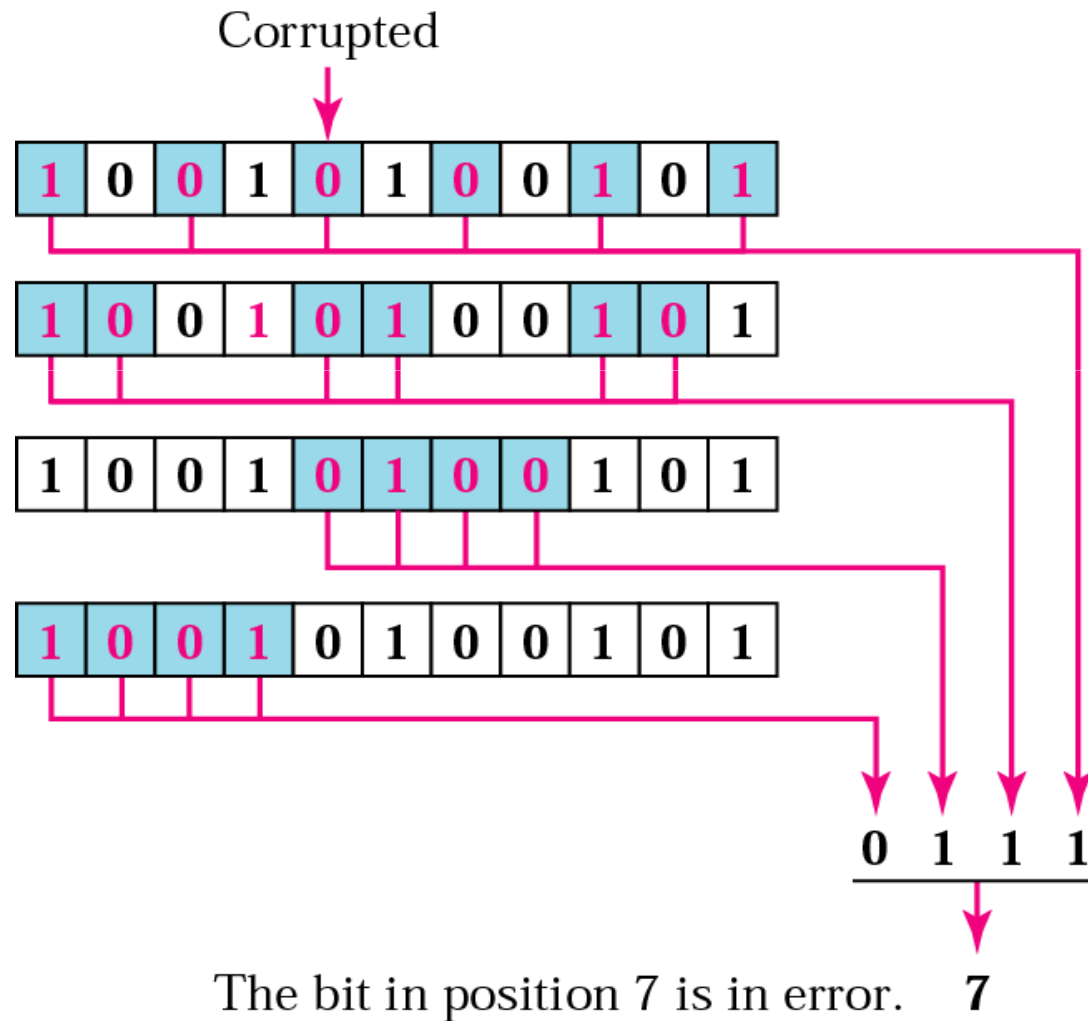


Data:  
1001101

Code:  
10011100101

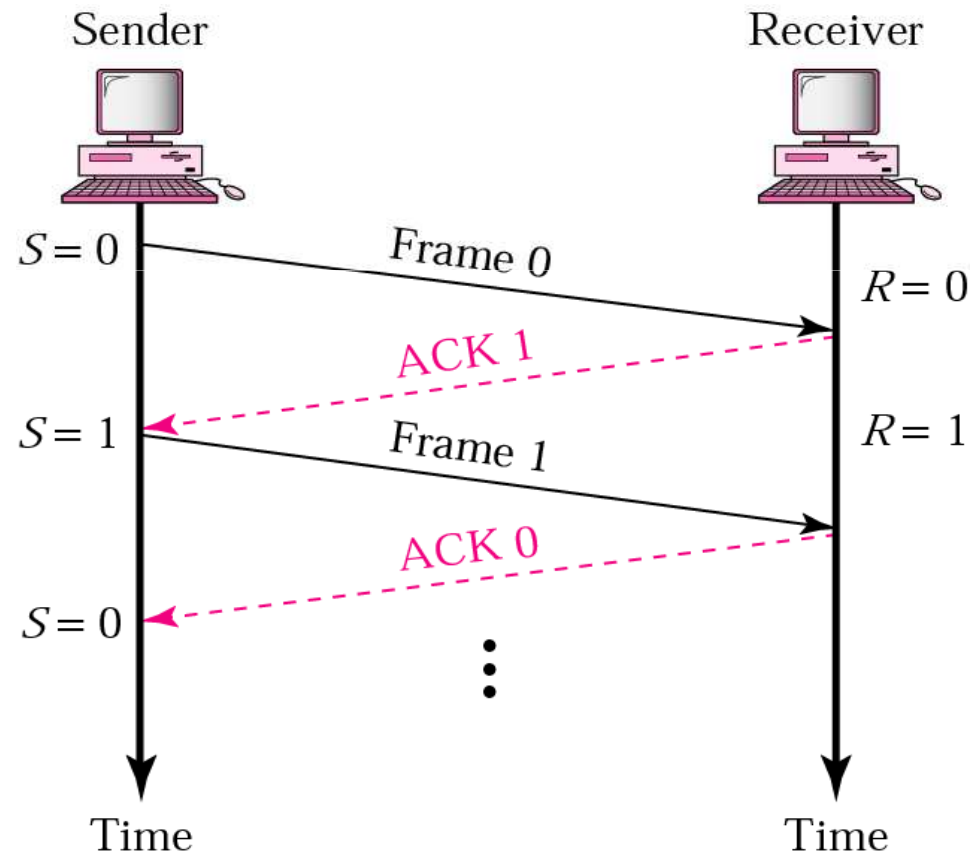
# Hamming Code: Error Detection

20



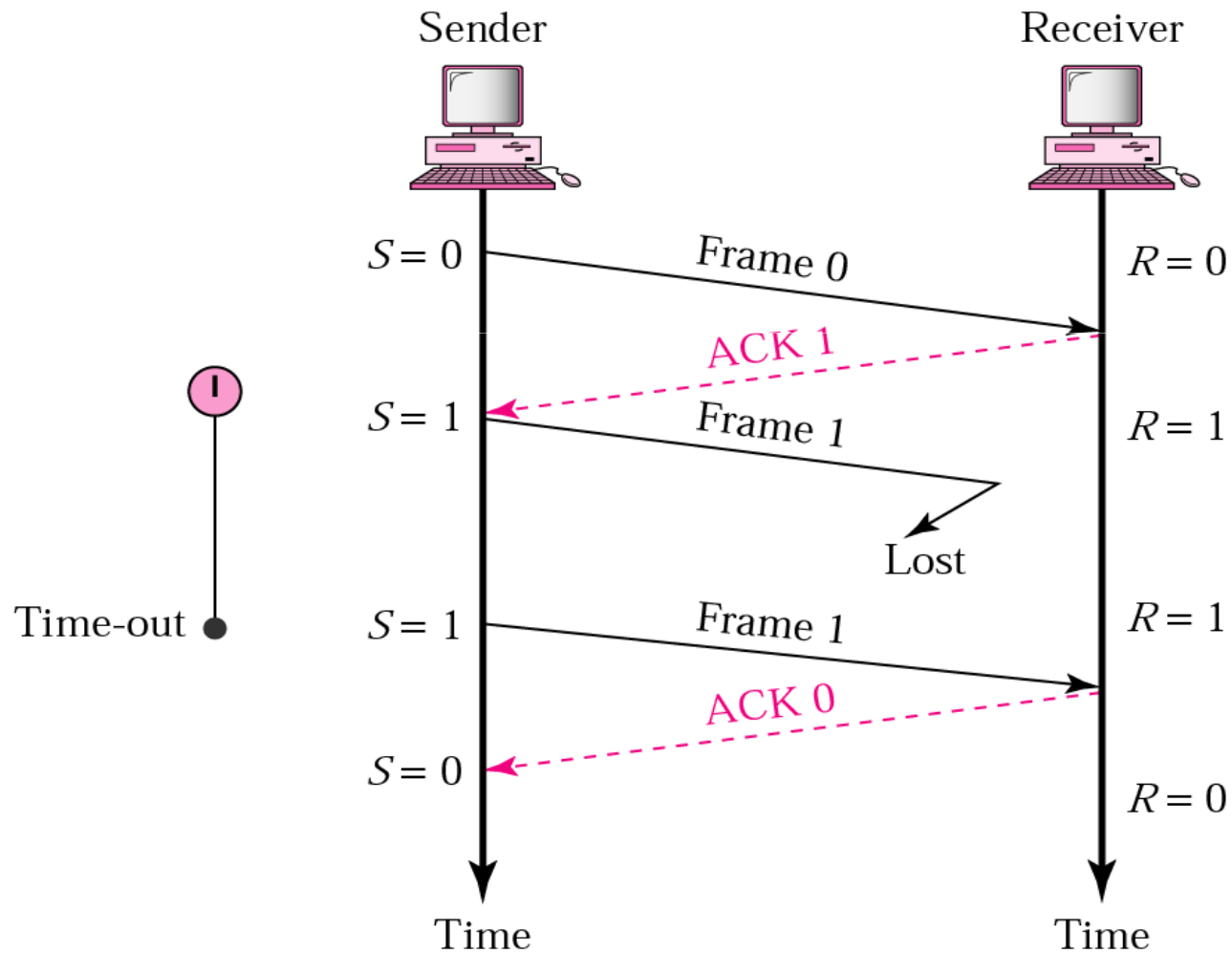
# Stop and Wait ARQ : Normal Operation

21



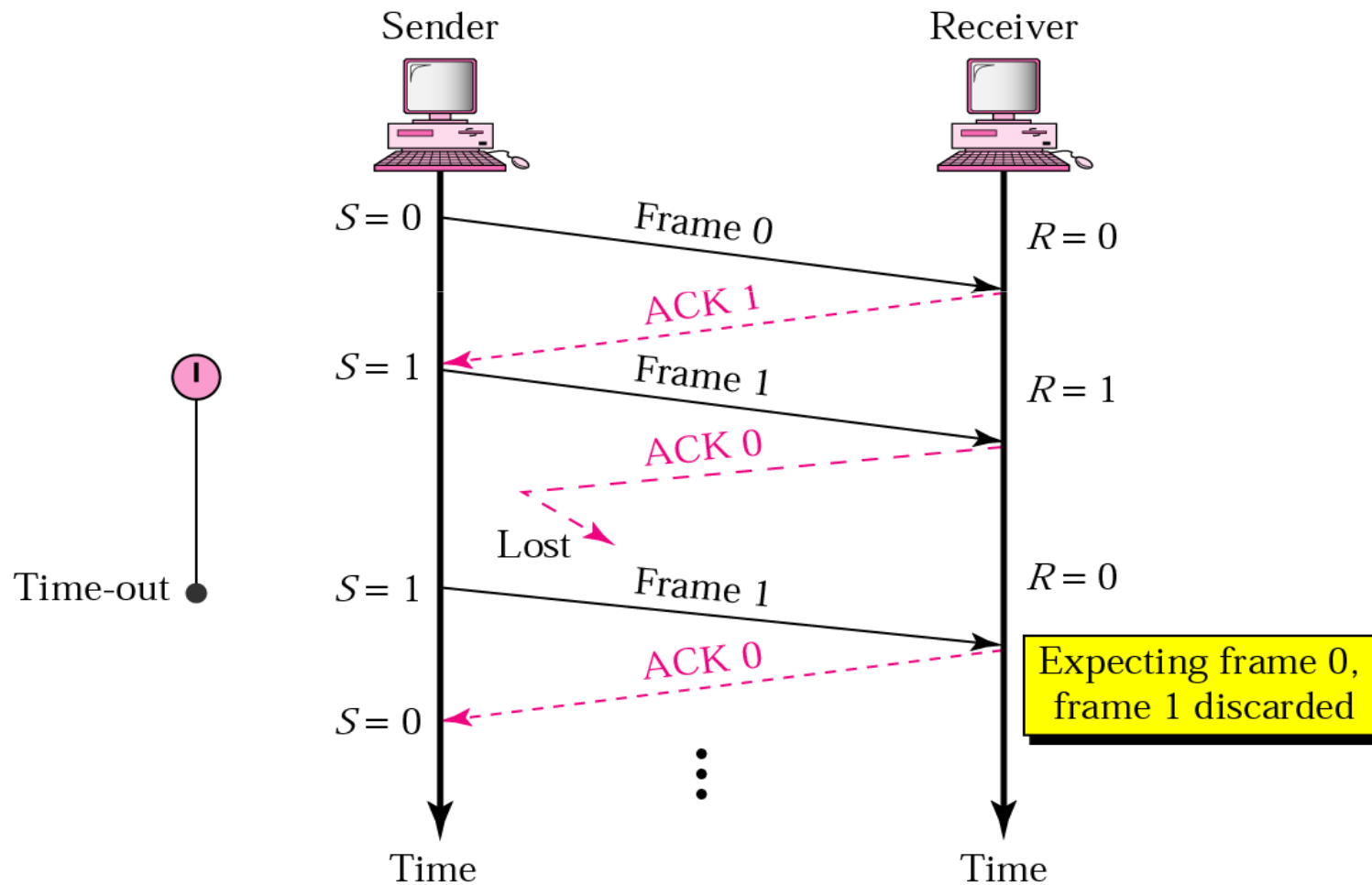
# Stop and Wait ARQ : Lost Frame

22



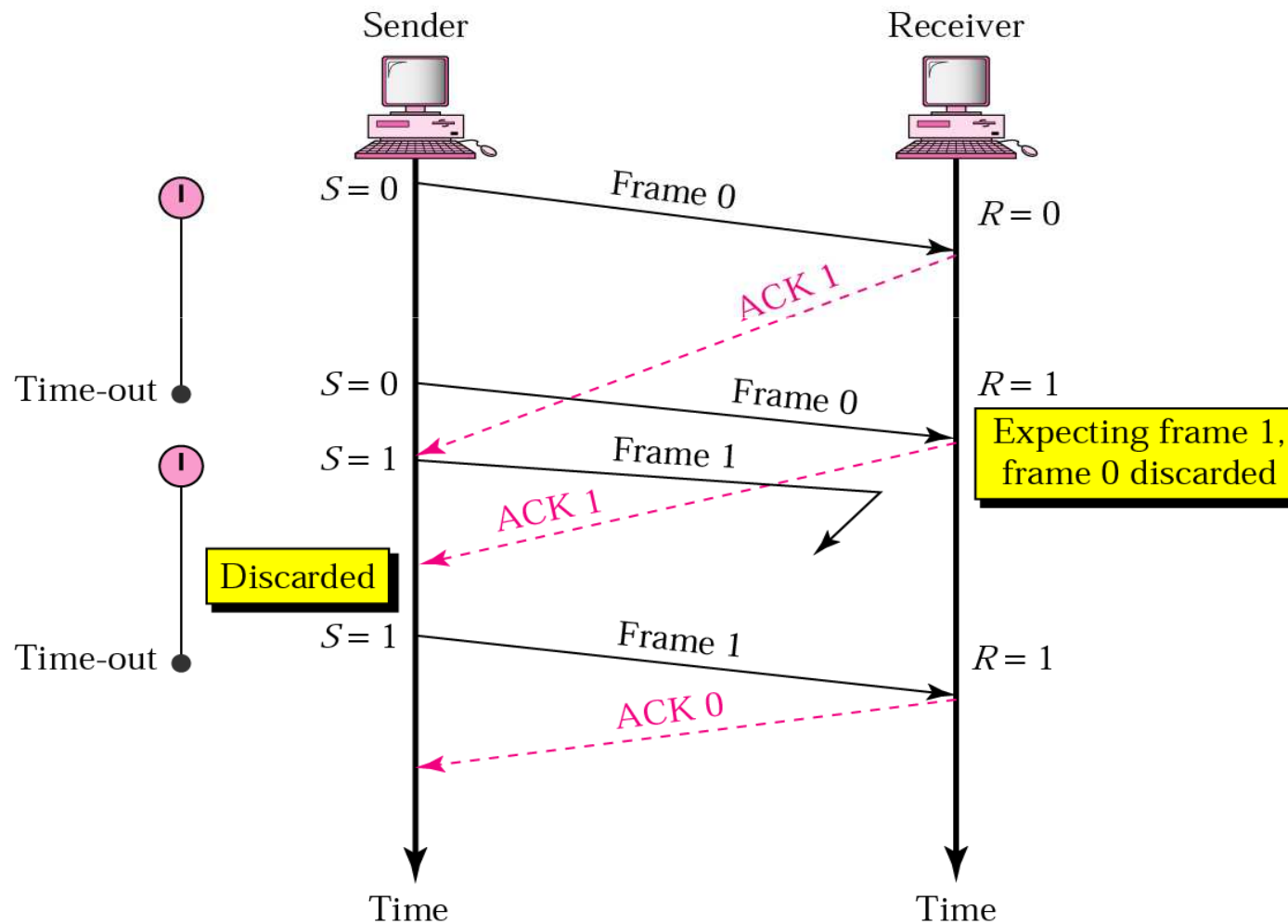
# Stop and Wait ARQ : Lost ACK

23



# Stop and Wait ARQ : Delayed ACK

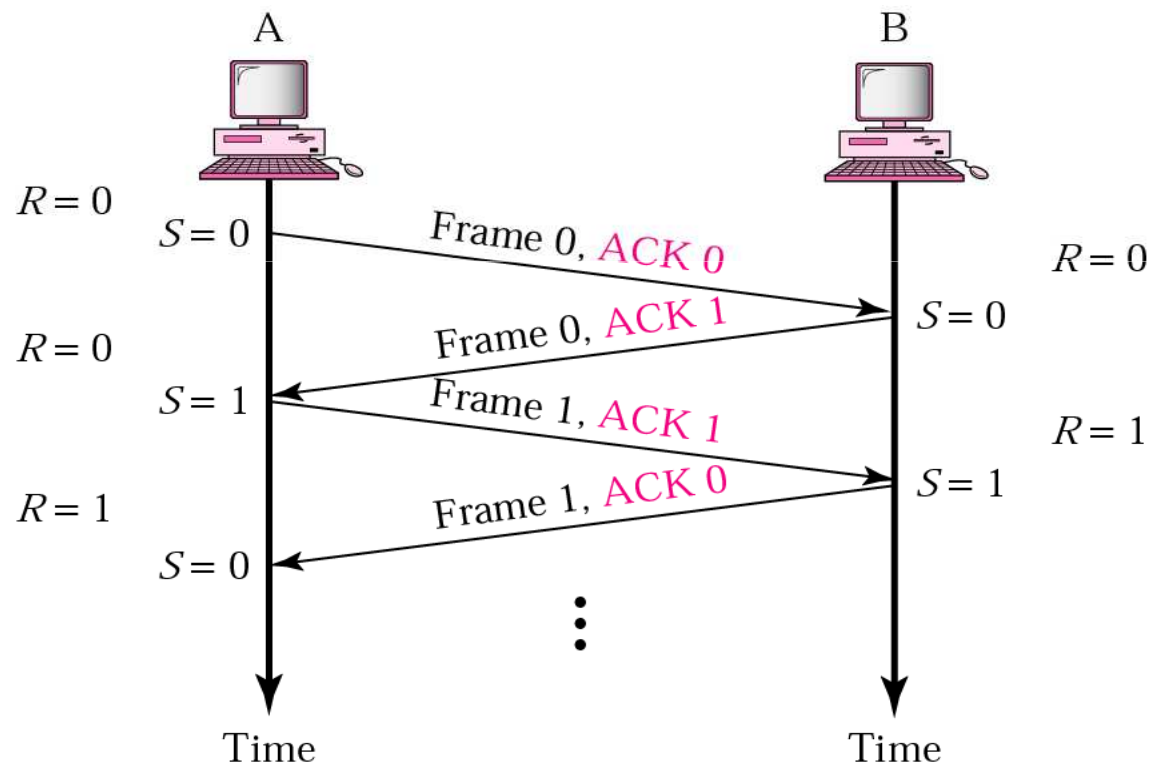
24





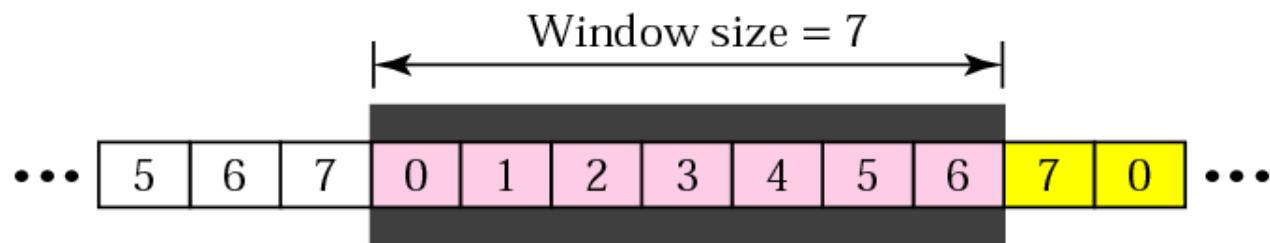
# Piggybacking : Bidirectional Transmission (Frame +ACK)

25

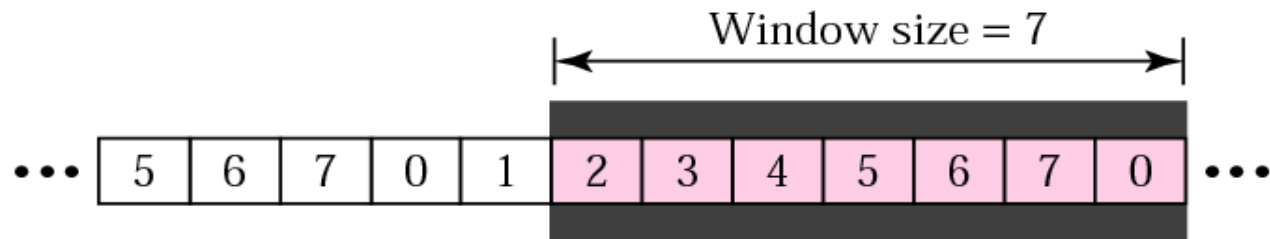


# Go-Back-N ARQ : Sender Sliding Window

26



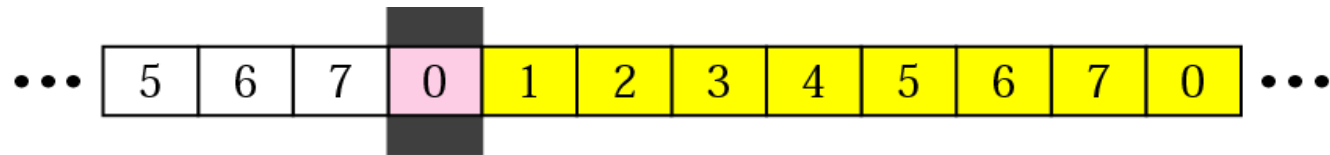
a. Before sliding



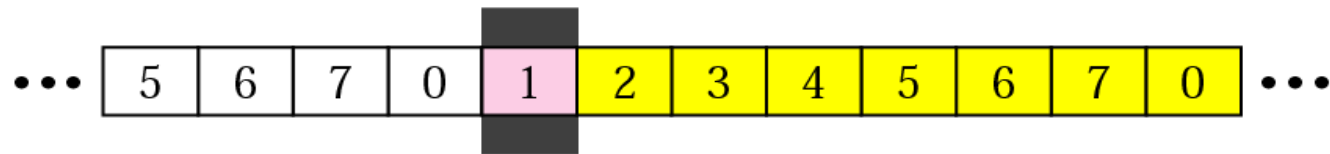
b. After sliding two frames

# Go-Back-N ARQ : Receiver Sliding Window

27



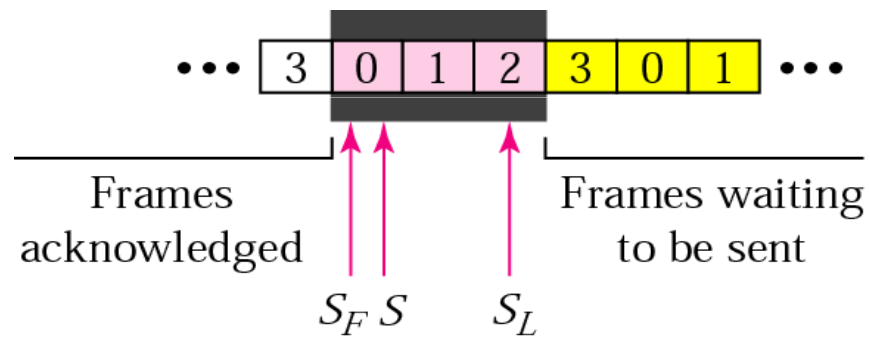
a. Before sliding



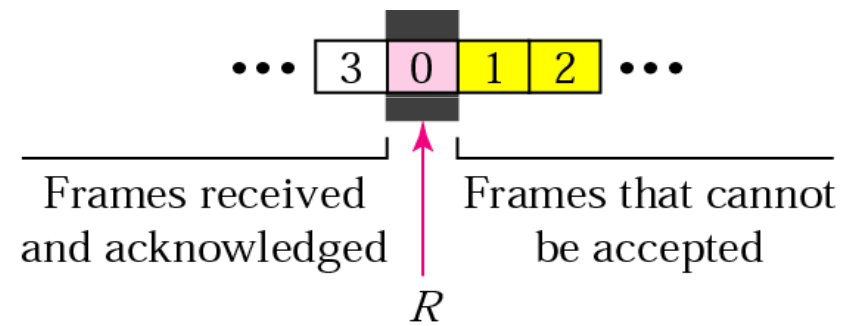
b. After sliding

# Go-Back-N ARQ : Control Variables

28



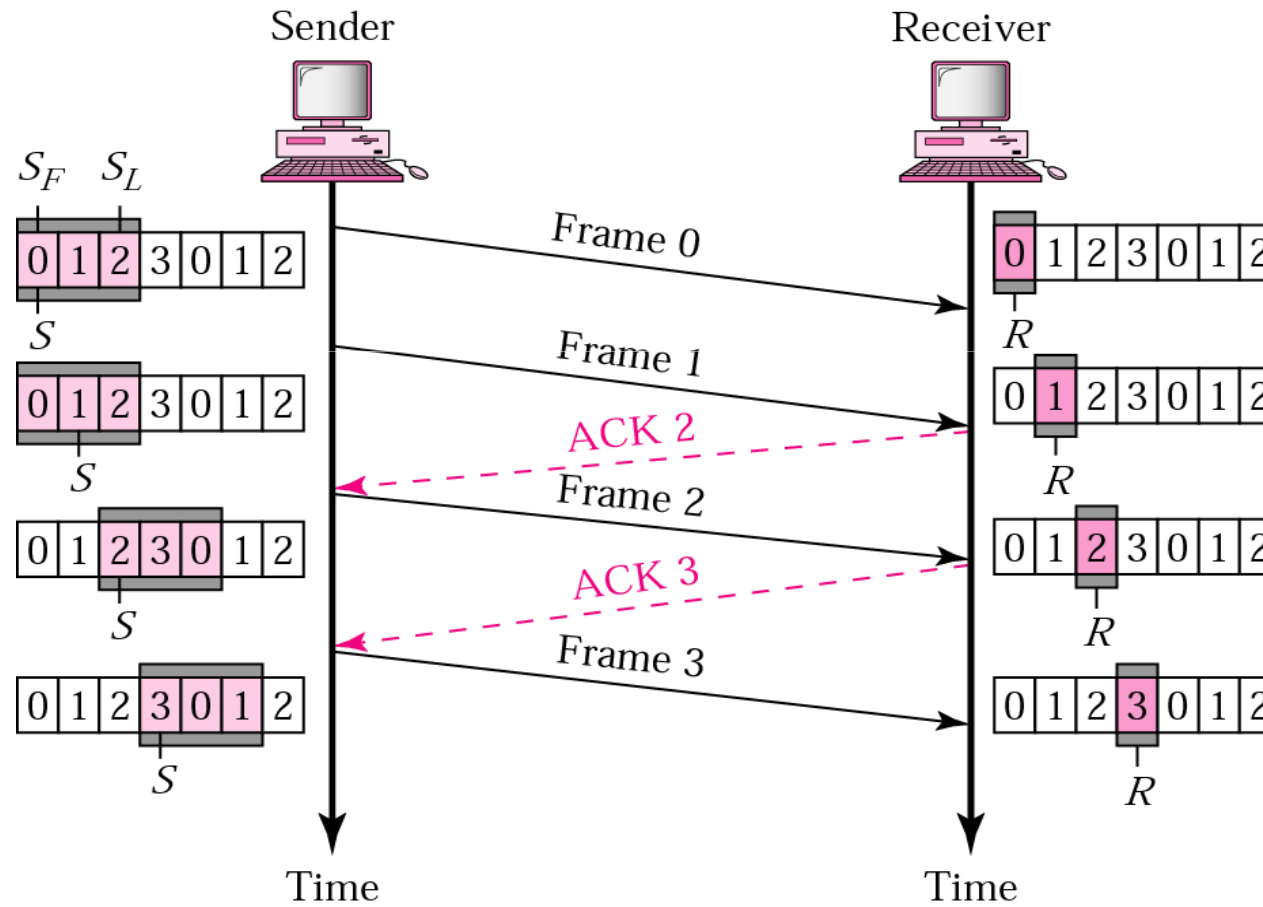
a. Sender window



b. Receiver window

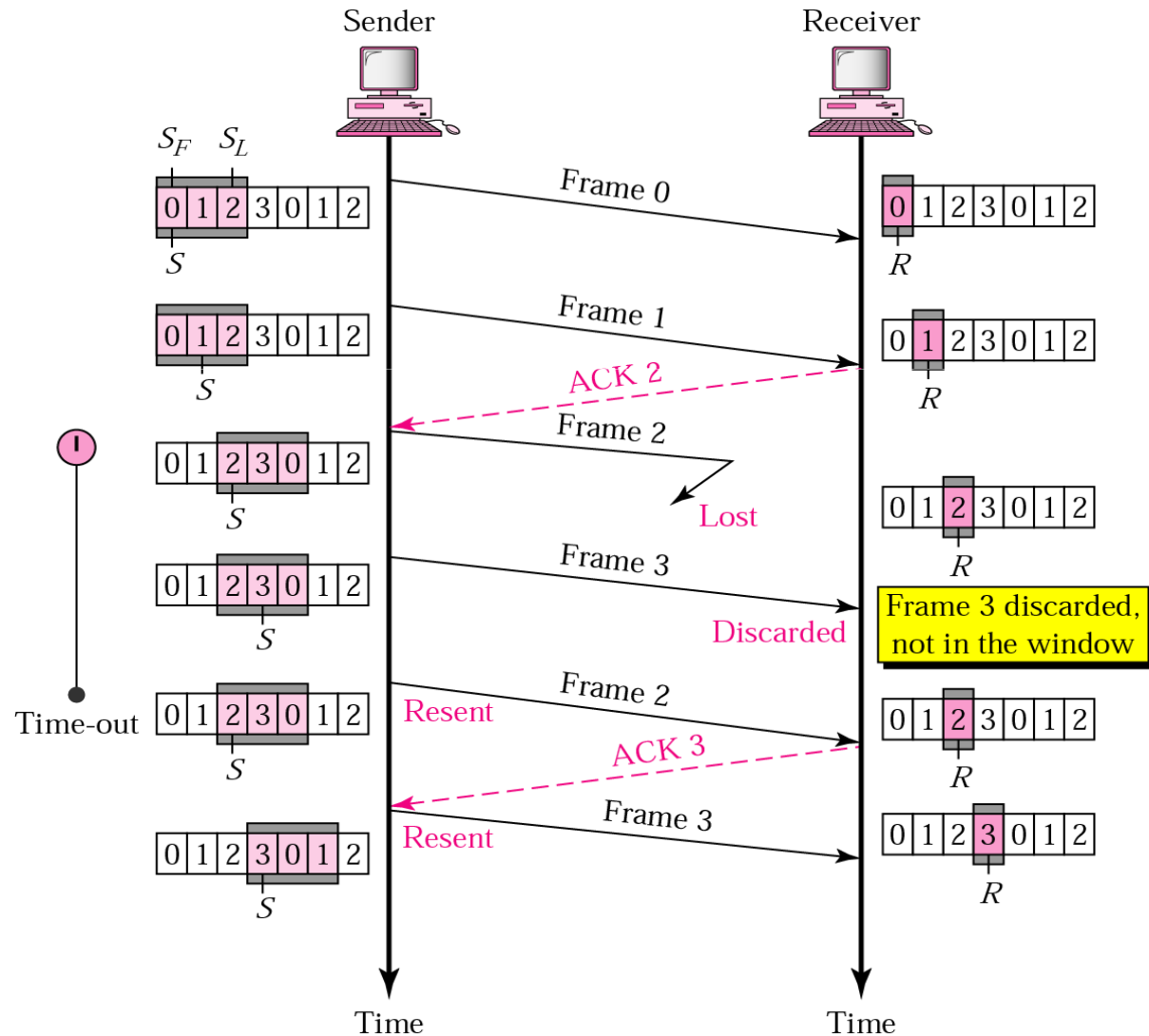
# Go-Back-N ARQ : Normal Operation

29



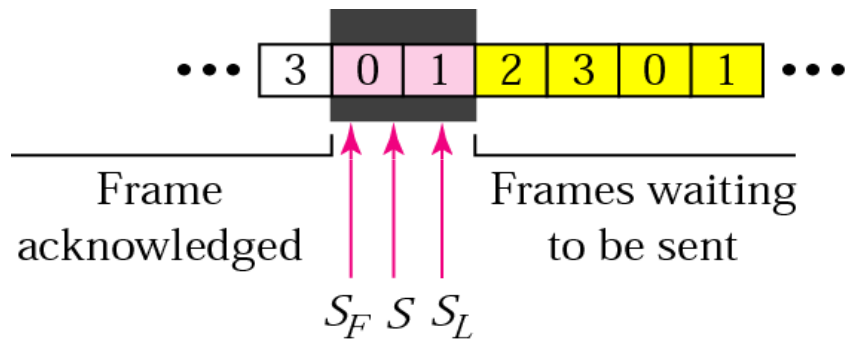
# Go-Back-N ARQ : Lost Frame

30

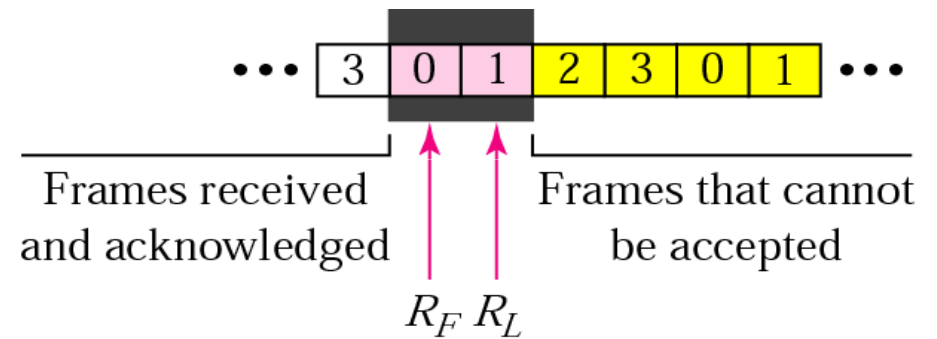


# Selective Repeat ARQ : Sender and Receiving Windows

31



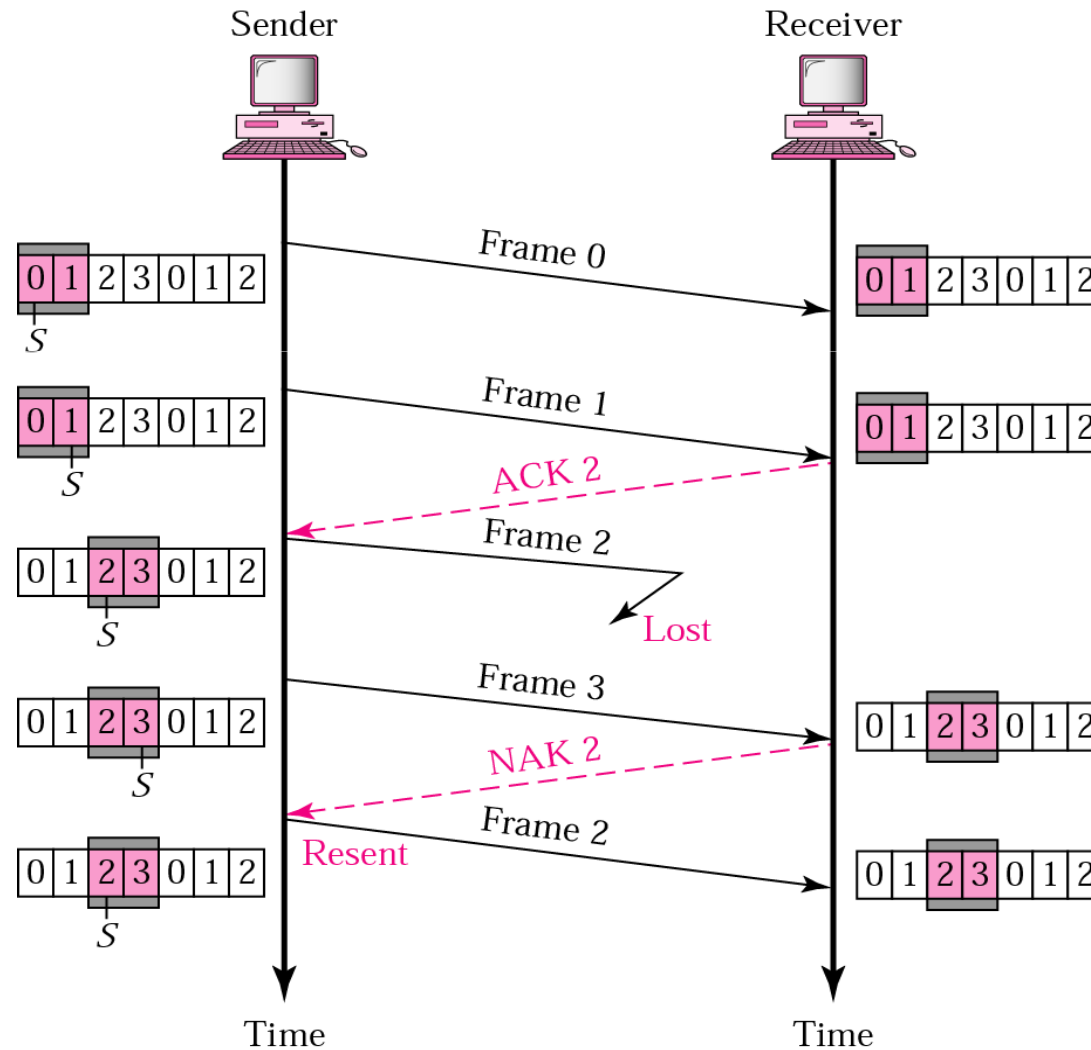
a. Sender window



b. Receiver window

# Selective Repeat ARQ : Lost Frame

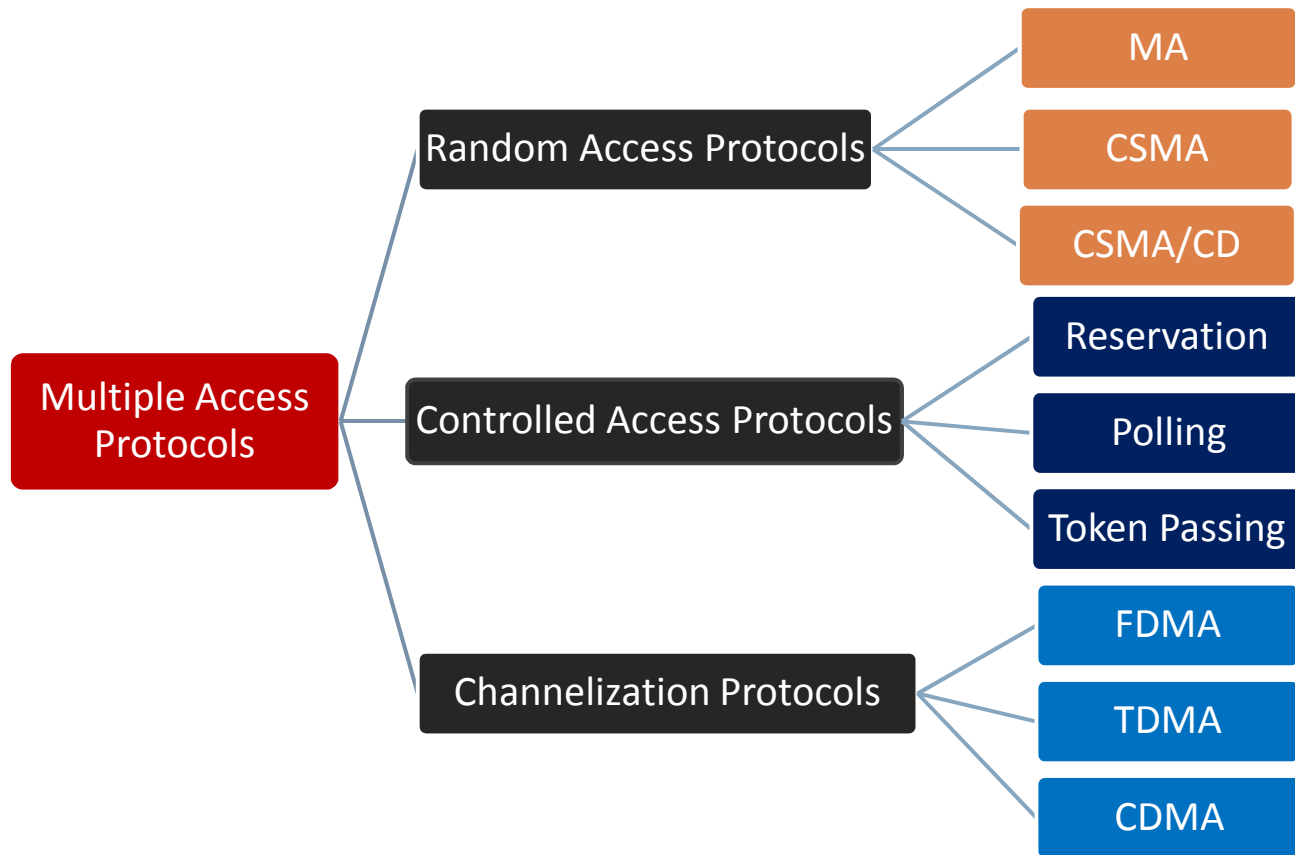
32





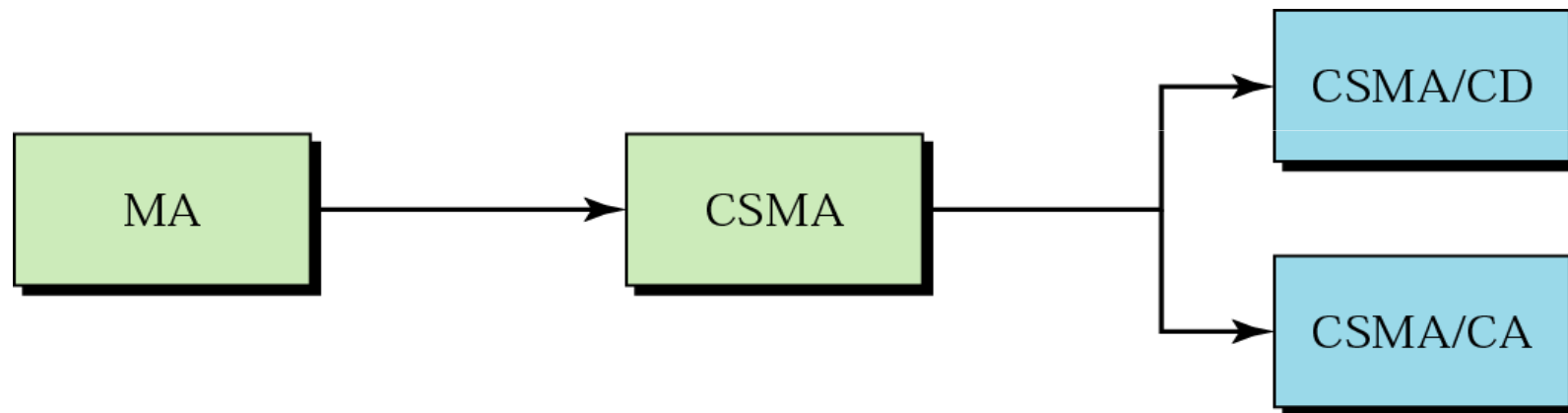
# Multiple Access Protocols

33



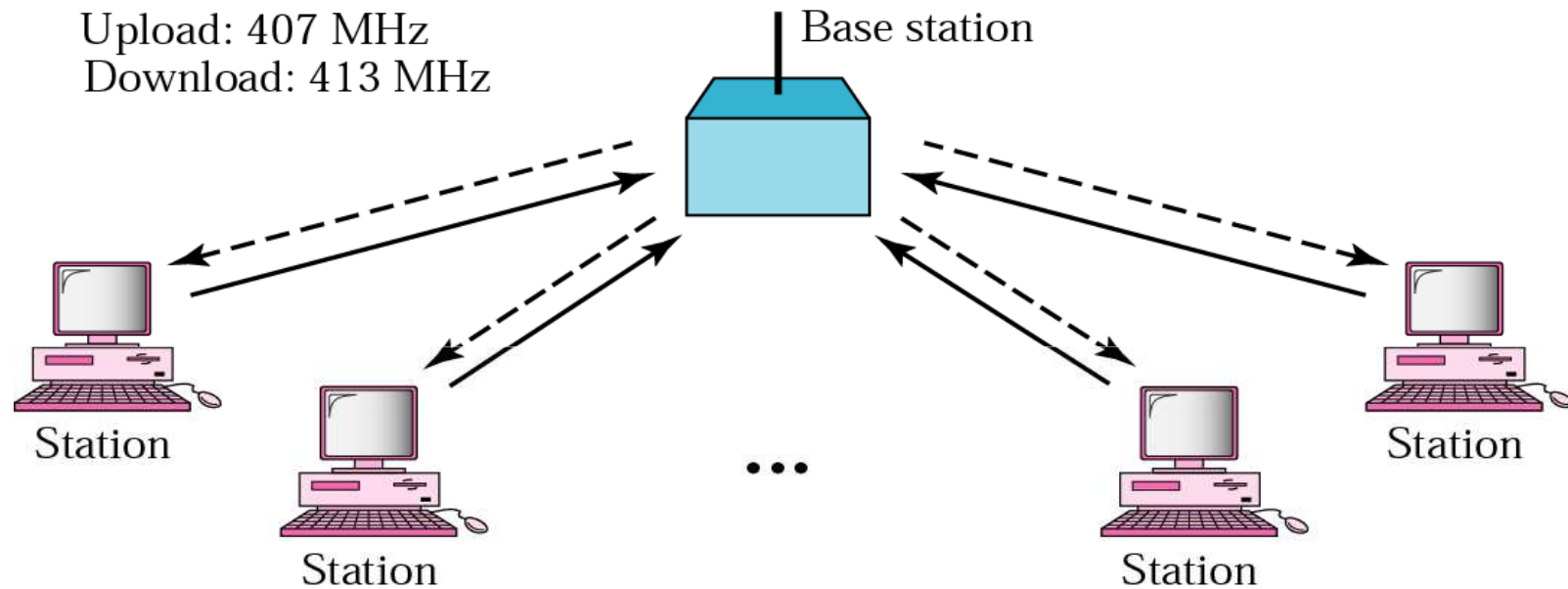
# Evolution of Random Access Protocols

34



# Multiple Access : ALOHA

35



## 1. Pure ALOHA

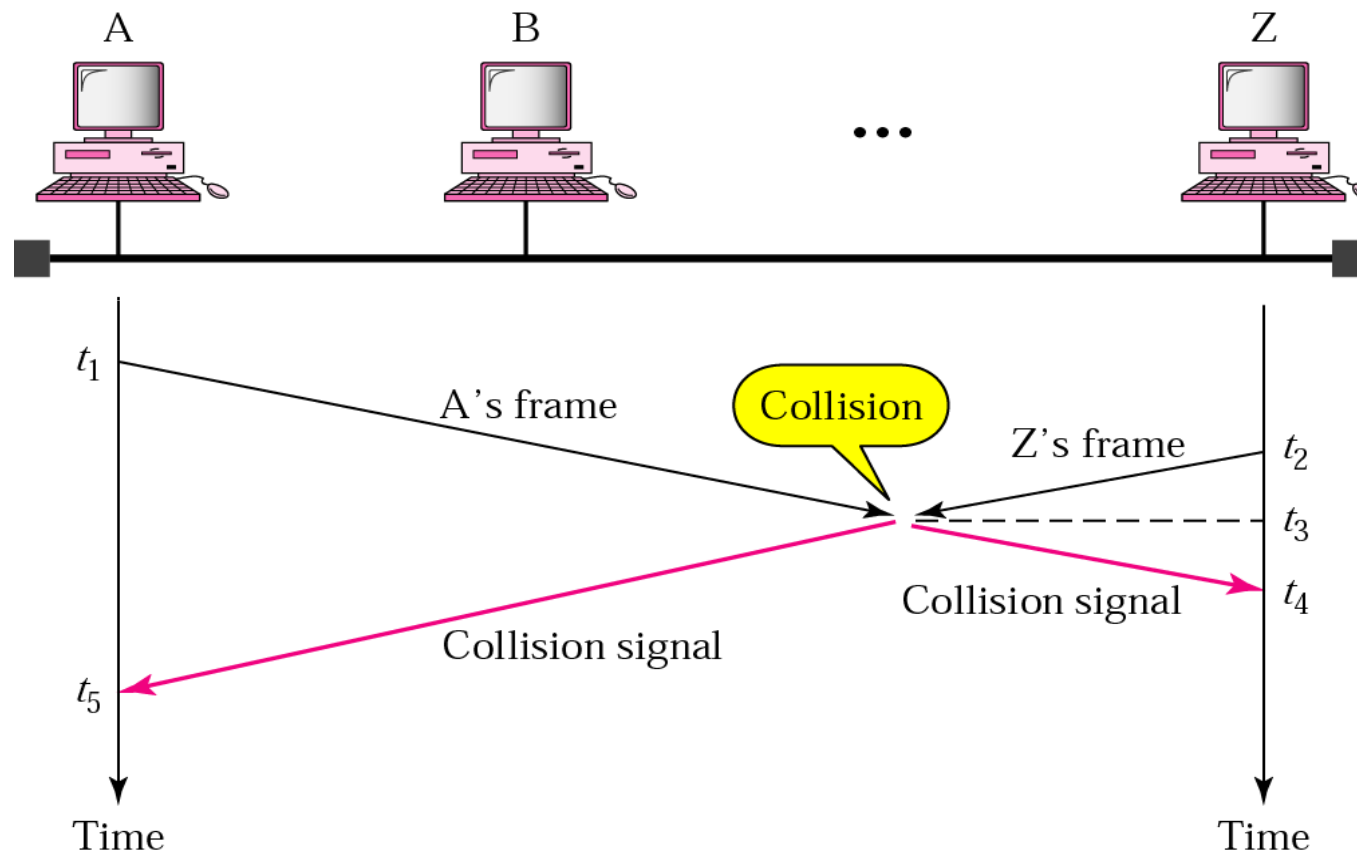
If you have data to send, send the data. If message collides with other transmission try resending later.

## 2. Slotted ALOHA

Introduced discrete timeslots and increased the maximum throughput.

# Collision in CSMA

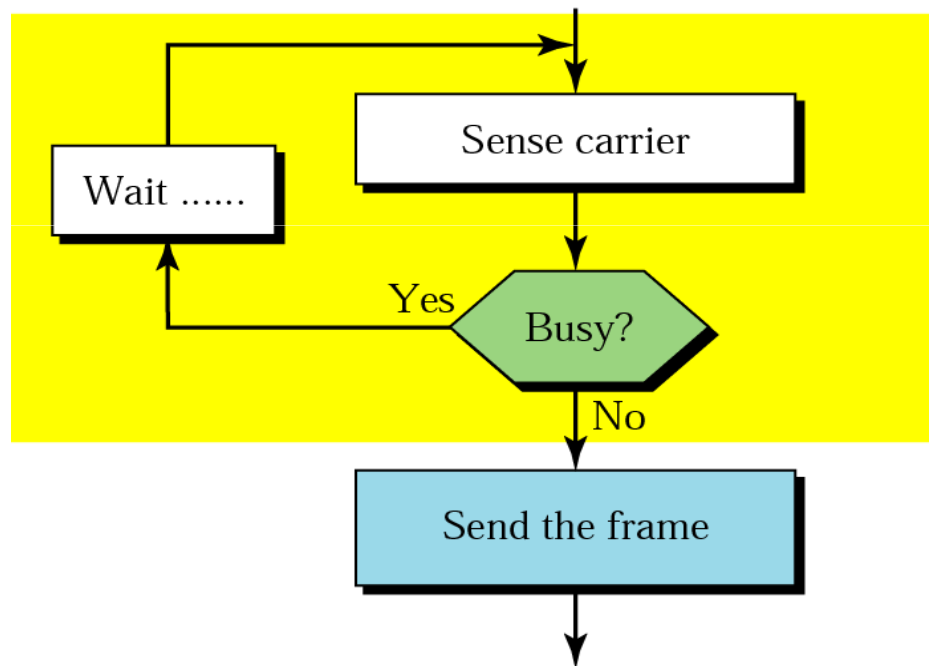
36



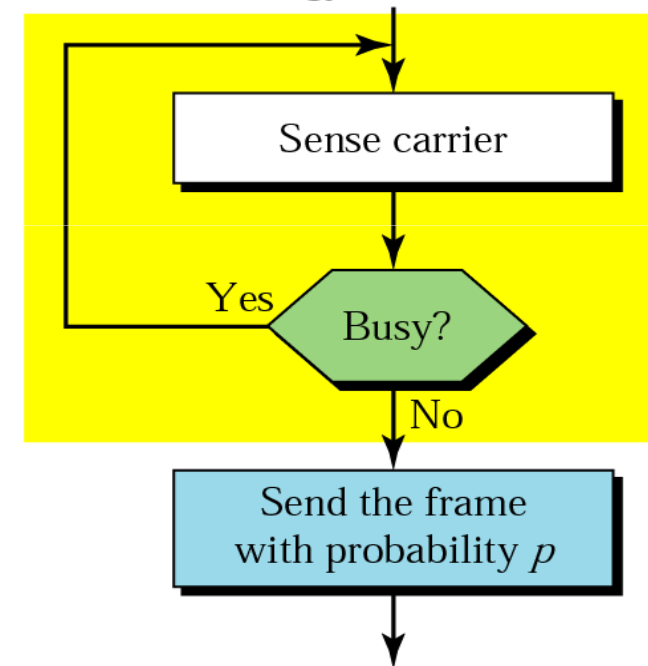
# Carrier Sense: Strategies

37

Nonpersistent strategy

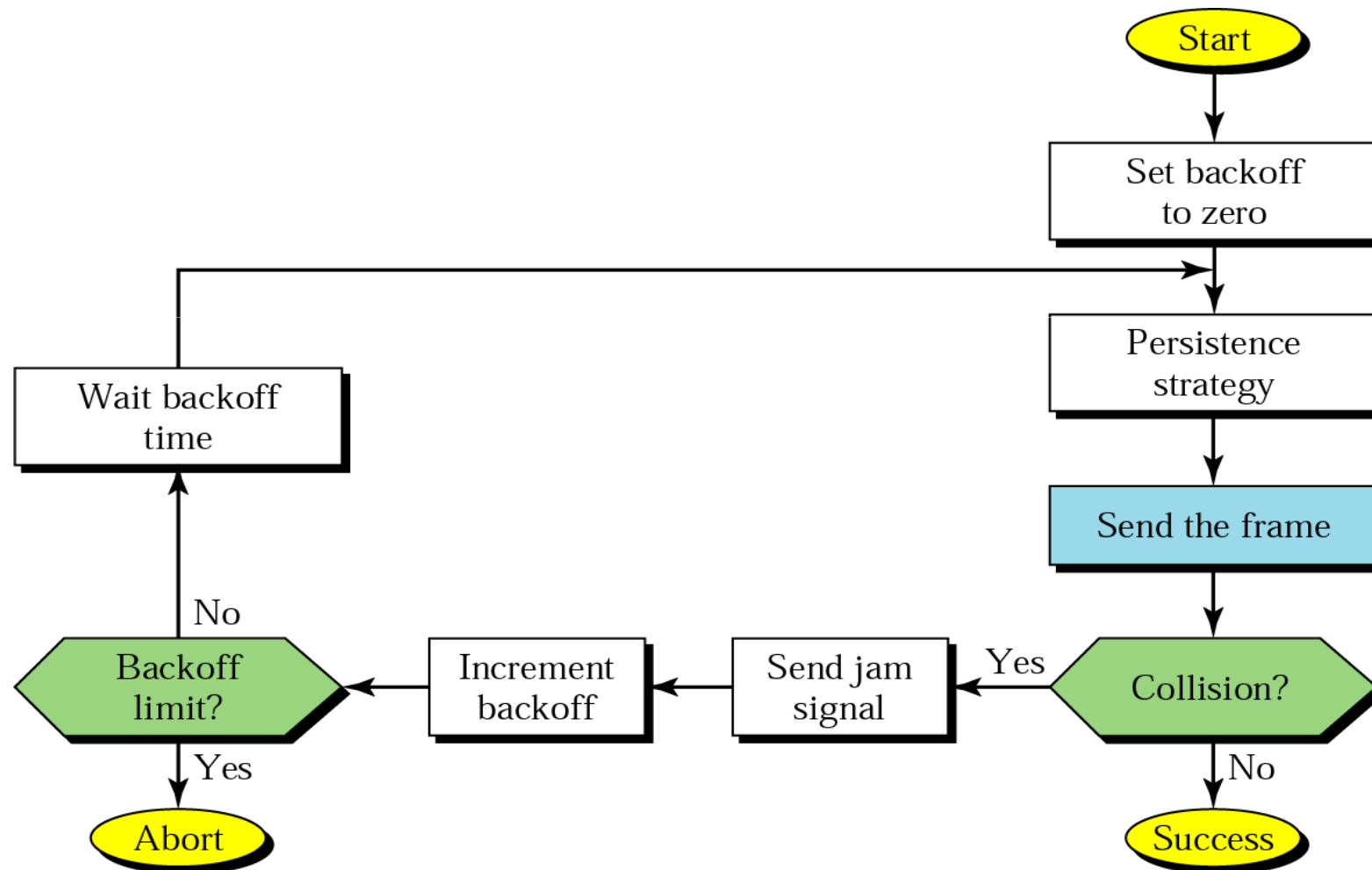


Persistent strategy



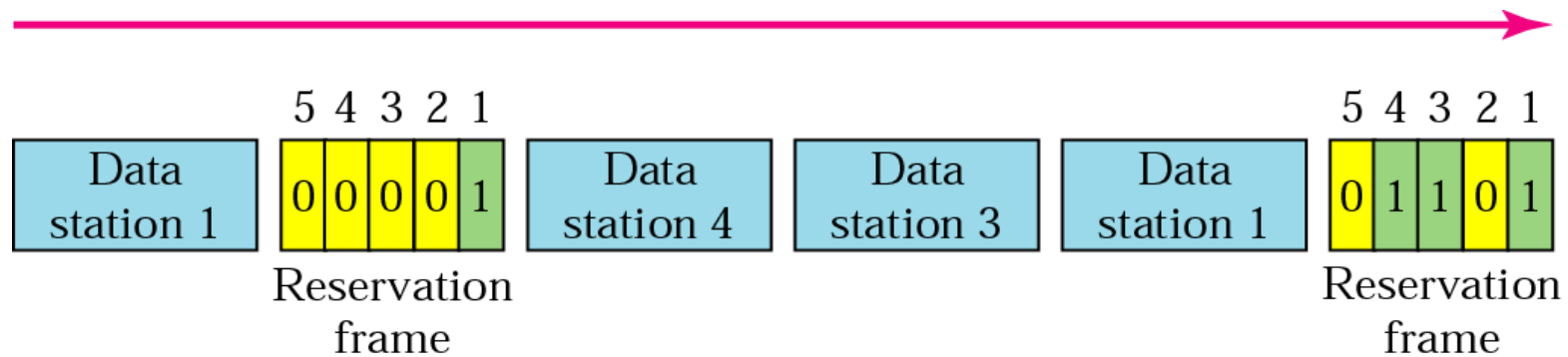
# CSMA/CD: Algorithm

38



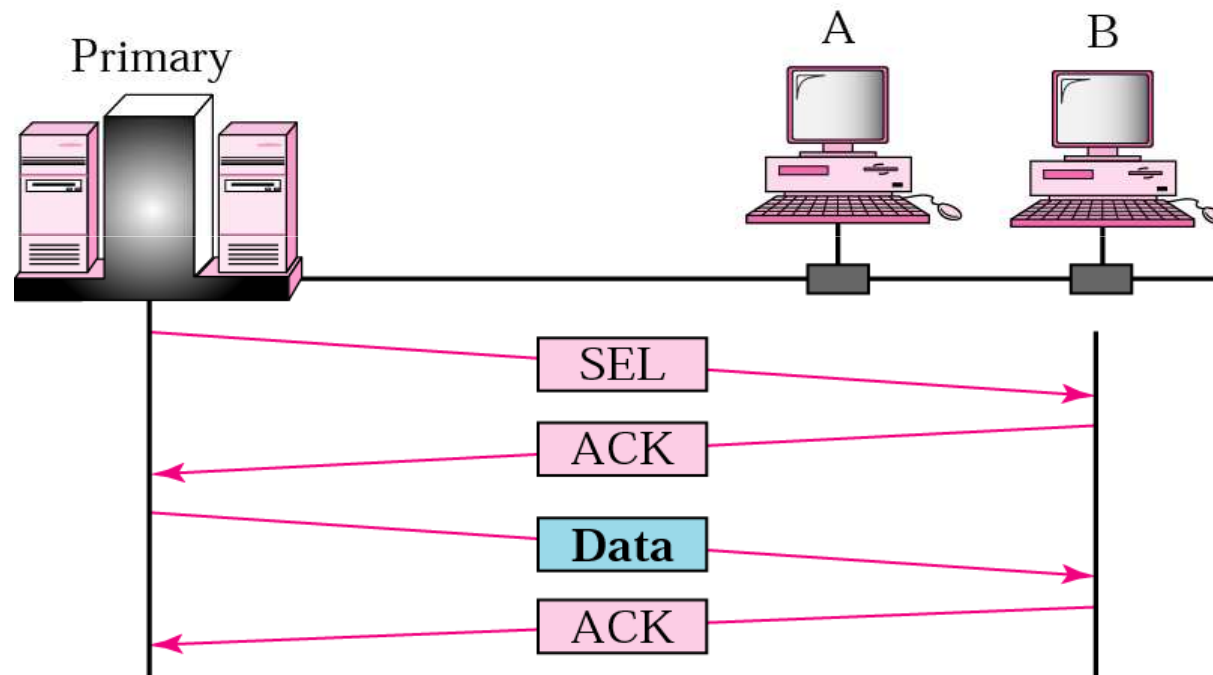
# Controlled Access: Reservation Access Method

39



# Controlled Access: Select (Primary intended to Send)

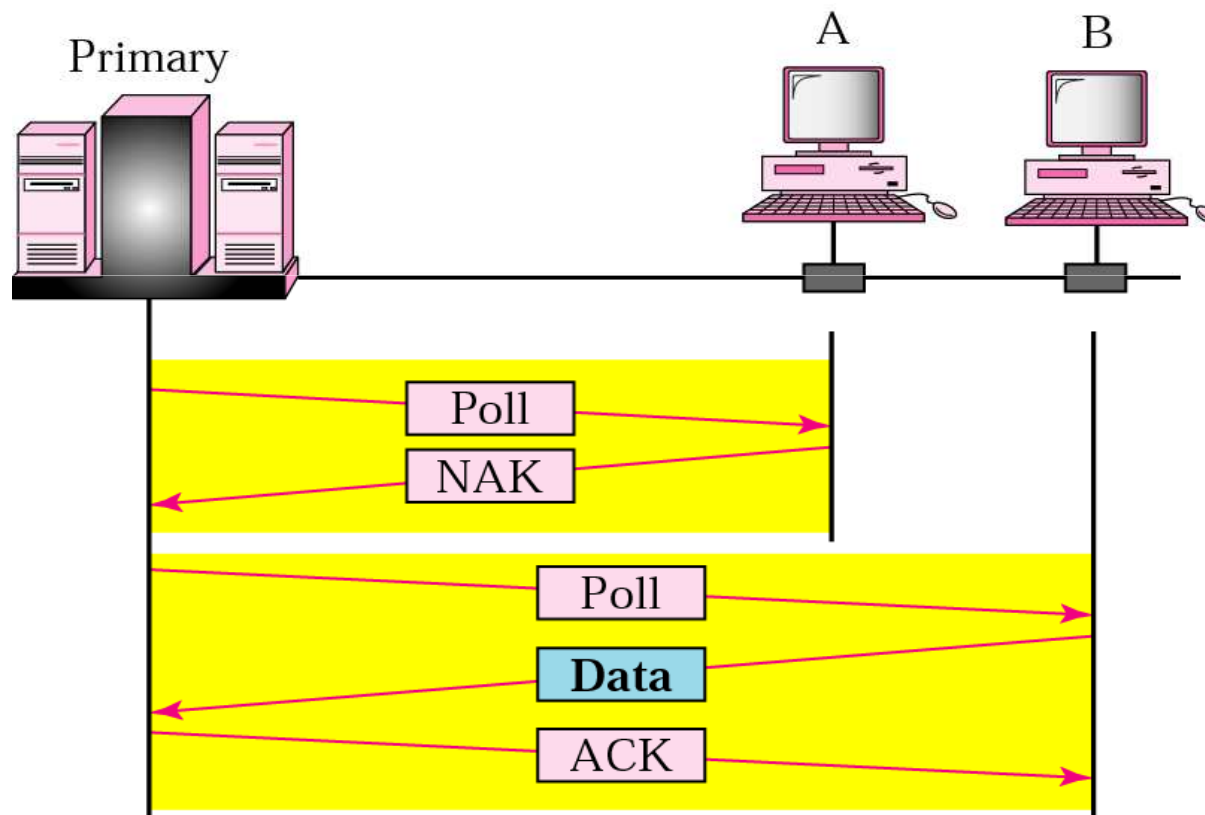
40





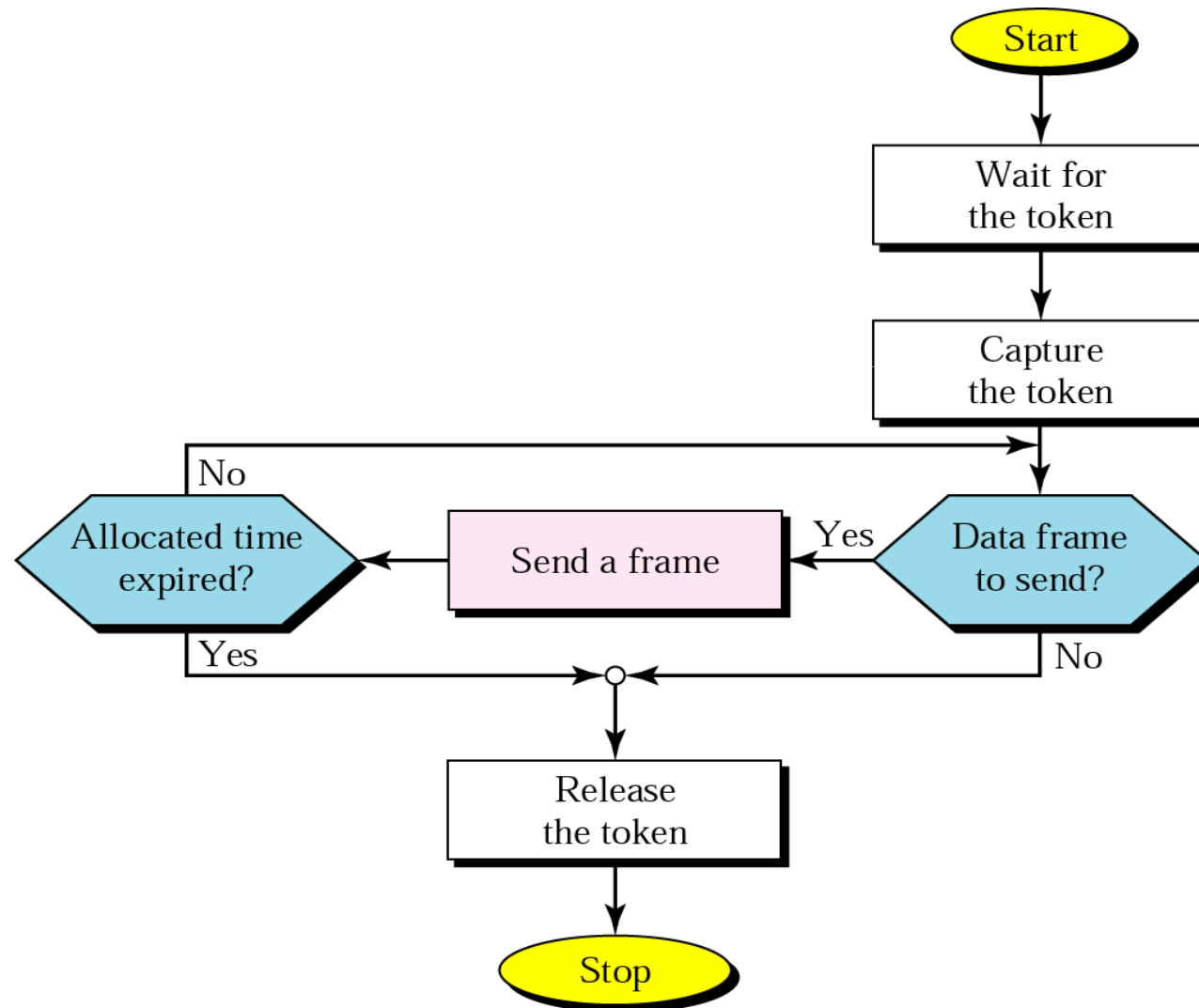
# Controlled Access: Poll (Primary Intended to Receive)

41



# Controlled Access: Token Passing

42



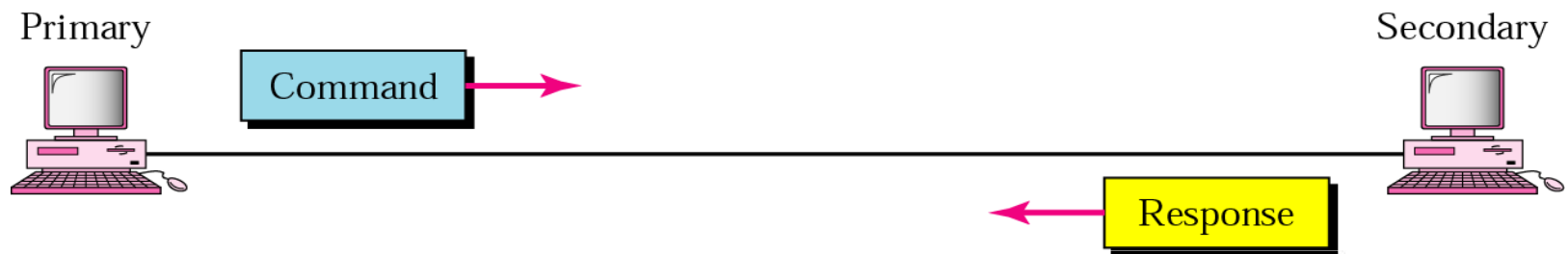
# Data Link Protocols: HDLC

43

- High Level Data Link Control Protocol.
- Designed to support Half Duplex and Full Duplex Communication.
- It can be used over Point to Point and Multipoint Links.
- HDLC Provides two common modes of transmission
  - ✓ NRM ( Normal Response Mode)
  - ✓ ABM (Asynchronous Balanced Mode)

# Normal Response Mode: NRM

44



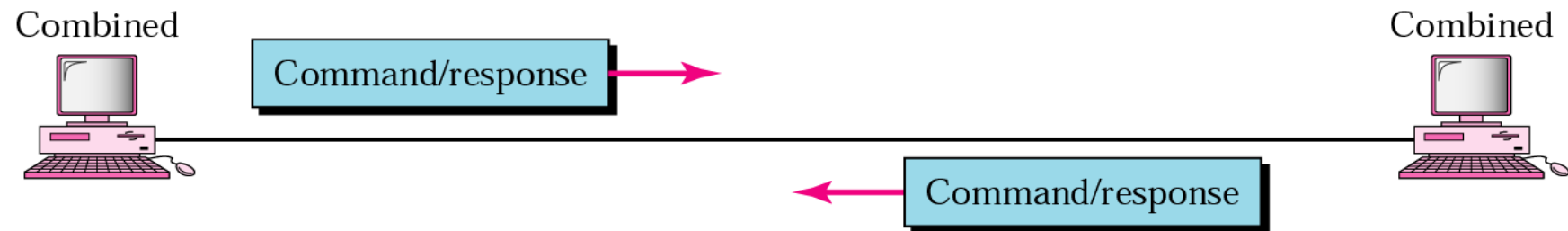
a. Point-to-point



b. Multipoint

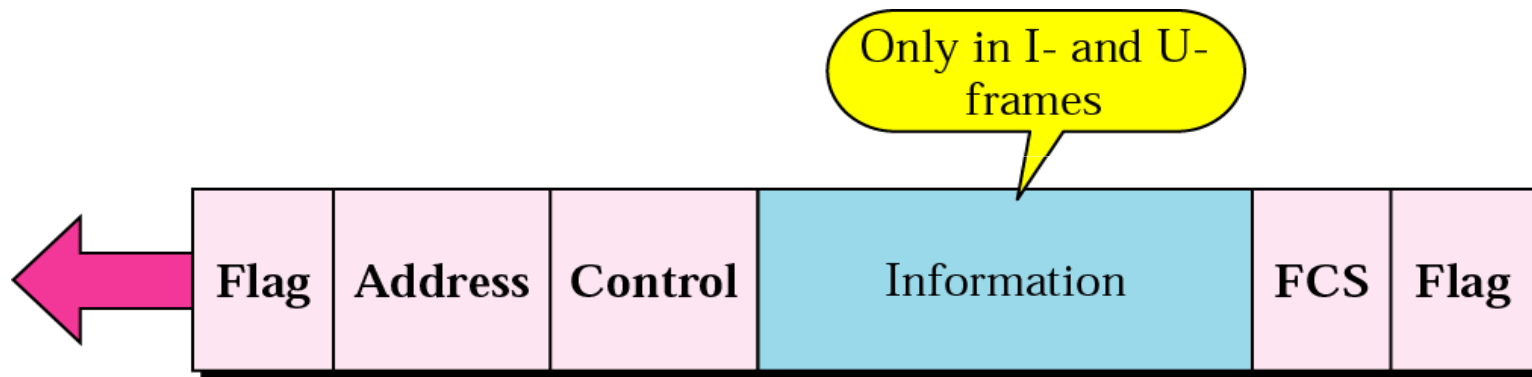
# Asynchronous Balanced Mode: ABM

45



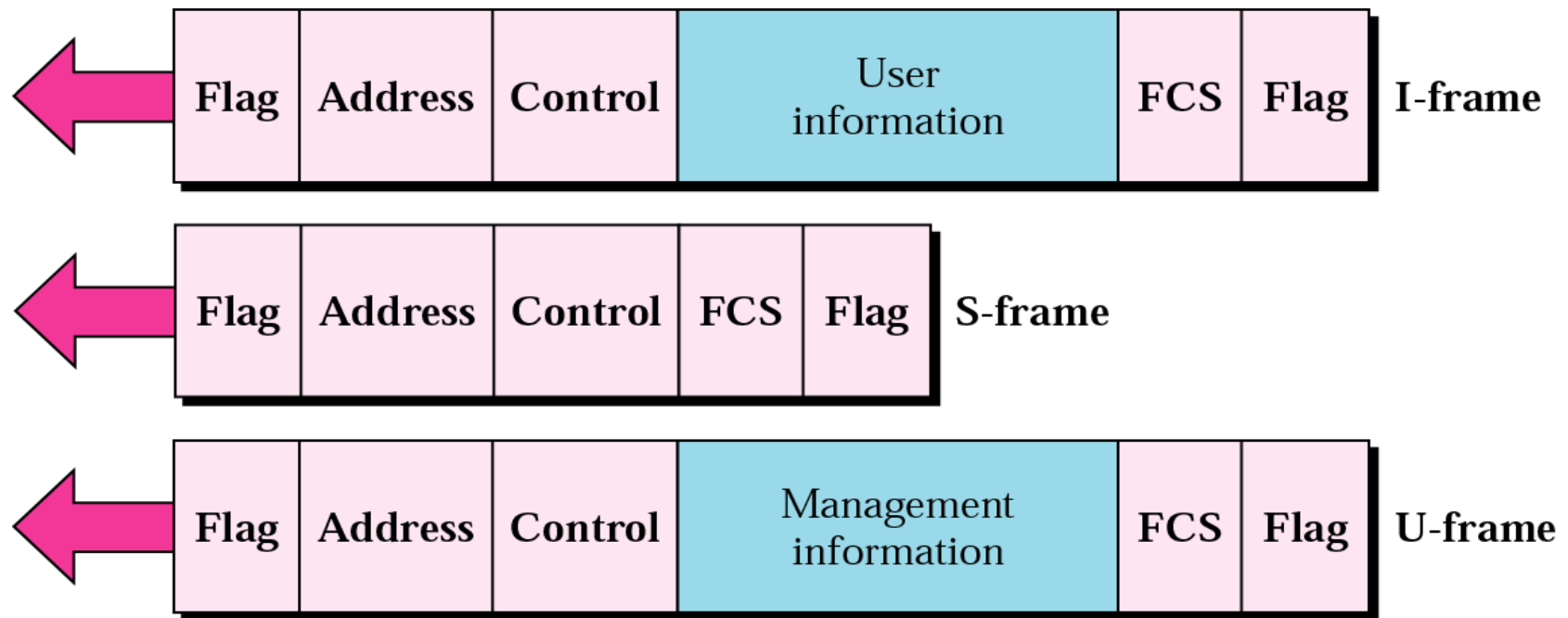
# HDLC : Frame Format

46



# HDLC : Frame Types

47



# HDLC Frame Format: Discussions

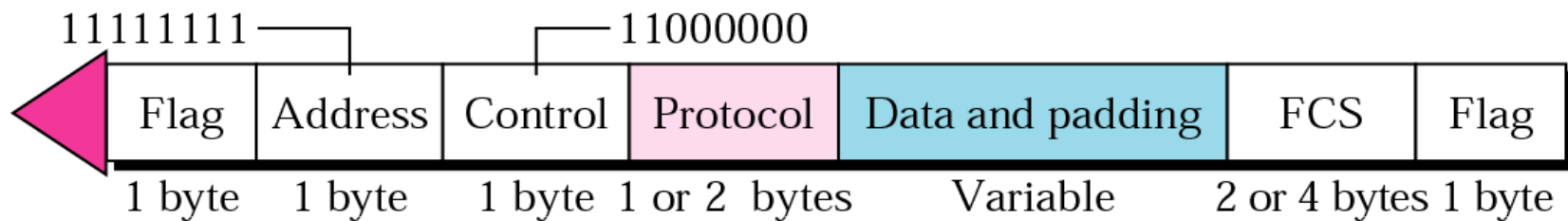
48

- I-Frames => Information Frames
- I-Frames are used to transport User data and Control Information.
- S-Frames => Supervisory Frames
- S-Frames are used only to transport control information.
- U-Frames => Unnumbered Frames
- U-Frames are reserved for system management.
- It is intended for managing the link itself.



# PPP: Frame Format

49



- Most Common Protocol For Point to Point Access.
- PPP Employs the version of HDLC.

# PPP: Frame Format Discussions

50

- Flag Field : Identify the Boundaries of PPP. Value is 01111110
- Address Field : Uses Broadcast Address of 11111111.
- Control Field : It Contains 11000000 to show that Frame does not contain any Sequence Numbers and there is no Flow and Error Control.
- Protocol Field : Specifies what is carried in the data field.
- Data Field : Carries Either User data or other Information.
- FCS : Contains 2 byte or 4 byte CRC.

# SLIP: Serial Line Internet Protocol

51

- ❑ Older Protocol Used by PCs to Connect to Internet Via Modem.
- ❑ Data Link Layer Protocol that Provides Connectivity Across Telephone Line and No Error Correction.
- ❑ Relies on Hardware For Error Checking and Correction.
- ❑ Supports only on TCP/IP.
- ❑ Not Used Much in Today's Environment.

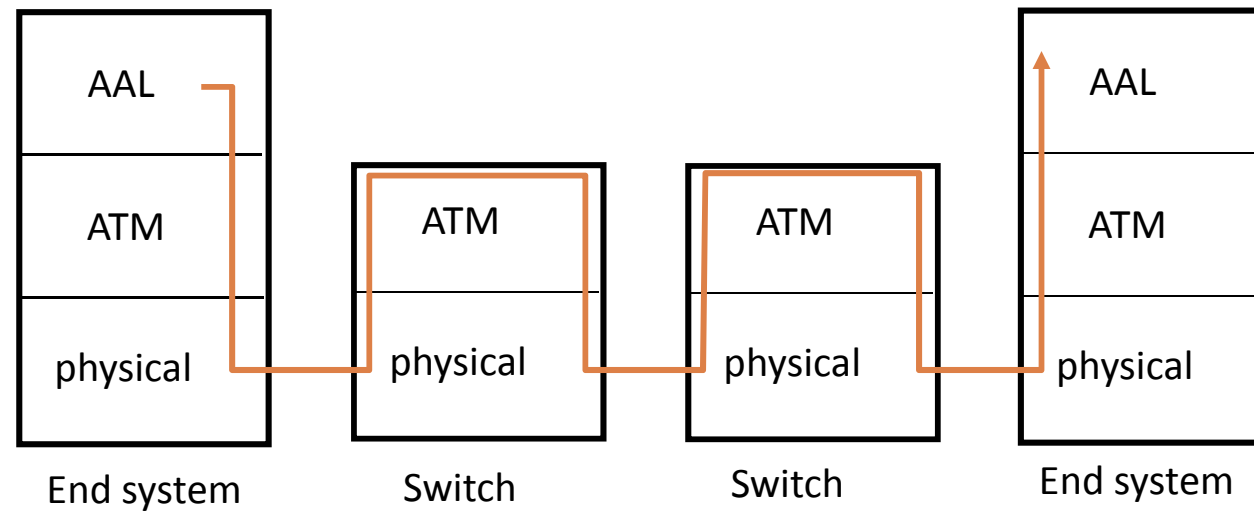
# ATM : Asynchronous Transfer Mode

52

- 1990s Standard for High Speed for Broadband Integrated Service Digital Network Architecture.
- Data Rate => 155 Mbps to 622 Mbps and Higher.
- Goal => Integrated Voice, Video and Data Transport.
- Provide QoS Requirements for Integrated Traffic.
- Root of Next Generation Telephony.
- Fixed Length Packets => Cells ( Uses Virtual Circuit Approach).

# ATM : Architecture ??

53



# ATM : Protocol Architecture

54

## AAL (ATM Adaptation Layer)

- Used only at edge of ATM Network.
- Data Segmentation Reassembly.
- Analogous to Internet Transport Layer.

## ATM Layer

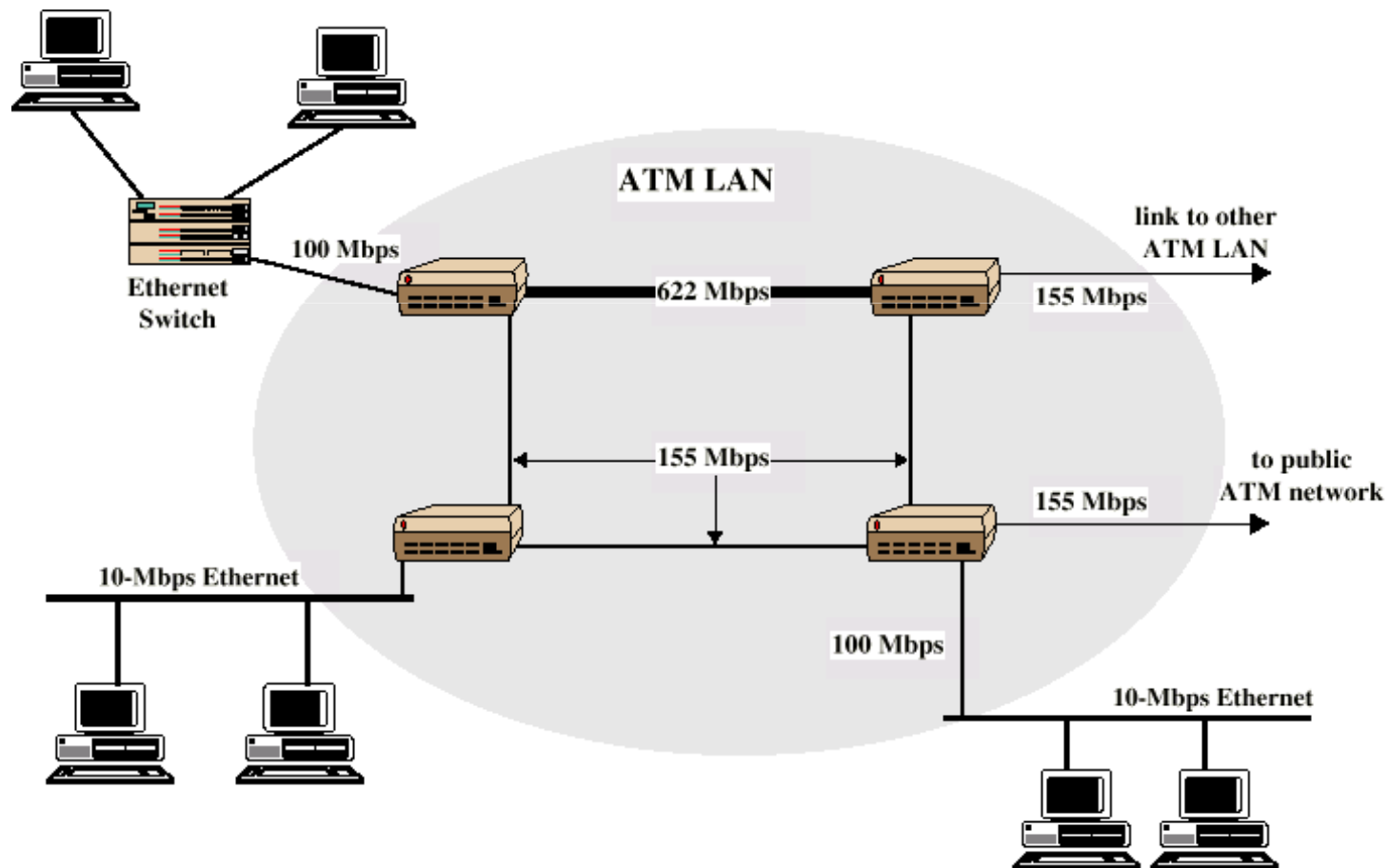
- Analogous to Internet Network Layer.
- Cell Switching and Routing.

## Physical Layer

- Analogous to Internet Physical Layer.

# ATM : Example ATM LAN

55



# Frame Relay

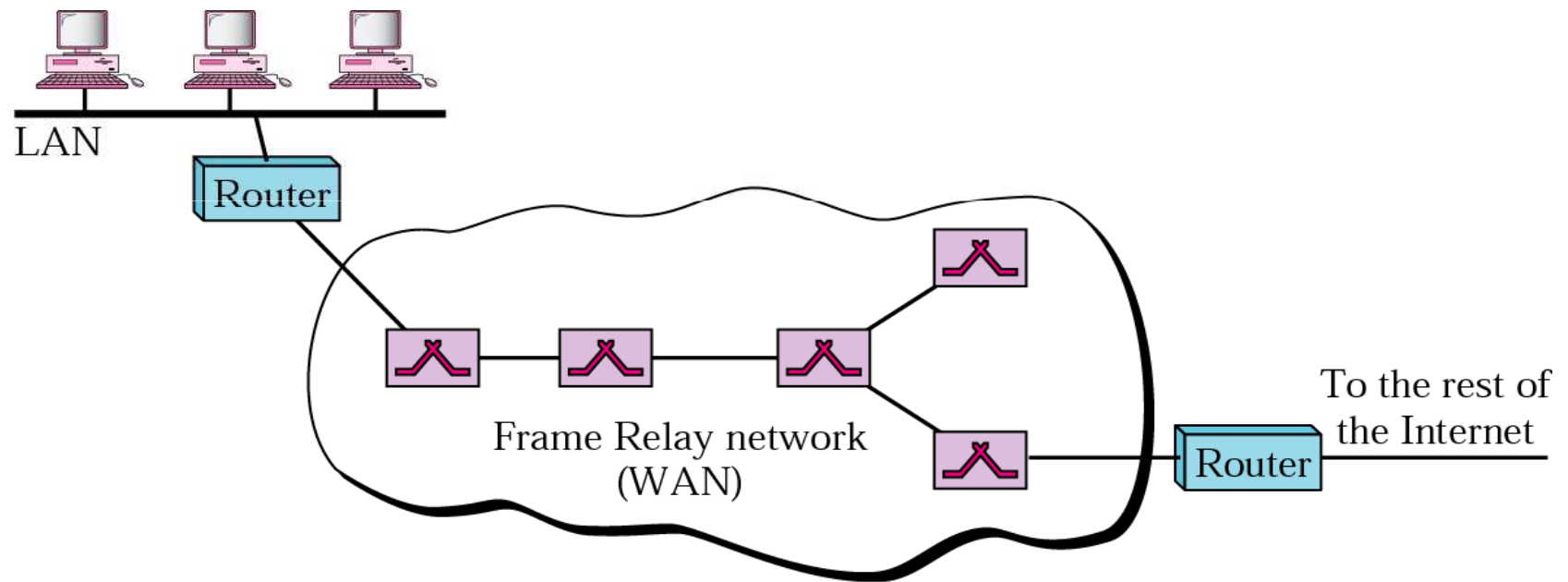
56

- It is a Virtual Circuit Wide Area Networks.
- Designed to respond for new type of WAN in late 1980s.
- Prior to Frame Relay => X.25 were Used.
  
- Demerits of X.25
  - ✓ Low Data Rate (64 Kbps).
  - ✓ Flow and Error Control at Data Link Layer and Network Layer.
  - ✓ X.25 has its own Network Layer.
  
- Frame Relay Operates at Higher Speed (1.54 Mbps).
- It Operates in Physical and Data Link Layers.
- Can be easily used as a backbone Network.



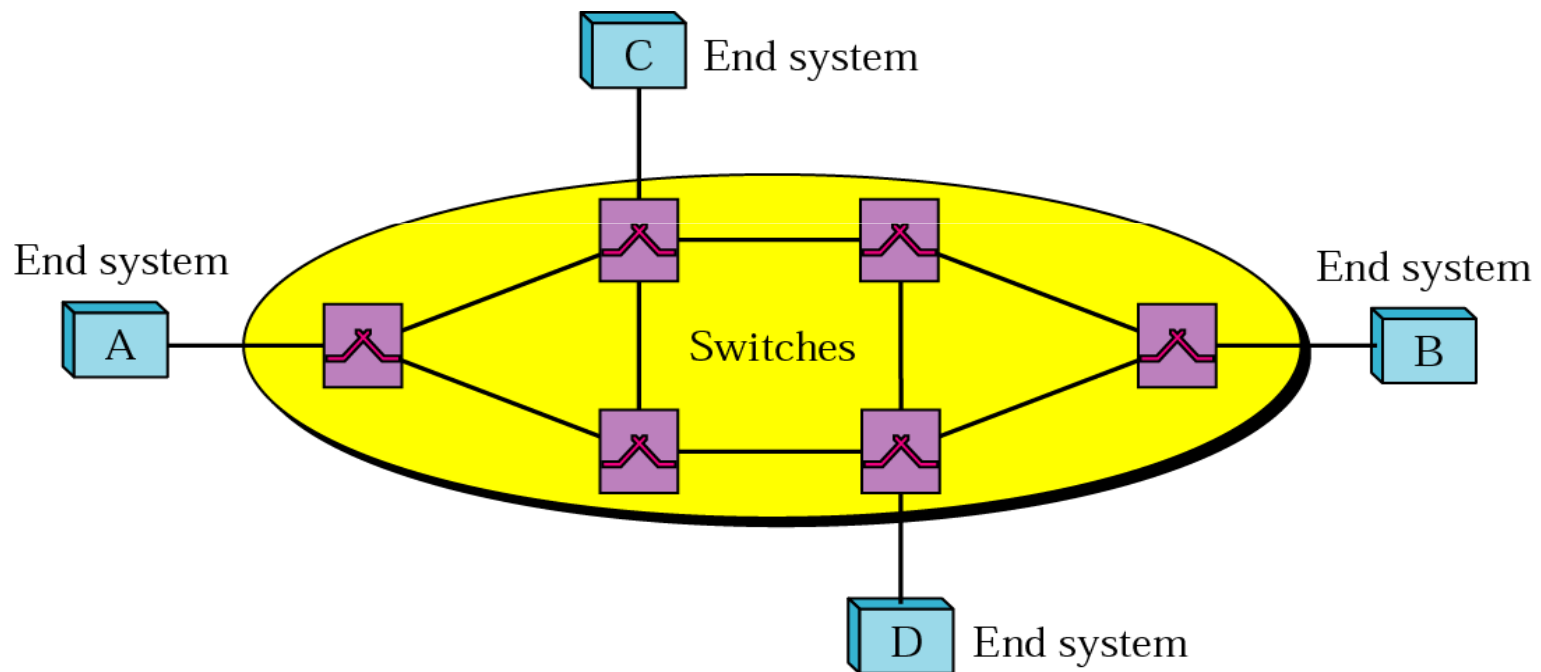
# Frame Relay Networks

57



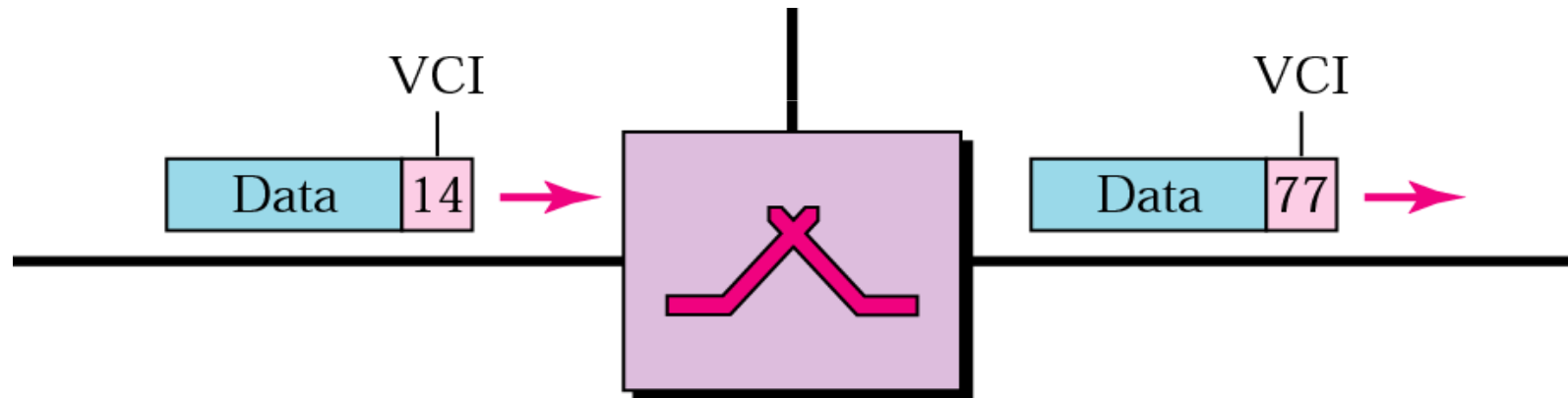
# Frame Relay Networks: Virtual Circuit Wide Area Network

58



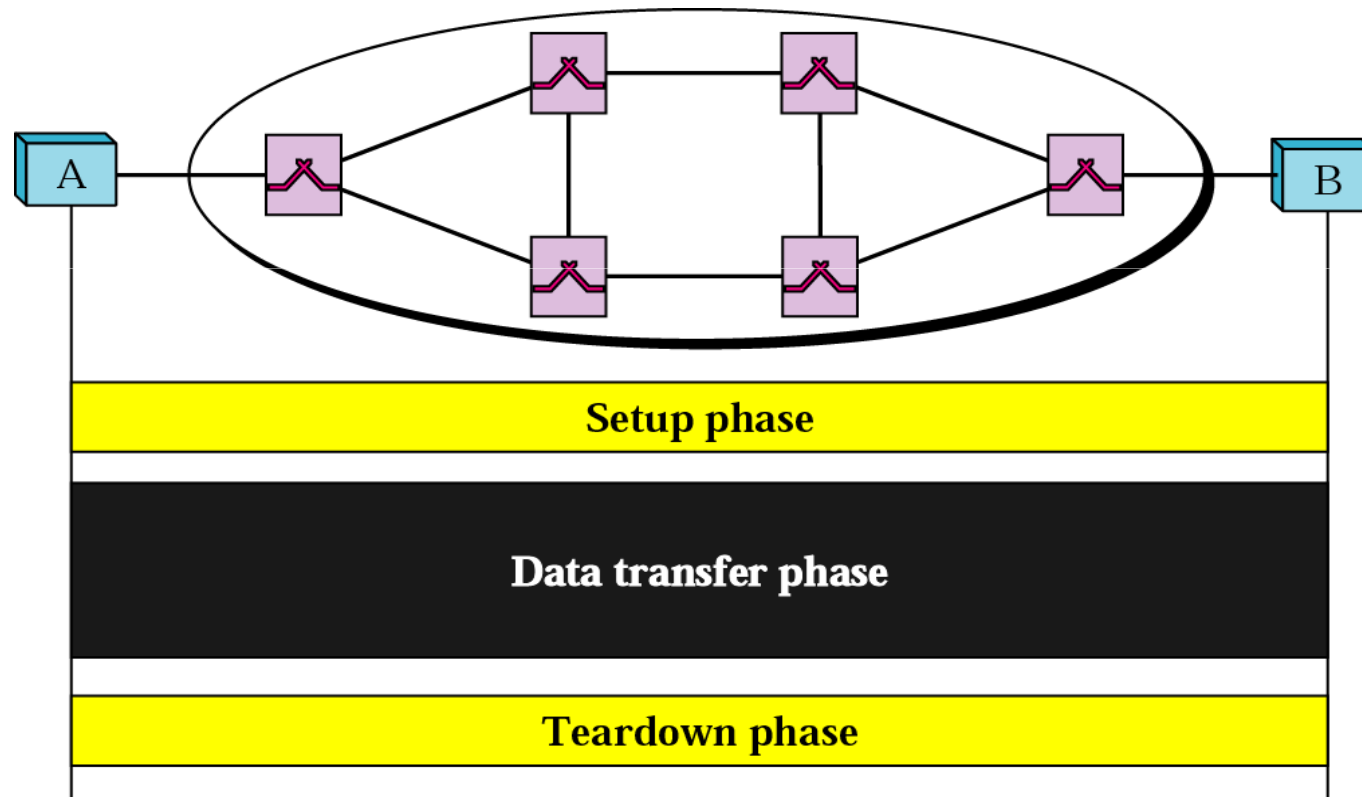
# VCI: Virtual Connection Identifier

59



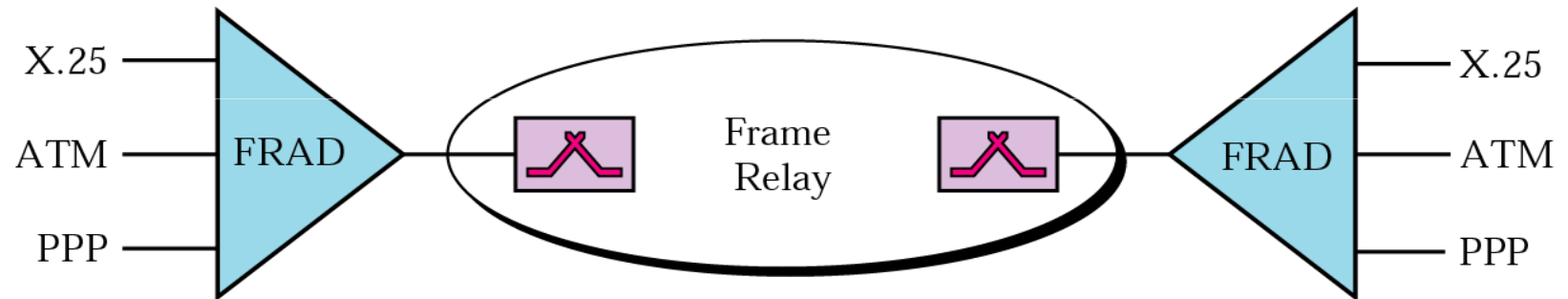
# VCI Phases: Three Phases of VCI

60



# FRAD: Frame Relay Assembler Disassembler

61



# ISDN: Integrated Service Digital Network

62

- ITU Standard For global Digital Communication.
- It was Developed in 1984 to replace Analog Telephone System.
- Allow the Complete Integration of both Voice, Video and Data Within a Single System.
- Two Types of ISDN
  - ✓ Basic Rate ISDN => Provides 2B+D Channels.
  - ✓ B Channel of 64 Kbps and D Channel of 16 Kbps.
  - ✓ B Channel for Data and D channel for Control.
- Primary Rate ISDN => Provides 23B+D Channels.
- Provides Data Rate of 1.544 Mbps.

Thank You