TIM interrupt Definition TIM Master Mode Selection TIM Flag Definition TIM Break System **TIM Trigger Selection** TIM Input Capture Polarity TIM Commutation Source TIM DMA Base Address TIM OSSI OffState Selection for Idle mode state TIM Input Capture Prescaler TIM OSSR OffState Selection for Run mode state TIM Clear Input Source TIM Auto-Reload Preload TIM Update Interrupt Flag Remap **TIM Output Compare** Polarity TIM One Pulse Mode TIM Clock Prescaler TIM Capture/Compare Channel State TIM Break Input Polarity TIM Slave mode TIM TI1 Input Selection **TIM Clock Division** TIM Output Fast State TIM Input Capture Selection TIM ETR Prescaler TIM Channel TIM Break Input Enable TIM Output Compare TIM **TIM Exported Constants** Idle State TIM Encoder Input Polarity TIM DMA Sources TIM Break Input 2 Polarity TIM Clear Input Polarity TIM Trigger Polarity TIM Input Channel polarity TIM Clock Source TIM Break input 2 Enable TIM Complementary Output Compare State TIM DMA Burst Length TIM Complementary Output Compare Polarity TIM Clear Input Prescaler TIM Master/Slave Mode TIM Master Mode Selection 2 (TRGO2) TIM Counter Mode TIM Trigger Prescaler TIM Complementary Output Compare Idle State TIM Output Compare and PWM Modes TIM DMA Handle Index TIM Encoder Mode TIM ETR Polarity TIM Automatic Output Enable TIM Event Source TIM Lock level TIM Clock Polarity TIM Output Compare State TIM Group Channel 5

and Channel 1, 2 or 3