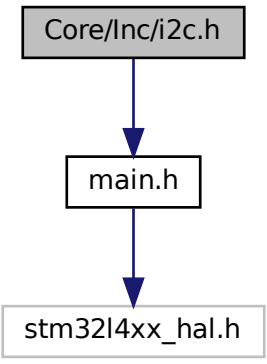


Core/Inc/i2c.h



```
graph TD; A[Core/Inc/i2c.h] --> B[main.h]; B --> C[stm32l4xx_hal.h];
```

main.h

stm32l4xx_hal.h