RCC Backup Domain Reset AHB1 Peripheral Force Release Reset RCC RTC Clock Configuration AHB3 Peripheral Clock Sleep Enable Disable AHB1 Peripheral Clock Sleep Enable Disable APB1 Peripheral Clock Sleep Enabled or Disabled Status APB2 Peripheral Clock **Enabled or Disabled** Status APB2 Peripheral Clock Sleep Enabled or Disabled Status APB1 Peripheral Clock Enabled or Disabled Status AHB1 Peripheral Clock Enable Disable AHB3 Peripheral Clock **Enable Disable** AHB2 Peripheral Force Release Reset APB1 Peripheral Force Release Reset AHB1 Peripheral Clock Sleep Enabled or Disabled Status **RCC RCC Exported Macros** AHB3 Peripheral Force Release Reset AHB2 Peripheral Clock Sleep Enabled or Disabled Status APB2 Peripheral Clock Sleep Enable Disable APB1 Peripheral Clock **Enable Disable** AHB2 Peripheral Clock Enabled or Disabled Status APB2 Peripheral Clock Enable Disable APB2 Peripheral Force Release Reset APB1 Peripheral Clock Sleep Enable Disable AHB2 Peripheral Clock Sleep Enable Disable AHB3 Peripheral Clock Sleep Enabled or Disabled Status AHB3 Peripheral Clock **Enabled or Disabled** Status Flags Interrupts Management AHB2 Peripheral Clock **Enable Disable** AHB1 Peripheral Clock Enabled or Disabled Status