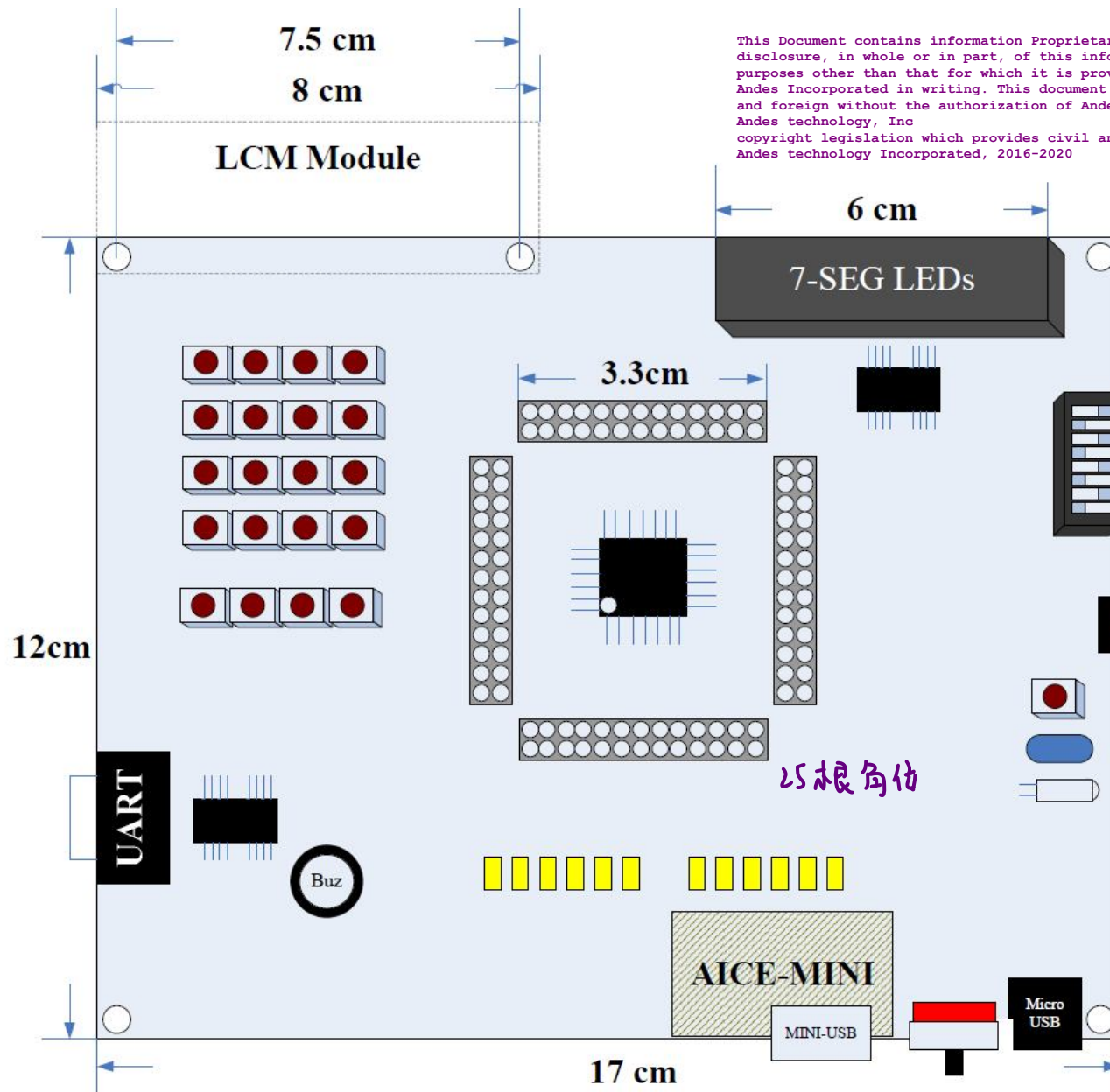


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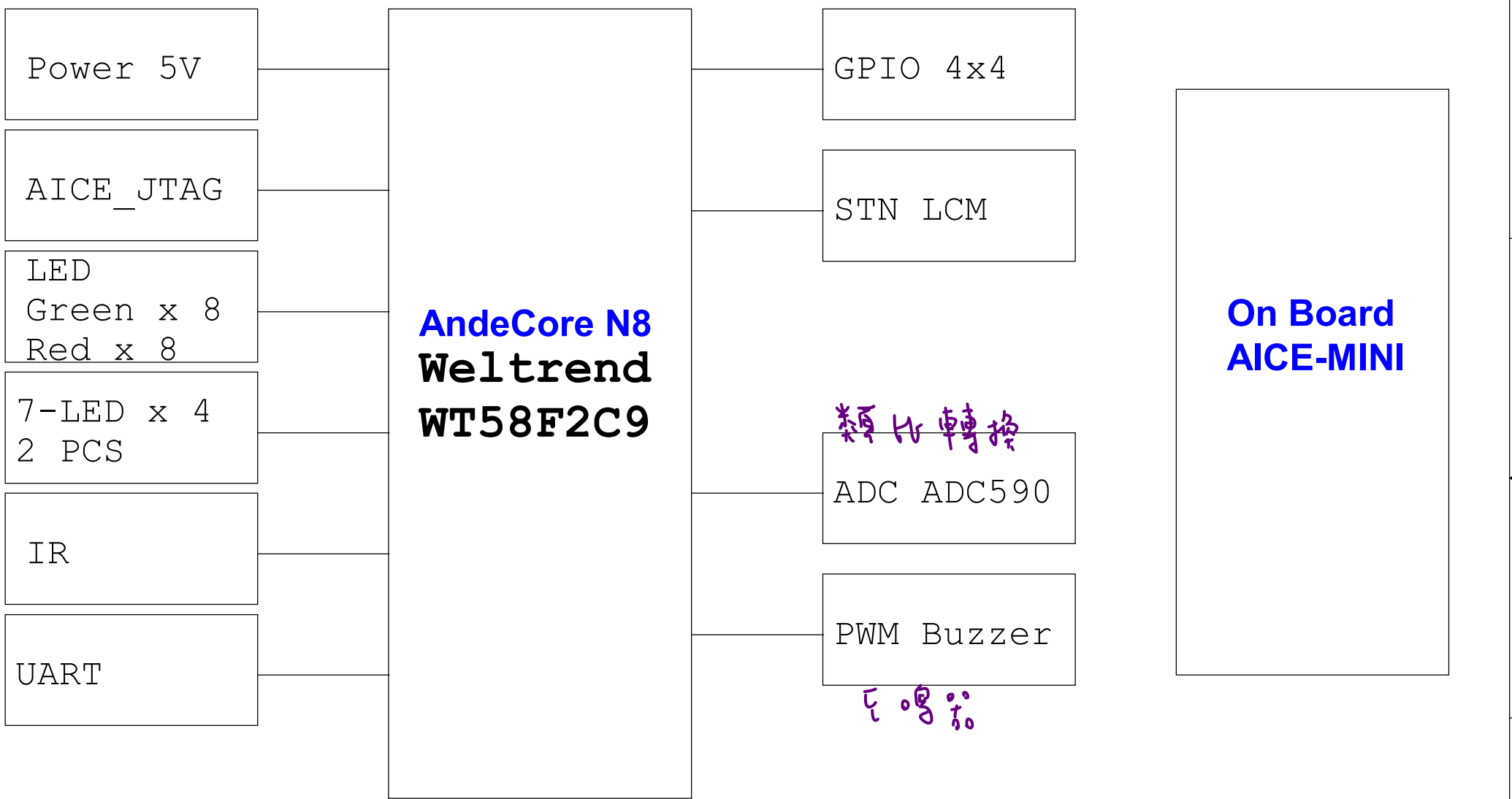
Four layer of PCB stack

Signal layer  
GND layer  
Power layer  
Signal layer

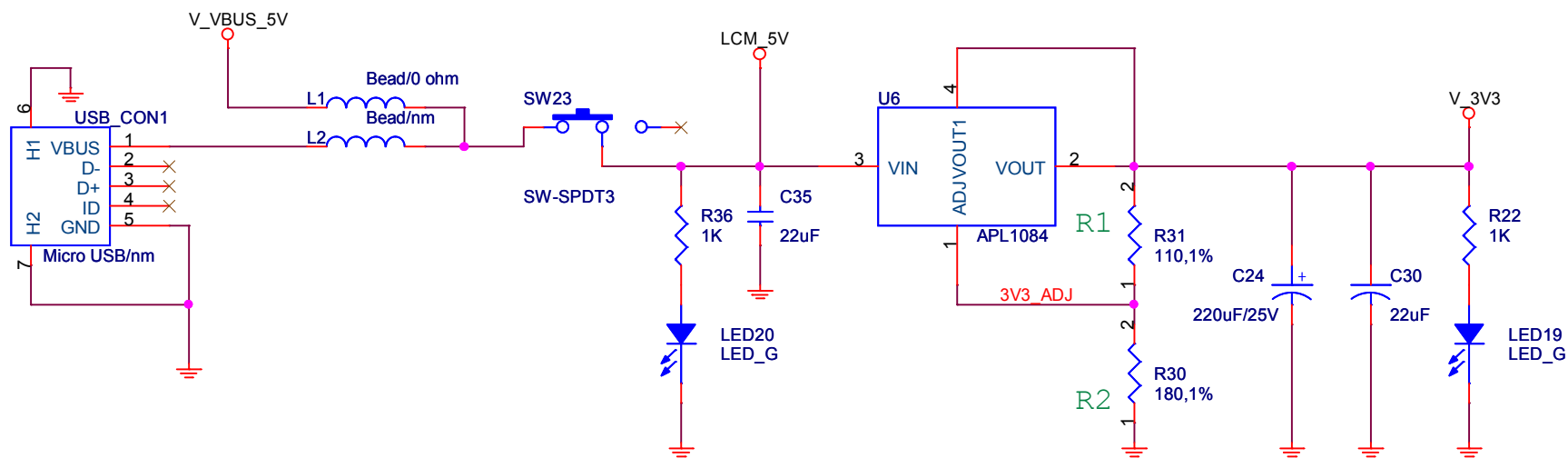
5/10/5 mil

USB 90 ohm DIFF Pair

|                                                                                       |                               |                            |          |
|---------------------------------------------------------------------------------------|-------------------------------|----------------------------|----------|
|  |                               | ANDESTECH TECHNOLOGY CORP. |          |
| Title: <b>Placement Diagram</b>                                                       |                               |                            |          |
| Size: B                                                                               | Document Number: ADP-WT58F2C9 | Designer: Jonathan         | Rev: 1.0 |
| Date: Wednesday, June 29, 2016                                                        | Sheet: 1                      | of 11                      |          |

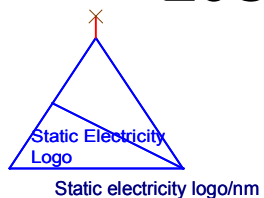


NOTES :  
1. Unless otherwise noted, all Resistor values are in OHMs, all capacitor values are in FARADs, and all Inductor values are in HENRIES.

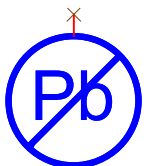
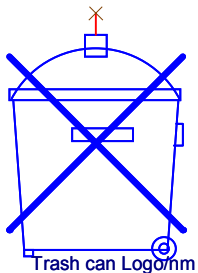
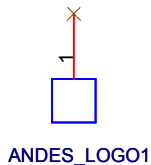


$$V_{out} = 1.25 * (1 + R2/R1) = 1.25 * (1 + 180/110) = 3.3V$$

## LOGOS

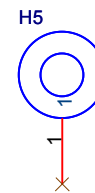
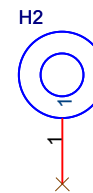
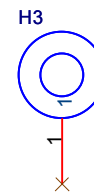
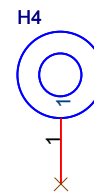
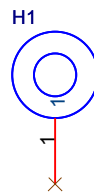



Static electricity logo/nm

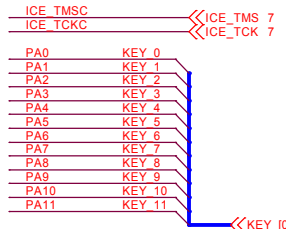
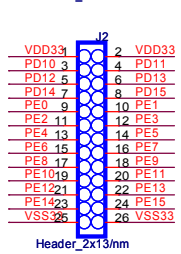
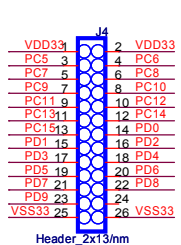
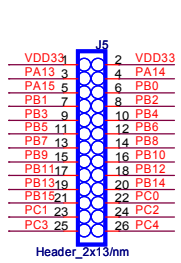
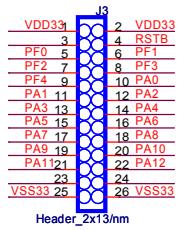
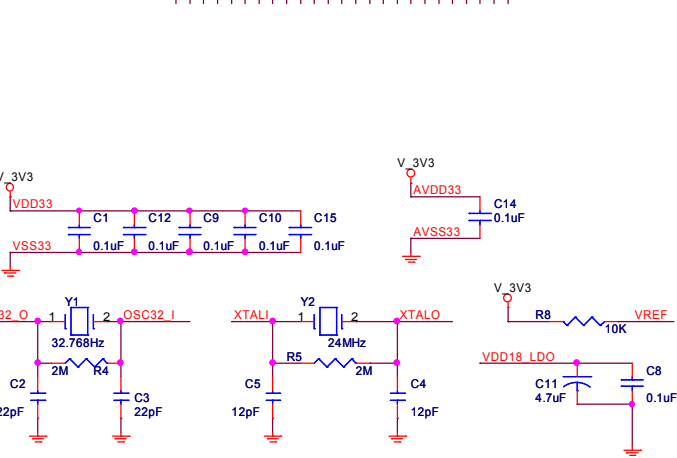
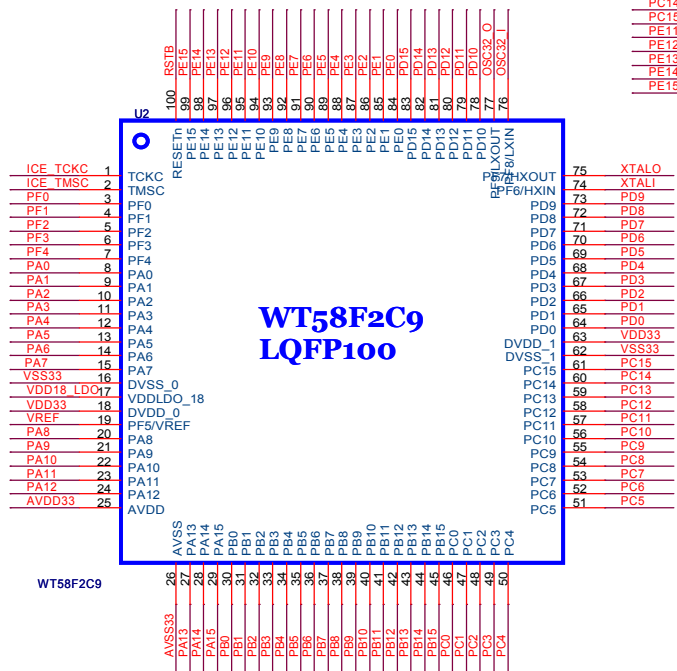


Pb Free Logo/nm

## SCREW HOLE



|                                                                                       |                          |                           |           |         |
|---------------------------------------------------------------------------------------|--------------------------|---------------------------|-----------|---------|
|  |                          | ANDESTECH TECHNOLOGY CORP |           |         |
| Title:                                                                                |                          |                           |           |         |
| POWER                                                                                 |                          |                           |           |         |
| Size:                                                                                 | Document Number:         |                           | Designer: | Rev:    |
| A                                                                                     | ADP-WT58F2C9             |                           | Jonathan  | 1.0     |
| Date:                                                                                 | Wednesday, June 29, 2016 |                           | Sheet:    | 3 of 11 |



## ICE

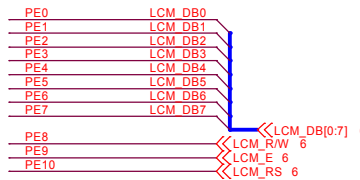


## ADC

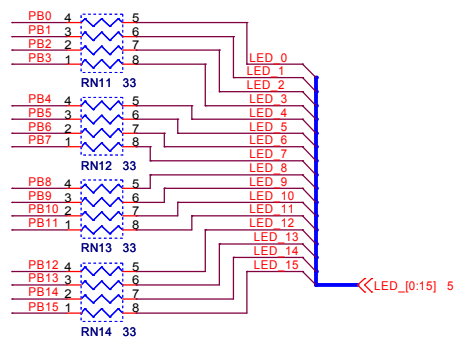
## I2C

## IR

## KEY Switch

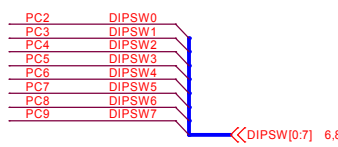


## LCM

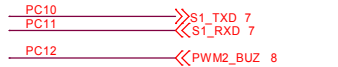


## LEDs

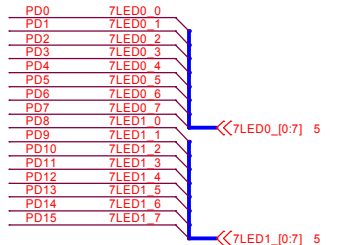
1 暗  
0 亮



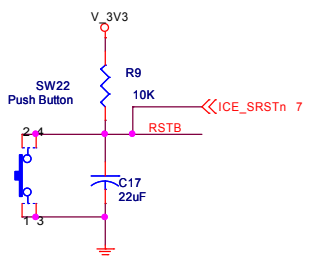
## DIP SW



## UART Buzzer



## 7-SEG LED



ANDESTECH TECHNOLOGY CORP.

Title: WT58F2C9

Size: B

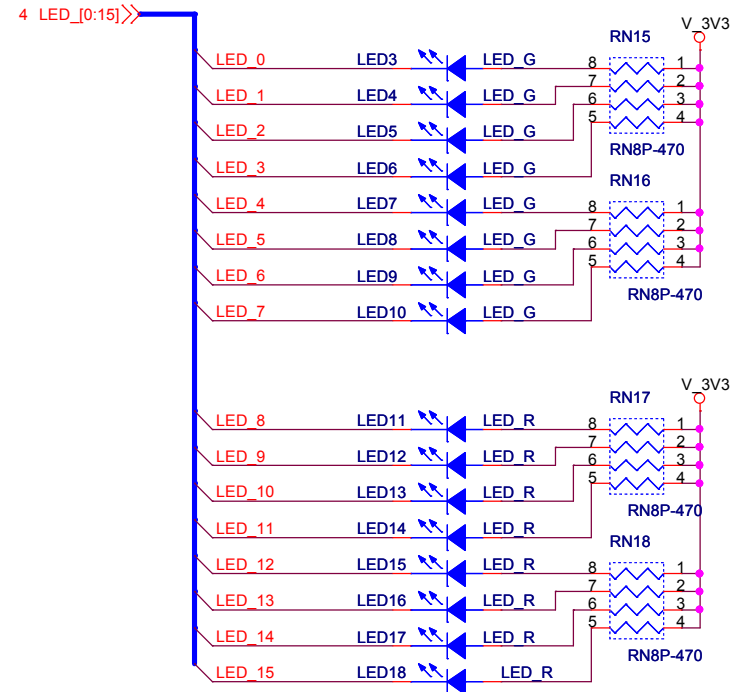
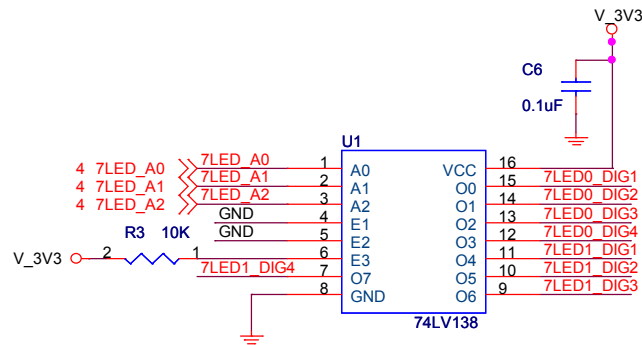
Document Number: ADP-WT58F2C9

Designer: Jonathan

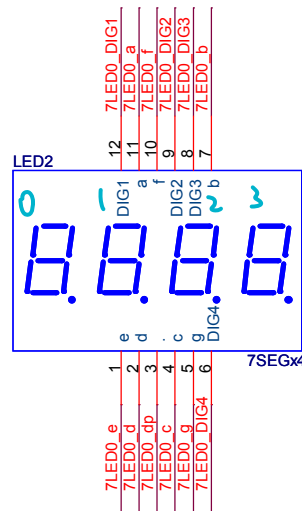
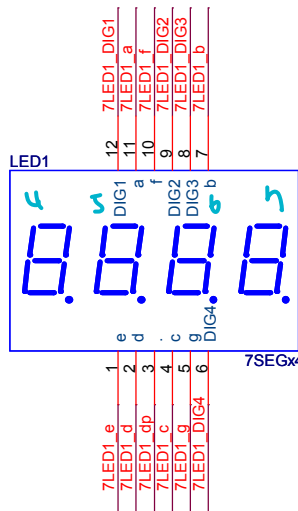
Rev: 1.0

Date: Thursday, July 14, 2016

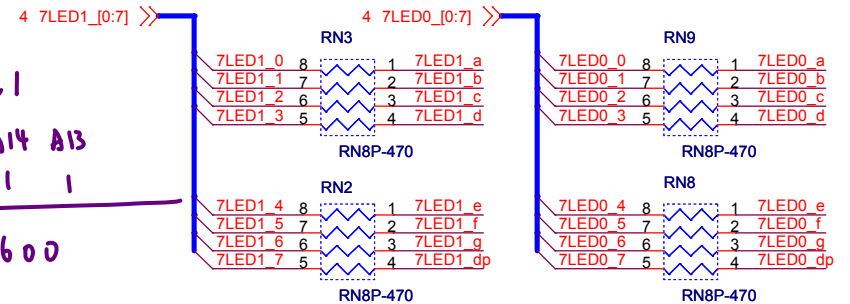
Sheet: 4 of 11



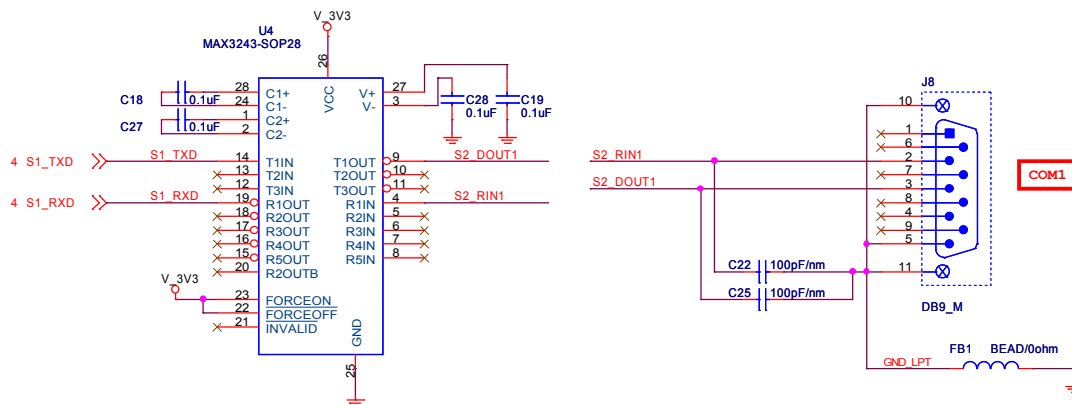
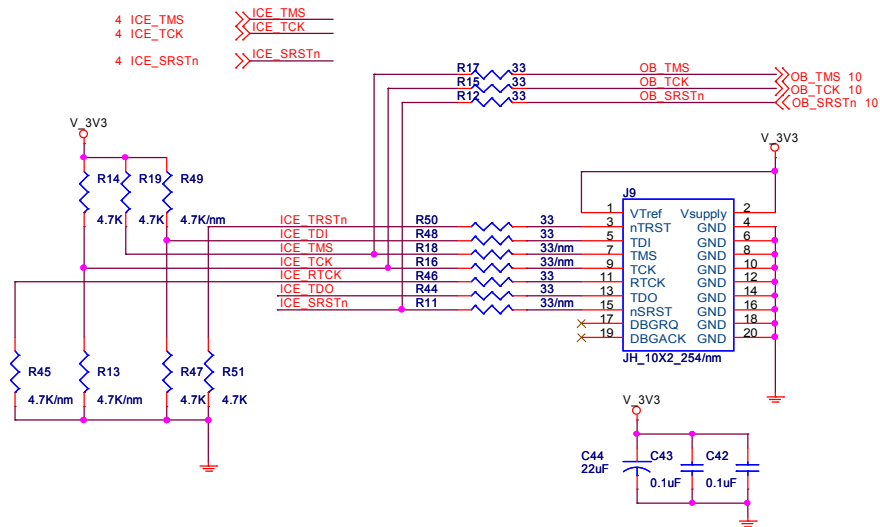
decoder  
输入数字

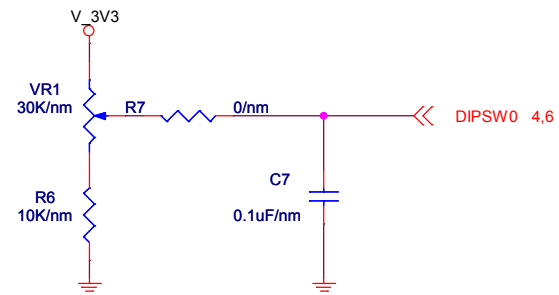
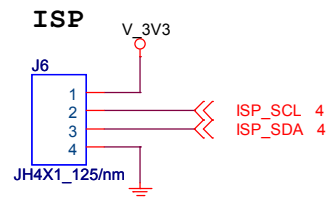
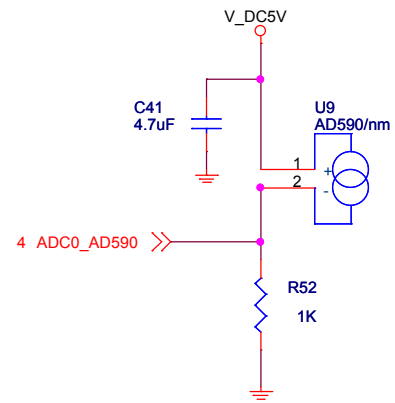
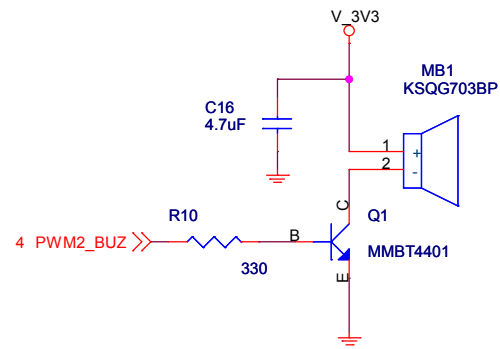
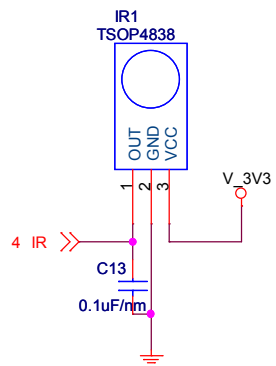



digit 1  
A15 A14 A13  
0 1 1  
0x600



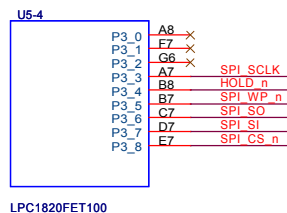
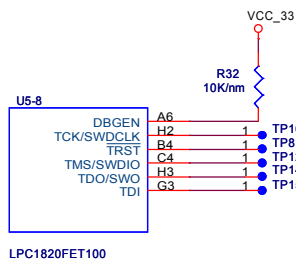
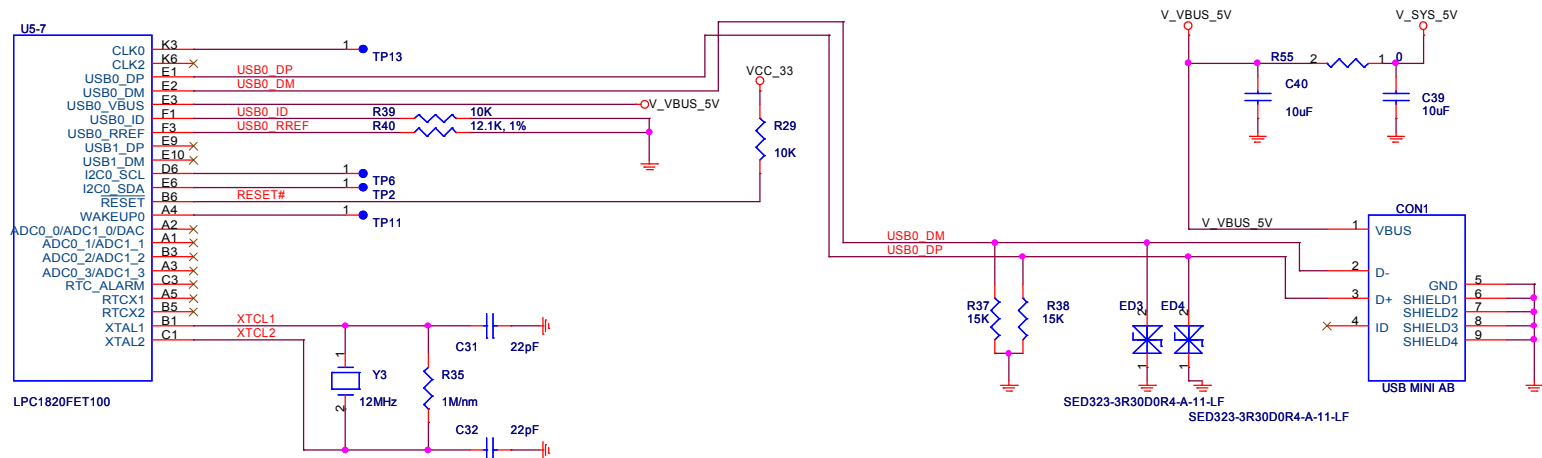




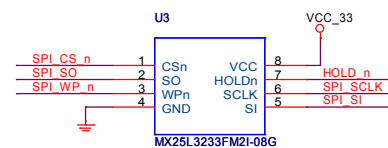


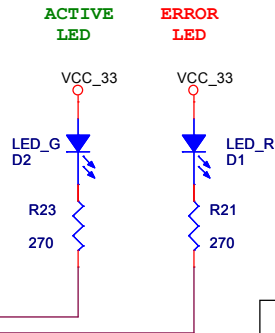
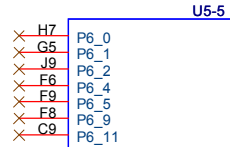
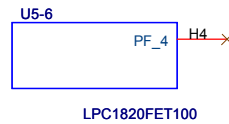
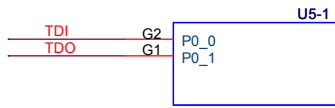
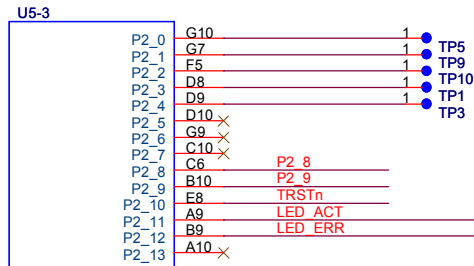
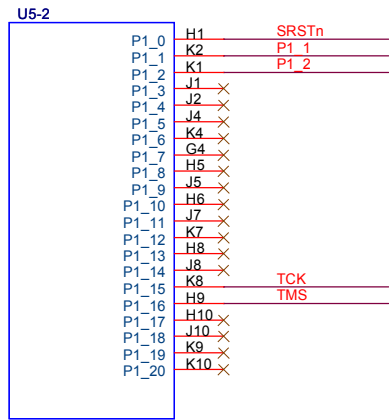
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|  |                                  | ANDESTECH TECHNOLOGY CORP. |             |       |
| Title: <b>Peripheral</b>                                                              |                                  |                            |             |       |
| Size:<br>A4                                                                           | Document Number:<br>ADP-WT58F2C9 | Designer:<br>Jonathan      | Rev:<br>1.0 |       |
| Date:                                                                                 | Wednesday, June 29, 2016         | Sheet:                     | 8           | of 11 |





## SPI ROM (1MB)



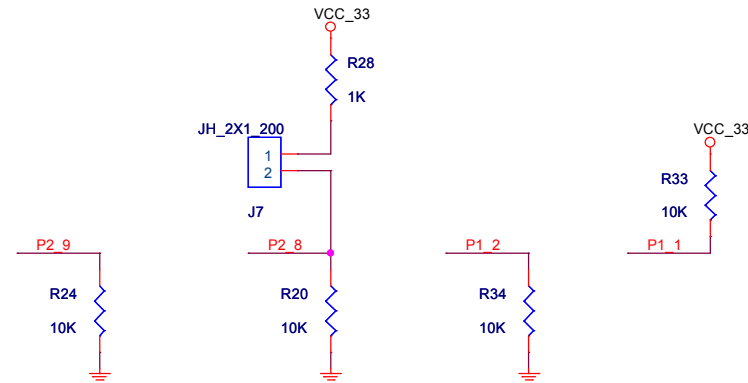


P2\_9

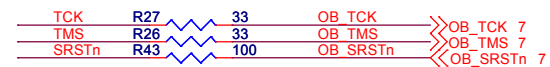
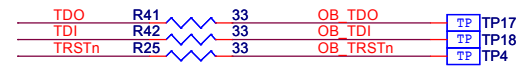
P2\_8

P1\_2

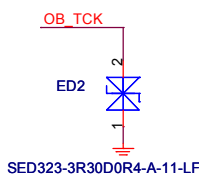
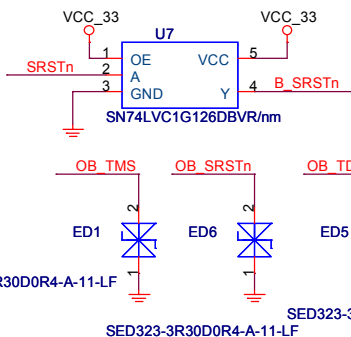
P1\_1



| Pin Number                      | P2_9 | P2_8 | P1_2 | P1_1 |
|---------------------------------|------|------|------|------|
| USB0 Boot                       | LOW  | HIGH | LOW  | HIGH |
| SPIFI Boot<br>(Default setting) | LOW  | LOW  | LOW  | HIGH |



**TWO WIRE JTAG**

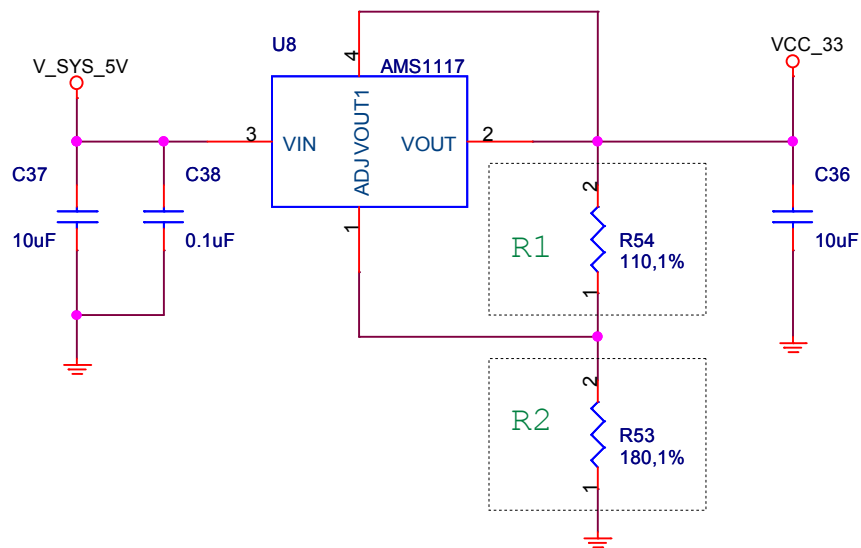


ANDES TECHNOLOGY CORP.

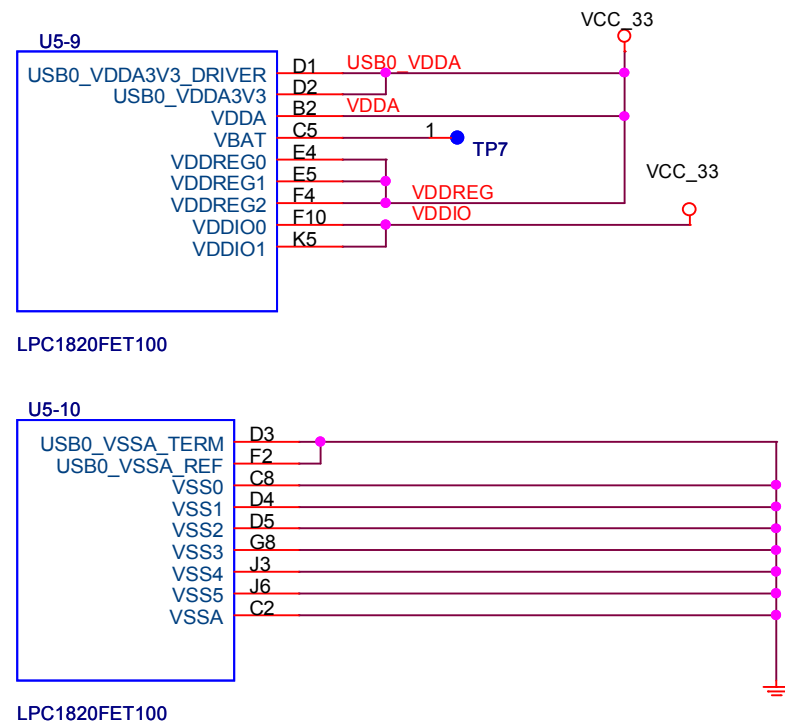
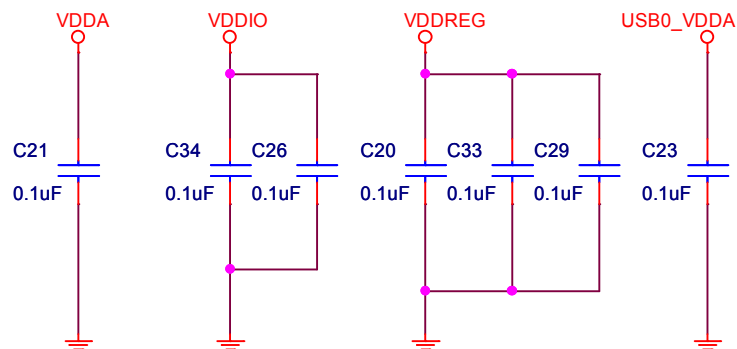
Title: **AICE\_CONFIG\_LED**


Size: Custom Document Number: ADP-WT58F2C9 Designer: Jonathan Rev: 1.0

Date: Wednesday, June 29, 2016 Sheet: 10 of 11



$$V_{out} = 1.25 * (1 + R2/R1) = 1.25 * (1 + 180/110) = 3.3V$$



|                                                                                                                     |                               |                    |          |
|---------------------------------------------------------------------------------------------------------------------|-------------------------------|--------------------|----------|
|  <b>ANDES TECHNOLOGY CORP.</b> |                               |                    |          |
| Title: <b>AICE_POWER</b>                                                                                            |                               |                    |          |
| Size: A                                                                                                             | Document Number: ADP-WT58F2C9 | Designer: Jonathan | Rev: 1.0 |
| Date: Thursday, July 14, 2016                                                                                       | Sheet: 11 of 11               |                    |          |