

DATA SHEET

74HC08; 74HCT08
Quad 2-input AND gate

Product specification
Supersedes data of 1990 Dec 01

2003 Jul 25

Quad 2-input AND gate

74HC08; 74HCT08

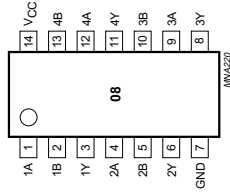
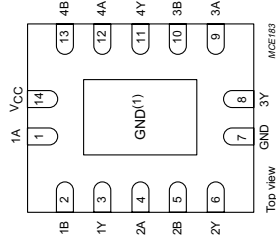


Fig.1 Pin configuration DIP14, SO14 and TSSOP14.



(1) The die substrate is attached to this pad using conductive die attach material. It can not be used as a supply pin or input.

Fig.2 Pin configuration QFN14.

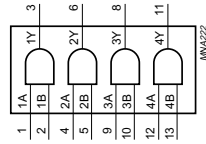


Fig.3 Logic symbol.

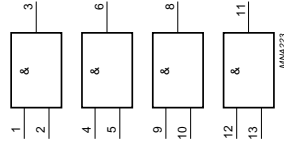


Fig.4 IEC logic symbol.

