

DATA SHEET

74HC04; 74HCT04  
Hex inverter

Product specification  
Supersedes data of 1993 Sep 01

2003 Jul 23

Hex inverter

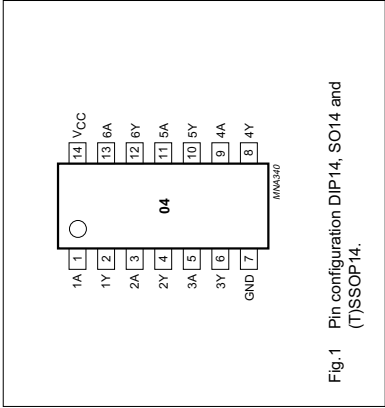
74HC04; 74HCT04

ORDERING INFORMATION

TYPE NUMBER	PACKAGE				
	TEMPERATURE RANGE	PINS	PACKAGE	MATERIAL	CODE
74HC04N	−40 to +125 °C	14	DIP14	plastic	SOT27-1
74HCT04N	−40 to +125 °C	14	DIP14	plastic	SOT27-1
74HC04D	−40 to +125 °C	14	SO14	plastic	SOT108-1
74HCT04D	−40 to +125 °C	14	SO14	plastic	SOT108-1
74HC04DB	−40 to +125 °C	14	SSOP14	plastic	SOT337-1
74HCT04DB	−40 to +125 °C	14	SSOP14	plastic	SOT337-1
74HC04PW	−40 to +125 °C	14	TSSOP14	plastic	SOT402-1
74HCT04PW	−40 to +125 °C	14	TSSOP14	plastic	SOT402-1
74HC04BQ	−40 to +125 °C	14	DHVQFN14	plastic	SOT762-1
74HCT04BQ	−40 to +125 °C	14	DHVQFN14	plastic	SOT762-1

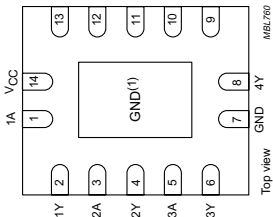
PINNING

PIN	SYMBOL	DESCRIPTION
1	1A	data input
2	1Y	data output
3	2A	data input
4	2Y	data output
5	3A	data input
6	3Y	data output
7	GND	ground (0 V)
8	4Y	data output
9	4A	data input
10	5Y	data output
11	5A	data input
12	6Y	data output
13	6A	data input
14	V <sub>CC</sub>	supply voltage



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(1) The die substrate is attached to this pad using conductive die attach material. It can not be used as a supply pin or input.

Fig.2 Pin configuration DHVQFN14.

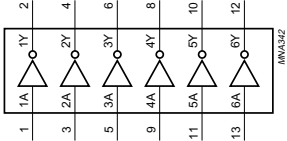


Fig.3 Logic symbol.

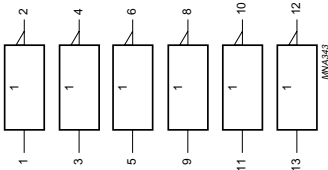


Fig.4 IEC logic symbol.



Fig.5 Logic diagram (one inverter).