

4-bit magnitude comparator

74HC/HCT85

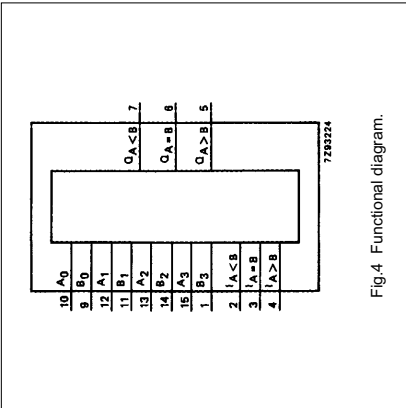


Fig.4 Functional diagram.

APPLICATIONS

- Process controllers
- Servo-motor control

FUNCTION TABLE

COMPARING INPUTS				CASCADING INPUTS			OUTPUTS		
A ₃ , B ₃	A ₂ , B ₂	A ₁ , B ₁	A ₀ , B ₀	I _{A>B}	I _{A=B}	I _{A<B}	Q _{A>B}	Q _{A=B}	Q _{A<B}
A ₃ >B ₃	X	X	X	X	X	X	L	L	L
A ₃ <B ₃	X	X	X	X	X	X	H	H	H
A ₃ =B ₃	A ₂ >B ₂	X	X	X	X	X	L	L	L
A ₃ =B ₃	A ₂ <B ₂	X	X	X	X	X	H	H	H
A ₃ =B ₃	A ₂ =B ₂	A ₁ >B ₁	X	X	X	X	L	L	L
A ₃ =B ₃	A ₂ =B ₂	A ₁ <B ₁	X	X	X	X	H	H	H
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ >B ₀	X	X	X	L	L	L
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ <B ₀	X	X	X	H	H	H
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ =B ₀	H	L	L	L	L	L
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ =B ₀	L	H	H	H	H	H
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ =B ₀	L	L	L	L	L	L
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ =B ₀	X	X	X	L	L	L
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ =B ₀	H	H	H	H	H	H
A ₃ =B ₃	A ₂ =B ₂	A ₁ =B ₁	A ₀ =B ₀	L	L	L	L	L	L

Notes

1. H = HIGH voltage level
L = LOW voltage level
X = don't care

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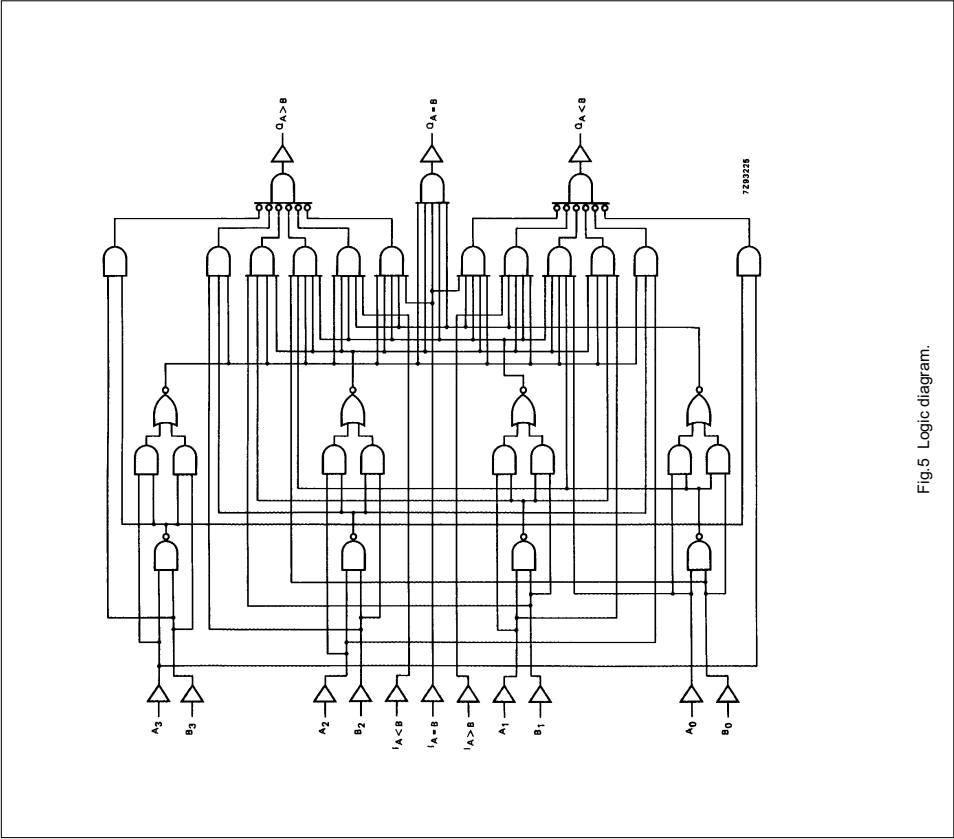


Fig.5 Logic diagram.

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DATA SHEET

For a complete data sheet, please also download:

- The IC06 74HC/HCT/HCU/HCMOS Logic Family Specifications
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Information
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Outlines

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Product specification

File under Integrated Circuits, IC06

December 1990

PIN DESCRIPTION

PIN NO.	SYMBOL	NAME AND FUNCTION
2	I _{A<B}	A < B expansion input
3	I _{A=B}	A = B expansion input
4	I _{A>B}	A > B expansion input
5	Q _{A<B}	A > B output
6	Q _{A=B}	A = B output
7	Q _{A>B}	A < B output
8	GND	ground (0 V)
9, 11, 14, 1,	B ₀ to B ₃	word B inputs
10, 12, 13, 15	A ₀ to A ₃	word A inputs
16	V _{CC}	positive supply voltage

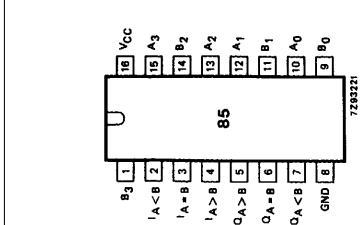


Fig.1 Pin configuration.

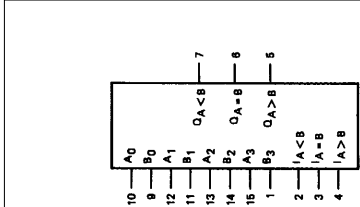


Fig.2 Logic symbol.

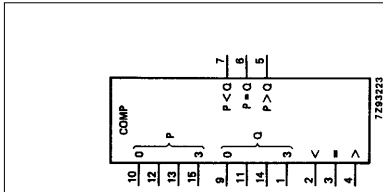


Fig.3 IEC logic symbol.

