

DATA SHEET

For a complete data sheet, please also download:

- The IC06 74HC/HCT/HCU/HCMOS Logic Family Specifications
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Information
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Outlines

74HC/HCT32

Quad 2-input OR gate

Product specification
File under Integrated Circuits, IC06

December 1990



Quad 2-input OR gate

74HC/HCT32

PIN DESCRIPTION

PIN NO.	SYMBOL	NAME AND FUNCTION
1, 4, 9, 12	1A to 4A	data inputs
2, 5, 10, 13	1B to 4B	data inputs
3, 6, 8, 11	1Y to 4Y	data outputs
7	GND	ground (0 V)
14	V _{CC}	positive supply voltage

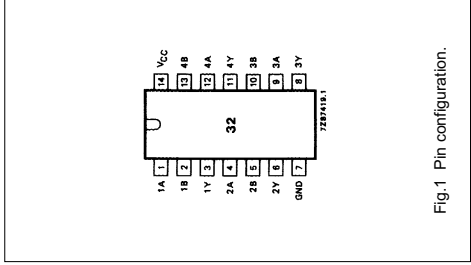


Fig.1 Pin configuration.

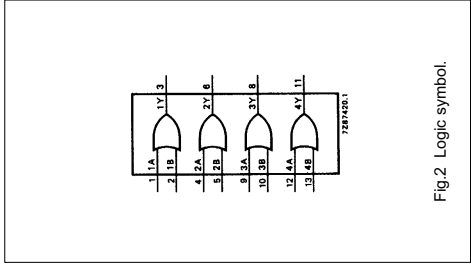


Fig.2 Logic symbol.

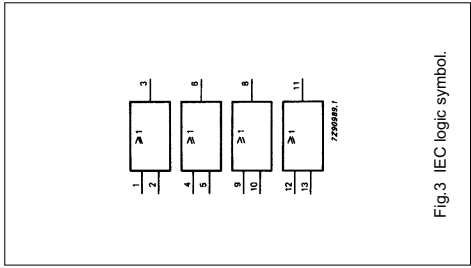


Fig.3 IEC logic symbol.

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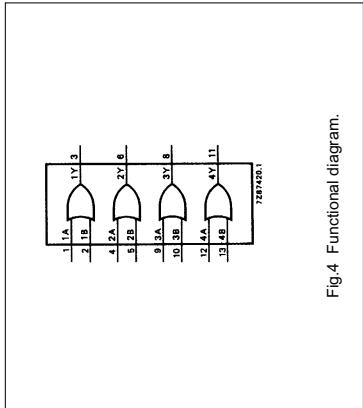


Fig.4 Functional diagram.

FUNCTION TABLE			
INPUTS		nB	OUTPUT nY
nA			
L		L	L
L		H	H
H		L	H
H		H	H

Notes
1. H = HIGH voltage level
L = LOW voltage level

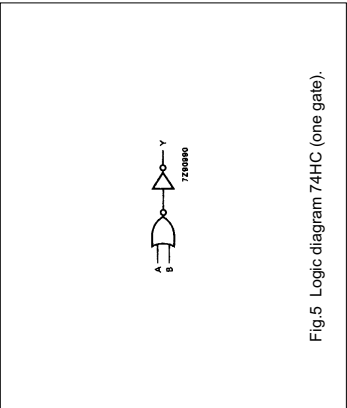


Fig.5 Logic diagram 74HC (one gate).

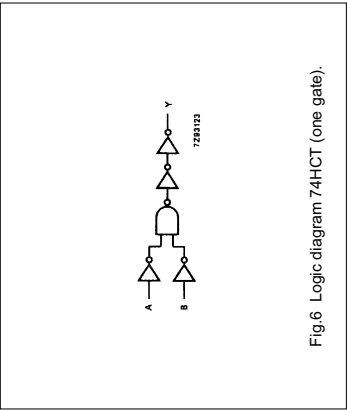


Fig.6 Logic diagram 74HCT (one gate).