

4-bit magnitude comparator

74HC/HCT85

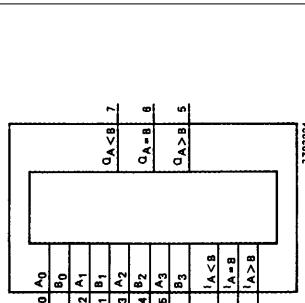


Fig.4 Functional diagram.

APPLICATIONS

- Process controllers
- Servo-motor control

FUNCTION TABLE

COMPARING INPUTS		CASCADING INPUTS		OUTPUTS			
A ₀ , B ₀	A ₁ , B ₁	A ₀ , B ₀	I _{A-B}	I _{A-B}	Q _{A-B}	Q _{A-B}	Q _{A-B}
A ₀ >B ₀	X	X	X	X	X	H	L
A ₀ <B ₀	X	X	X	X	X	L	L
A ₀ =B ₀	X	X	X	X	X	H	L
A ₁ >B ₁	X	X	X	X	X	L	L
A ₁ <B ₁	X	X	X	X	X	H	L
A ₁ =B ₁	X	X	X	X	X	L	L
A ₂ >B ₂	X	X	X	X	X	H	L
A ₂ <B ₂	X	X	X	X	X	L	L
A ₂ =B ₂	X	X	X	X	X	H	L
A ₃ >B ₃	X	X	X	X	X	H	L
A ₃ <B ₃	X	X	X	X	X	L	L
A ₃ =B ₃	X	X	X	X	X	H	L
A ₀ >B ₀	X	X	X	X	X	H	L
A ₀ <B ₀	X	X	X	X	X	L	L
A ₀ =B ₀	X	X	X	X	X	H	L
A ₁ >B ₁	X	X	X	X	X	L	L
A ₁ <B ₁	X	X	X	X	X	H	L
A ₁ =B ₁	X	X	X	X	X	L	L
A ₂ >B ₂	X	X	X	X	X	H	L
A ₂ <B ₂	X	X	X	X	X	L	L
A ₂ =B ₂	X	X	X	X	X	H	L
A ₃ >B ₃	X	X	X	X	X	H	L
A ₃ <B ₃	X	X	X	X	X	L	L
A ₃ =B ₃	X	X	X	X	X	H	L

Notes

1. H = HIGH voltage level
L = LOW voltage level
X = don't care

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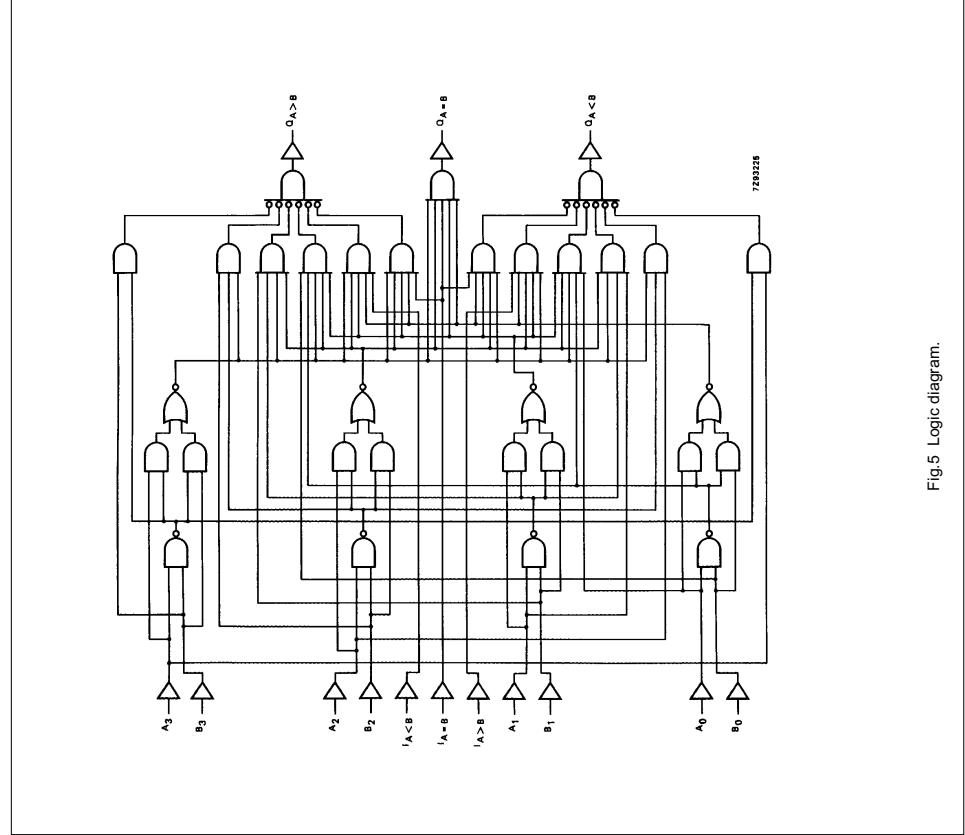


Fig.5 Logic diagram.

DATA SHEET

For a complete data sheet, please also download:

- The IC06 74HC/HCT/HCU/HCMOS Logic Family Specifications
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Information
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Outlines

74HC/HCT85 4-bit magnitude comparator

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Product specification
File under Integrated Circuits, IC06

Philips
Semiconductors



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Fig.1 Pin configuration

PIN NO.	PIN DESCRIPTION	SYMBOL	NAME AND FUNCTION
2	$I_{A < B}$	$I_{A < B}$	A < B expansion input
3	$I_{A = B}$	$I_{A = B}$	A = B expansion input
4	$I_{A > B}$	$I_{A > B}$	A > B expansion input
5	$Q_{A < B}$	$Q_{A < B}$	A > B output
6	$Q_{A = B}$	$Q_{A = B}$	A = B output
7	$Q_{A > B}$	$Q_{A > B}$	A < B output
8	GND	GND	ground (0 V)
9, 11, 14, 1,	B_0 to B_3	B_0 to B_3	word B inputs
10, 12, 13, 15	A_0 to A_3	A_0 to A_3	word A inputs
16	V_{CC}	V_{CC}	positive supply voltage

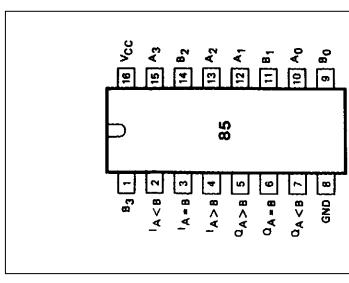


Fig.1 Pin configuration

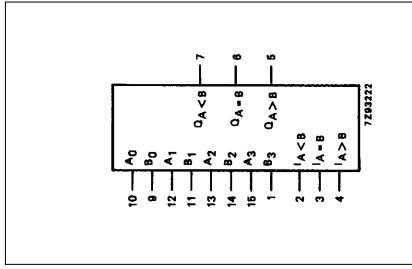


Fig.2 Logic symbol

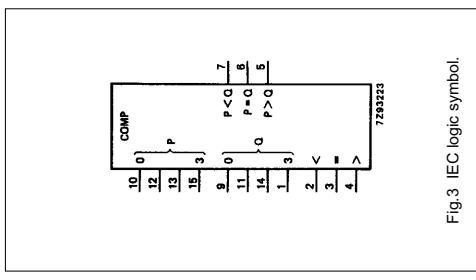


Fig.3 IEC logic symbol

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