

DATA SHEET

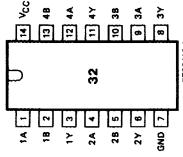
Quad 2-input OR gate

74HC/HCT32

74HC/HCT32

Quad 2-input OR gate

Fig.1 Pin configuration.



December 1990

Product specification
File under Integrated Circuits, IC06

Fig.2 Logic symbol.

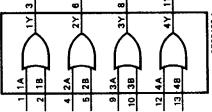


Fig.2 Logic symbol.

Fig.3 IEC logic symbol.

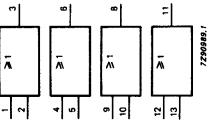


Fig.3 IEC logic symbol.



Quad 2-input OR gate

74HC/HCT32

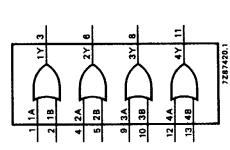


Fig.4 Functional diagram.

FUNCTION TABLE

INPUTS		OUTPUT	
nA	nB	nY	
L	L	L	L
L	H	H	H
H	L	H	H
H	H	H	H

Notes

1. H = HIGH voltage level
L = LOW voltage level
72874251

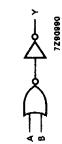


Fig.5 Logic diagram 74HC (one gate).

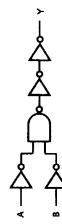


Fig.6 Logic diagram 74HCT (one gate).