

Quad 2-input NOR gate

74HC/HCT02

DATA SHEET

For a complete data sheet, please also download:

- The IC06 74HC/HCT/HCU/HCMOS Logic Family Specifications
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Information
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Outlines

PIN DESCRIPTION		SYMBOL	NAME AND FUNCTION
1, 4, 10, 13	1Y to 4Y	1Y to 4Y	data outputs
2, 5, 8, 11	1A to 4A	1A to 4A	data inputs
3, 6, 9, 12	1B to 4B	1B to 4B	data inputs
7	GND	GND	ground (0 V)
14	V _{CC}	V _{CC}	positive supply voltage

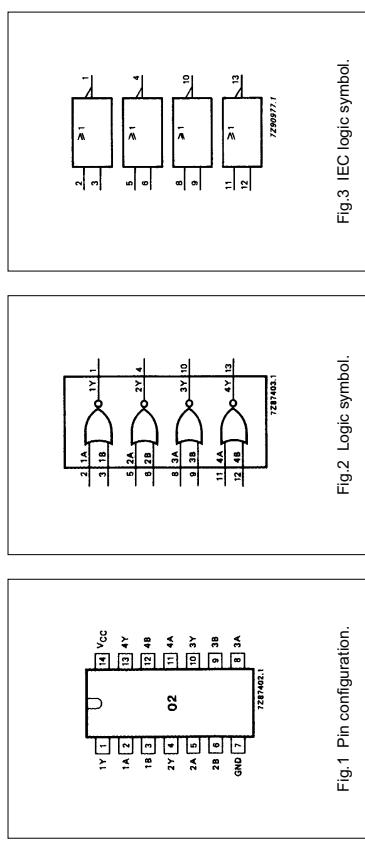


Fig.1 Pin configuration.

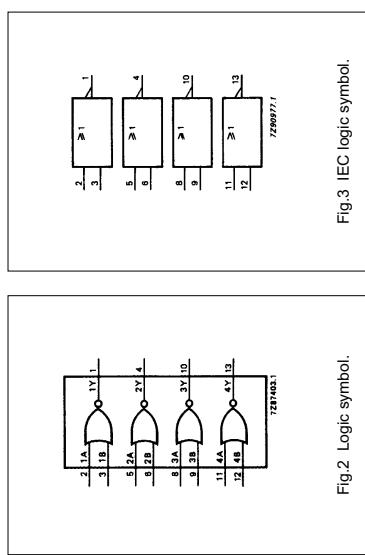


Fig.2 Logic symbol.

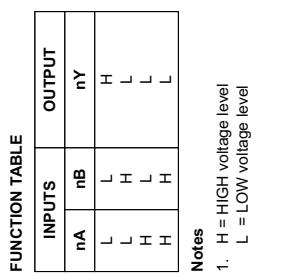


Fig.3 IEC logic symbol.

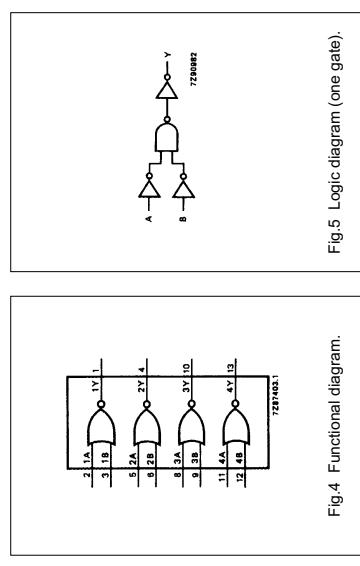


Fig.4 Functional diagram.

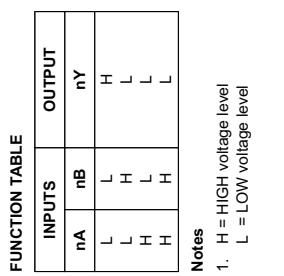


Fig.5 Logic diagram (one gate).



74HC/HCT02

Quad 2-input NOR gate

Product specification
File under Integrated Circuits, IC06

December 1990

PHILIPS