

Hex inverting Schmitt trigger

74HC/HCT14

TRANSFER CHARACTERISTIC WAVEFORMS

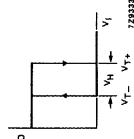
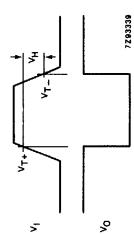
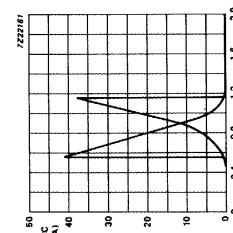
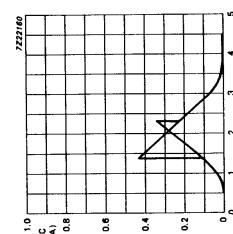
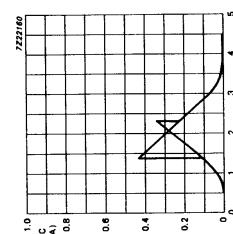
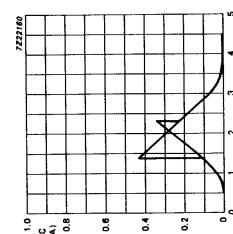


Fig.6 Transfer characteristic.

Fig.7 Waveforms showing the definition of V_{T+} , V_T^- and V_H ; where V_{T+} and V_T^- are between limits of 20% and 70%.Fig.8 Typical HC transfer characteristics; $V_{CC} = 2\text{ V}$.Fig.9 Typical HC transfer characteristics; $V_{CC} = 4.5\text{ V}$.Fig.10 Typical HCT transfer characteristics; $V_{CC} = 4.5\text{ V}$.Fig.11 Typical HCT transfer characteristics; $V_{CC} = 6\text{ V}$.

DATA SHEET

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- The IC06 74HC/HCT/HCU/HCMOS Logic Family Specifications
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Information
- The IC06 74HC/HCT/HCU/HCMOS Logic Package Outlines

For a complete data sheet, please also download:

PIN NO.	PIN DESCRIPTION	SYMBOL	NAME AND FUNCTION
1, 3, 5, 9, 11, 13	1A to 6A		data inputs
2, 4, 6, 8, 10, 12	1Y to 6Y		data outputs
7	GND		ground (0 V)
14	V _{CC}		positive supply voltage

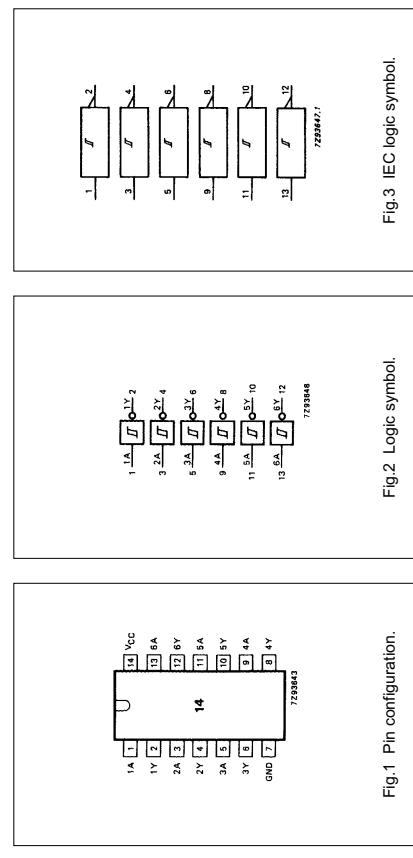


Fig.1 Pin configuration.

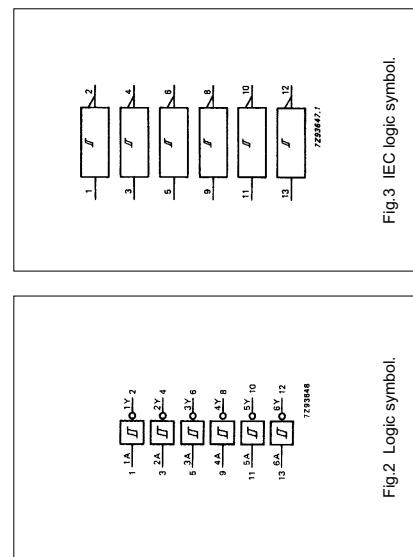


Fig.2 Logic symbol.

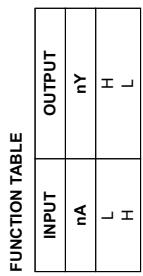


Fig.3 IEC logic symbol.

INPUT	OUTPUT
nA	nY
L	H

- Notes**
1. H = HIGH voltage level
 - L = LOW voltage level

APPLICATIONS

- Wave and pulse shapers
- Astable multivibrators
- Monostable multivibrators

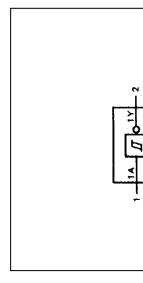


Fig.4 Functional diagram.

September 1993
Product specification
File under Integrated Circuits, IC06

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Philips
Semiconductors

