East West University

Department of Computer Science and Engineering Course Title: Computer Architecture, Course code: CSE360 Fall 2021, Quiz 2, Set A, Total Mark: 10, Time: 30 Mts. Instructor: Dr. Md. Sawkat Ali

Answer all of the following questions:

Total Mark 10

1.	Design an SRAM chip with all control signals having 512 KB memory	5
	location.	
2.	Write down the programing flow steps for the following arithmetic	5
	operation:	
	i) A=B/C	
	Note that, result (A) must follow the IEEE 754 standard.	