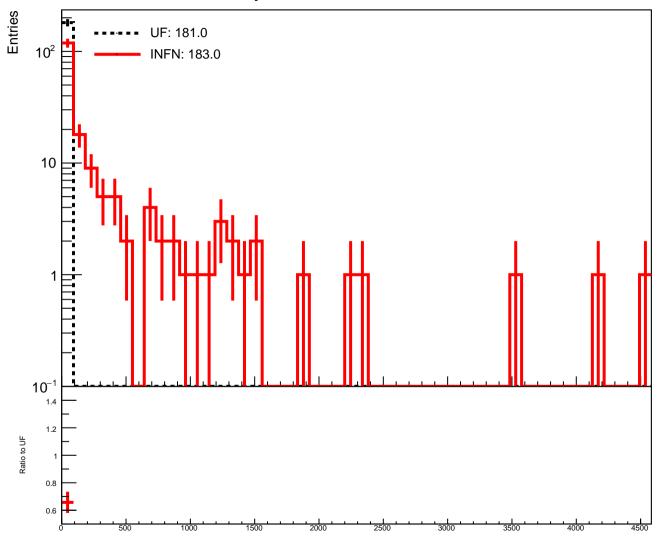
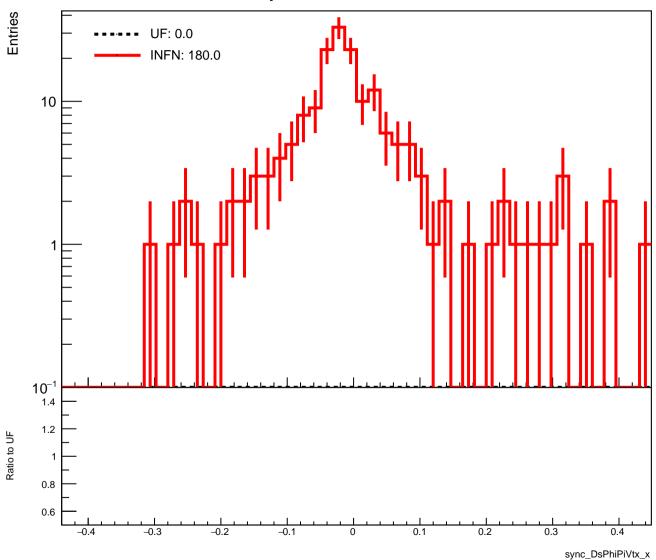


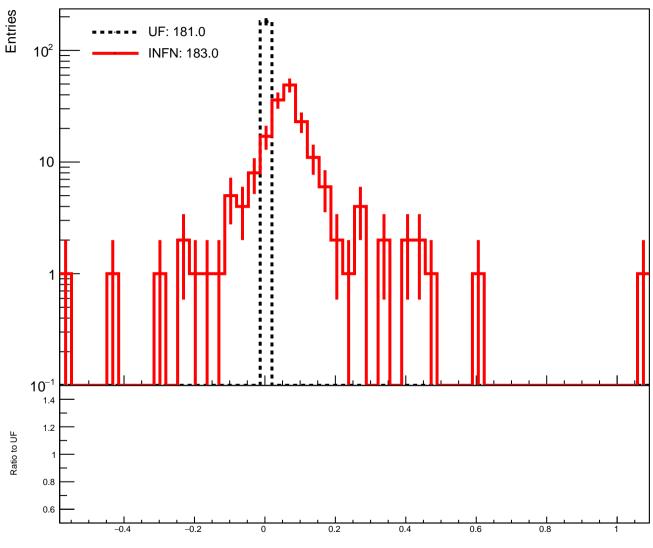
sync_DsPhiPiVtx_Chi2



sync_DsPhiPiVtx_x



sync_DsPhiPiVtx_y



 $sync_DsPhiPiVtx_z$

