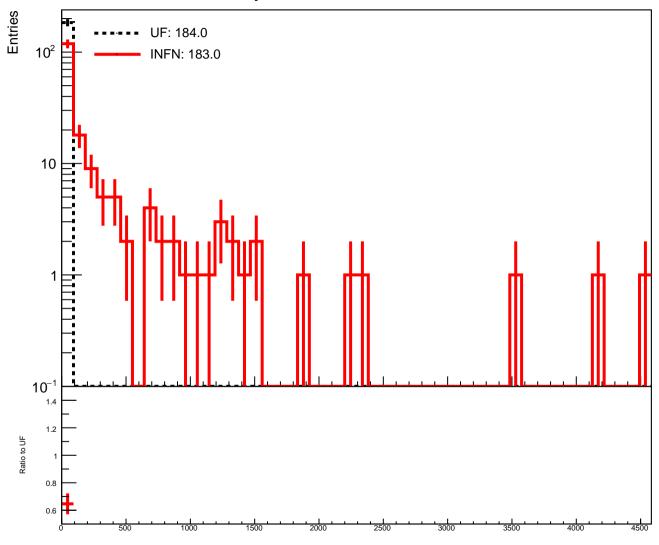
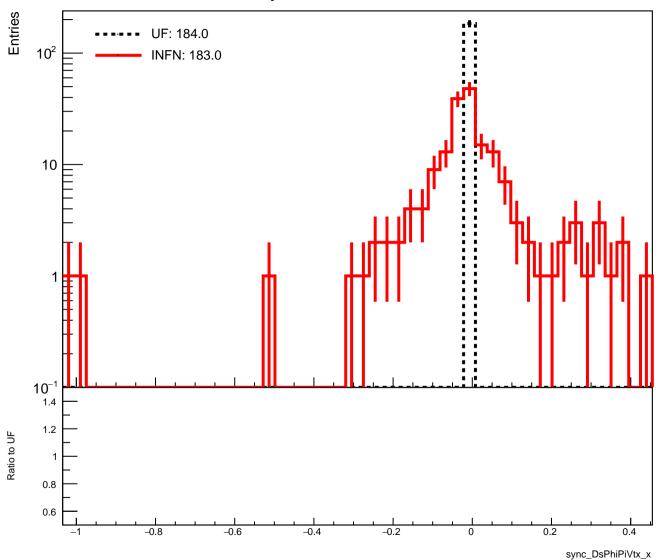


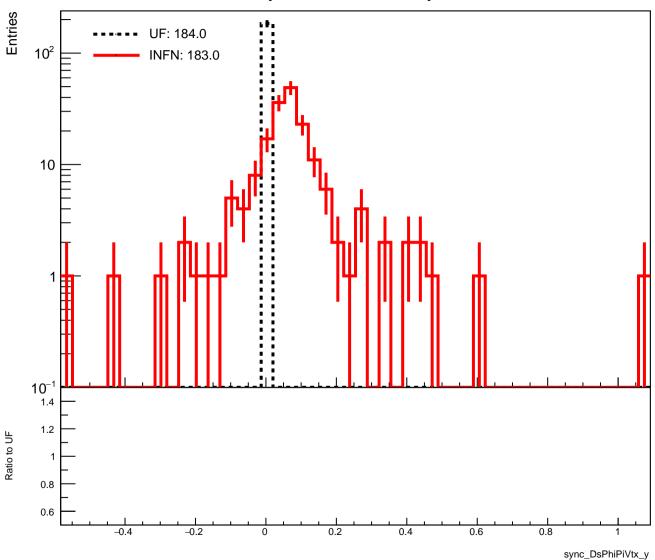
## sync\_DsPhiPiVtx\_Chi2



 $sync\_DsPhiPiVtx\_x$ 



sync\_DsPhiPiVtx\_y



 $sync\_DsPhiPiVtx\_z$ 

