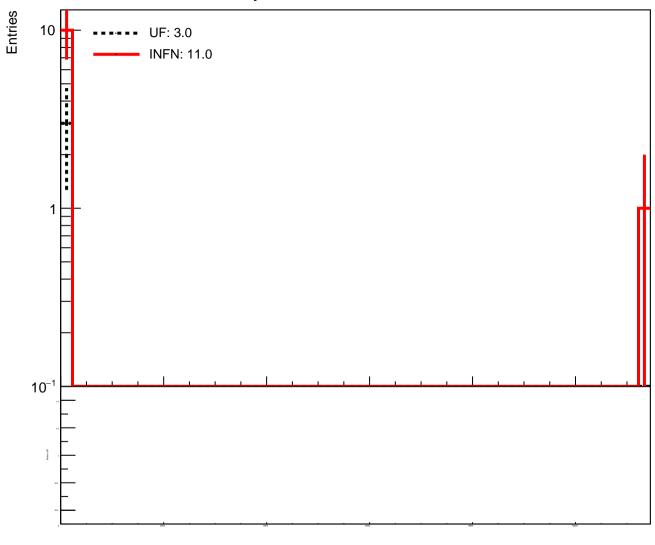
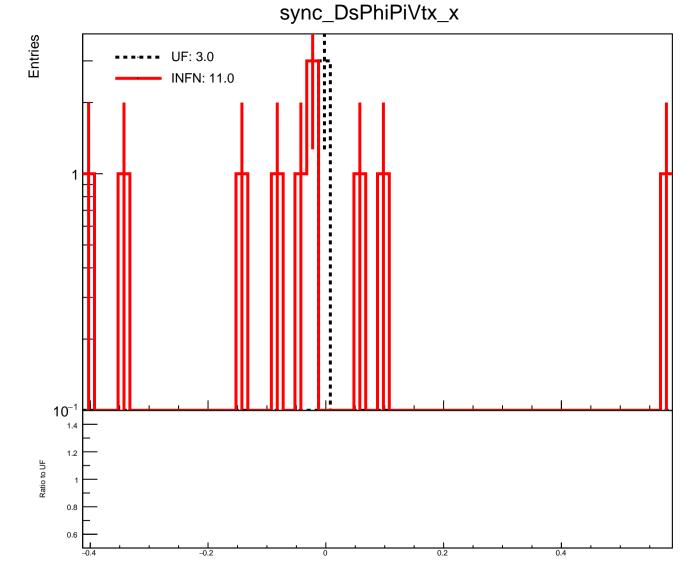
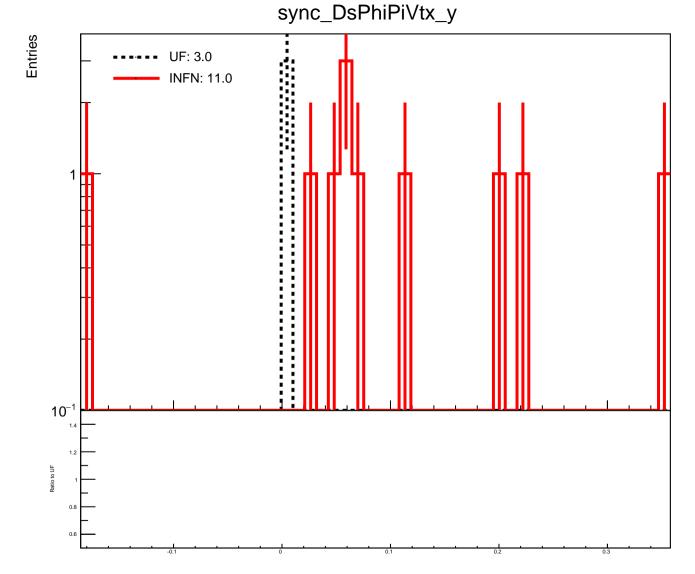


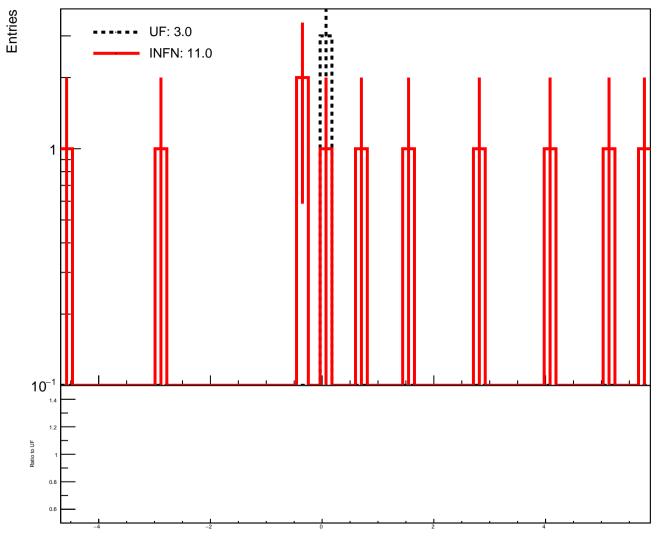
sync\_DsPhiPiVtx\_Chi2

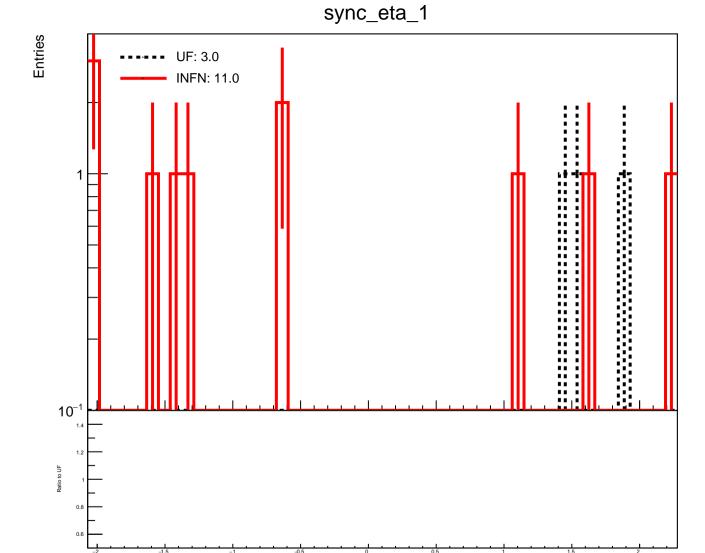


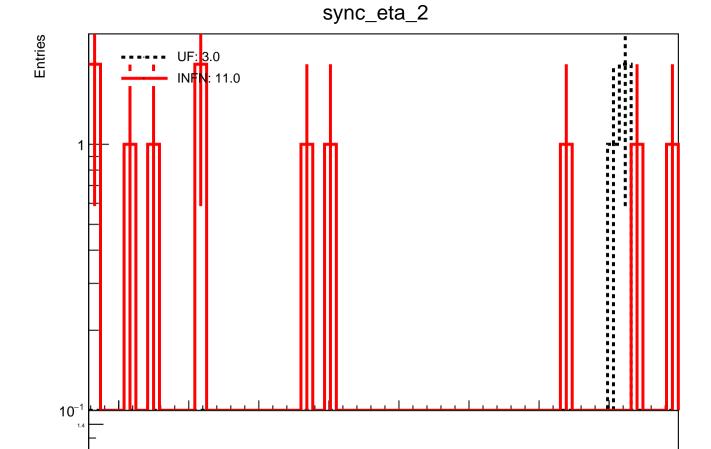












Ratio to UF

0.8

0.6

