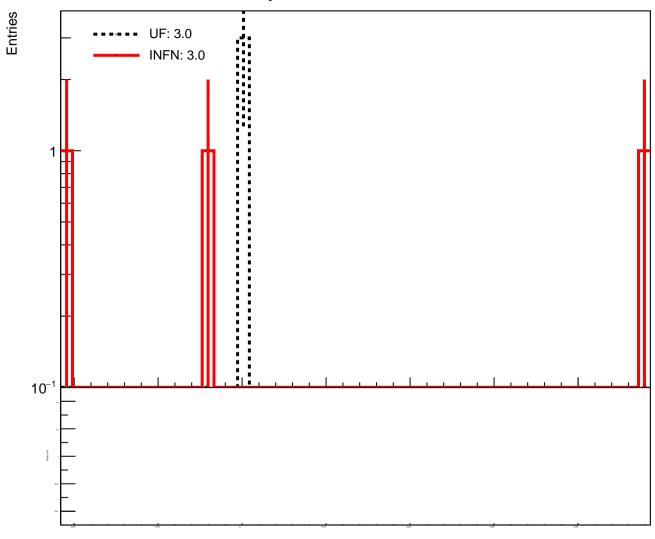


sync\_DsPhiPiVtx\_Chi2 Entries  $10^{-1}$ 

 $sync\_DsPhiPiVtx\_x$ 



sync\_DsPhiPiVtx\_y

