

UNIVERSITE LIBRE DE BRUXELLES
Faculté des Sciences
Département d'Informatique

Practical case of Black-box driven verification

Thomas Van Gysegem

Promotor : Prof. Gilles Geeraerts

Thesis in Computer Sciences

Academic year 2017 - 2018

abstract

This thesis will describe a practical case of software testing and explain all the choices that were made during the implementation process. It will also try to provide some generic way of doing the same for a different use case given a similar running environment and constraints.

Program testing can be a very
effective way to show the
presence of bugs, but it is
hopelessly inadequate for
showing their absence

Edsger W. Dijkstra , The
Humble Programmer (1972)

Contents

1	Introduction	8
I	Preliminaries	10
2	The economics of software testing	10
3	Our case	12
4	Related works	13
5	Conclusion	13
II	Software development and software testing	14
6	Waterfall	16
7	Prototyping	17
8	Agile	18
9	Validation and verification	20
10	Code coverage	21
11	Black-box and white-box testing	22
11.1	Grey-box testing	23
12	Static and Dynamic testing	23
13	Type of testing	24
13.1	Unit testing	24
13.2	Integration testing	24
13.3	Functional testing	26
13.4	System testing	27
13.5	Acceptance testing	27
13.6	Regression testing	28

13.7 Beta testing	28
14 Requirements-based testing	28
15 Conclusion	29
 III Formal Verification and Model Checking	 30
16 Finite State Machine	31
16.1 Description of Finite State Machine using graphs	34
17 Acceptors	34
18 Language	34
19 Non-Determinism	35
20 Alternating Finite Automaton	37
21 Timed Automata	38
22 Markov model	43
23 Applications	43
23.1 UPPAL	43
23.2 PRISM	45
24 Conclusion	46
 IV Case study	 47
25 Railnova	48
25.1 Existing system	48
25.2 Railster Universal Gateway	49
25.3 Short story	50
25.4 The Railnova Way	51
26 Conclusion	52

V	Contribution	53
27	Requirements analysis	54
27.1	Functional testing	55
27.2	System testing	55
27.3	Regression testing	57
28	Requirements satisfaction	58
29	Interfaces	58
29.1	Output(/Inputs) providers	60
30	Framework	63
31	Application	66
32	Conclusion	68
VI	Results	69
33	Domain-specific language	70
33.1	Create a scenario	71
33.2	Add states to scenario	71
33.3	Add transition to state	73
33.4	Set initial state	73
33.5	Manipulate clocks	73
33.6	Manipulate data sources	74
33.7	Determinism	75
34	Nursery	76
34.1	Architecture	76
34.2	Configuration	77
34.3	Command line tools	80
34.4	Web-administration	82
VII	Conclusion	87

A	Abstraction of Inputs/Outputs	89
A.1	Outputs	89
A.1.1	How does it works	89
A.1.2	Wanesy	90
A.1.3	Log	90
A.1.4	Miscellaneous	91
A.2	Inputs	91
A.2.1	Power Management	91
A.2.2	Connectivity	91
A.3	The generic way	92
A.3.1	Outputs	92
A.3.2	Inputs	93
B	Test cases	94
B.1	Timeout check	94
B.2	Date check	94
B.3	Battery cycle	95
	References	97
	Glossary	101

1 Introduction

Nowadays, software is everywhere. In banks, cars, vending machines, airplanes, televisions, ... You are using software in your everyday life even without knowing it and when you do, you expect it to meet some expectation, you don't want your plane to crash because of a software failure or a vending machine stealing your money. That's why some guidelines and processes exist to build those software. Those processes can last for days, months or even years before producing the expected software and during this process, some errors can be made that makes the software faulty or broken. That's why, all along this process, we need to make sure that everything is working just fine. To do that, we introduce a step in the development process called "testing". Without it, no complicated software will be able to do its task without problems. Of course, this testing step can be done differently depending on the overall development process but nevertheless, every software needs to go through that step.

This thesis is all about this particular testing step. The goal of it will be to provide a way of automating this step. As we will explain later on, providing this automatic process is of major interest in terms of time gained in the development process and reliability for the final software. Of course, detecting all problems during testing is a really complex task and the process developed throughout this thesis will only help to reduce the risk of shipping faulty software and to make sure that previously encountered/corrected problems are not re-introduced between different versions of it.

To guide us during this work, we will focus on a concrete case study that was encountered at the Railnova company in Brussels. Of course, as the goal of this work is to stay generic, we will go from the concrete examples solution to more generic solutions that can easily be extended for any other software. Despite the fact that this thesis will try to stay generic, we will not talk about hardware testing even if we can see that as a particular case of testing in a development process. We think that focusing on software development will help the reader to get a better understanding of the principles used and developed ideas.

I divided this work in many blocs that introduces them-self one after the other. As this topic and all the techniques involved are closely related, there

will often be references to area not already covered during the sequential reading of this thesis: First, the preliminaries will show why this work was started and what is the need that it satisfies. Then, we'll introduce software testing. We'll try to give an overview of all the main techniques and good practices currently used. After that, we will introduce the main tool we used to complete the task at hand, the Finite State Machine (FSM). We will also give various alterations of FSM which are relevant in our case. Then, we will introduce the context in which this work was done, where and why we had to solve such a task. Following this, we will show how we used all the previous chapters concepts to develop our solution, what was used or not in the previously covered topics and how we used them all together. Lastly, we will present the final product that was developed. Showing how it works and what it can do.

Part I

Preliminaries

In the following chapters, we will try to explain why software testing is an important task in any IT related business. We give some example of problems that can be solved using the product, Nursery, that was developed in this thesis.

2 The economics of software testing

Usually, when you work on a development project, you have many different development stages. The main ones are the proper development of the product and the final stage of the product, when it is shipped to the production environment.

Shipping the product to production simply means that you put the product into its real environment. In other words, you begin to manipulate real world data and not the test sets that you were probably using during the development. In our case, we install a RUG running Railster-OS in some engine.

But if the shipped product contains faults, it will probably begin to produce incorrect results and, depending on the product, it can even harm people or destroy things by using them incorrectly. The client will not be able to use the product he paid for to its full capabilities and will maybe want to switch to another company that produces more reliable products. It is easy to imagine other cases in which having a fault in the final product must imperatively be avoided.

All of this comes with a cost, for example, we can imagine that we are working on an assembly line controller. If our controller (product) does not work correctly, the assembly line can be damaged because of incorrect usage or the materials being manipulated can be damaged too resulting in repair cost for the assembly line and incorrect/damaged product assembled that cannot be sold afterwards.

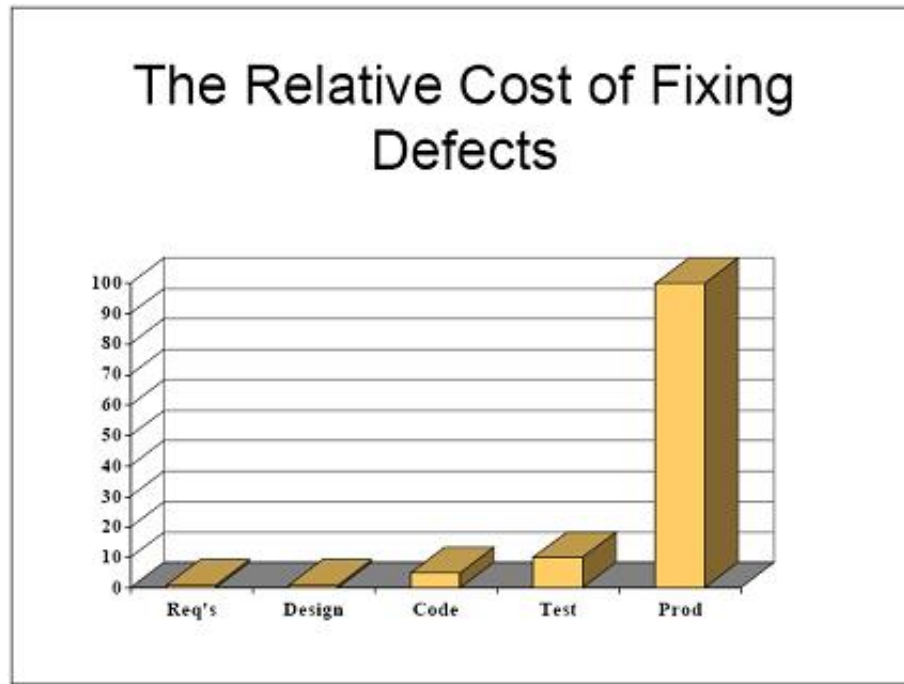


Figure 1: Relative costs of fixing faults [1]

In the Railster Universal Gateway (RUG) ecosystem, if our product, Railster-OS isn't working well, the RUG its running on will not send the intended data to the client. For him, it is like the engine itself is malfunctioning or not functioning at all if we are unable to send any data! He will then need to send a team to the engine to find the problems and a lot of work will be delayed because the engine is not running and, of course, every step in this process is very costly for the client and for the company itself because when a product in production is faulty, we also need to get back the devices that are malfunctioning and, perhaps, every other device running the same software of having the same hardware. And finally, we need to replace them and invest time in correcting them or, at least, solving the issue for the other product versions.

It is commonly believed that the earliest a fault is found, the cheaper it is to correct it [1] [2]. The case we mentioned is the latest possible time to detect a fault so, according to the previous statement, it is also the most

expensive! Those reasons are why we want to create some process that can be used to drastically reduces the amount of unexpected faults in the final product. That's the main motivation behind the project that is the subject of this thesis called Nursery.

3 Our case

We will now try to briefly introduce the problem we solve in this thesis with some of the use cases we had to solve. We are working on an embedded device that can be installed in trains to collect data and monitor them. This device is controlled using a software we develop and this software provides features. We want to check that, every time we update a new version of the software that is used to control the device, every features is still working as expected. Some of those features are:

- Data flow: We want that data are retrieved at least every 30 seconds.
- Date: We want the device date to be synchronized with the real date.
- Battery: Device battery must be able to power the device when it is not powered by the train.

In this thesis, we develop an approach that can be used to answer the following questions for every new version of the device and/or the software that controls it:

- «Are all the feature still working as expected ? »: Does my system fetch data every 30 seconds ? Does the battery work ? System date is still synchronized ?
- «Does the newly introduced feature works ? »

To answer those questions, we will need to know how the software is performing and to know that, we will need to analyze it's output, react to them to simulate the desired environment. In this context, outputs can be data, reports, logs, screenshots and many other things as long as they are produced by the analyzed program. Testing different environment allows us to answer questions like the following ones:

- What happen if the device is not connected to any equipment ?
- What happen when the train is shutdown ?
- What happen while the train is moving ?
- What happen when temperature is under some threshold ?

We will try to answer all those questions for any software or hardware using user defined scenario used to check that the software is working as expected. You can see the final implementation of those three use case in Section B.

4 Related works

This thesis is not the first attempt at software verification automation. Some tried to apply FSM to verify the design of a given software [3]. However, this approach is not useful to check the actual implementation of that design or to check the data-manipulation done by that implementation. Another work tried to use FSM to provide insight on test cases that should be designed [4]. While both techniques provides useful information for software development, none of them does a complete black-box verification of an implemented system which is what we are trying to achieve in this thesis.

5 Conclusion

We have explained briefly what is the final product expected from this thesis. Keeping that in mind should help you to understand the following chapters and the reasons why we need to talk about software development and finite state machine.

Part II

Software development and software testing

Software development is a complex area and different processes exist but at the very last, they all have some key principles in common. One of them being the testing part. For this work to be fully understood, we need to introduce a bit of software development and then, explain which are the different techniques used in software testing. This will provide us with the required technical vocabulary to describe our own testing process.

Software development can be defined in different ways, every one of them being more or less equivalent but in this work, we will focus on the basis and simply say that software development is some kind of plan to produce a given software. This plan can be more or less complex and must involve at some point, some testing steps.

Software is usually ordered by a client that can be ourselves, some company or any set of users [5]. Clients have needs and our task as software developer will be to fulfill them by producing a software. The client's request will first be translated into requirements specifications that, to put it simply, are formal descriptions of them. We usually create those requirements specifications during meetings with the client, this requires meetings and discussions to be sure that the idea we get from them is what they want.

Then the software can be built. Of course, if we got the wrong requirements specifications, our software will not be what the client asked and therefore, they will want us to fix that and update the requirements but modified requirements can invalidate the chosen architecture or software decisions thus requiring in-depth modification of the current built product. Let's assume that those requirements are good, then, once the software is built, we want to test it to ensure that it works and that it meets the given requirements. Once those tests are successfully passed, we can ship the software to our client.

This whole process, starting from the client order to the shipping of the produced software is what we call the software development process. Of

course, there is not one only way of doing it even for the most simple process like this one their can be divergence of work-flow from one team to another. [6] The goal here is to show that testing is crucial whatever the chosen methodology or software development process, we will now introduce three of them that we think are the most used or accessible for neophytes:

- Waterfall
- Prototyping
- Design: resulting in the software architecture
- Coding: the development, proving, and integration of software
- Testing: the systematic discovery and debugging of defects
- Operations: the installation, migration, support, and maintenance of complete systems

6 Waterfall

The waterfall model is probably the most basic development process still used today, it is often attributed to Winston Walker Royce in a paper from 1970 [7]. The whole process is sequential or plan driven. The whole process is divided in big parts and each of them is done only one time and one after the other. The original model was designed as follows:

- System and software requirements: captured in a product requirements document
- Analysis: resulting in models, schema, and business rules
- Design: resulting in the software architecture
- Coding: the development, proving, and integration of software
- Testing: the systematic discovery and debugging of defects
- Operations: the installation, migration, support, and maintenance of complete systems

The following problems can easily be spotted in this process:

- If an error is made during the analysis step, this error is propagated to the next steps.
- Between the first and the last step of it, there can be a lot of weeks, months or years! During this time, the project needs can evolve and then, the delivered product will only match outdated requirements, which, of course, is not a good thing for the client.
- If some problems occurs during the writing of the product requirements, let's say that the client forget to talk about a key feature. Then, again, the product we will deliver will miss that feature.

Probability to produce the wrong product is high because of those problems and because of that, organizations/teams tends to avoid it when possible. Nevertheless, there is still advantages to use this process. As the first

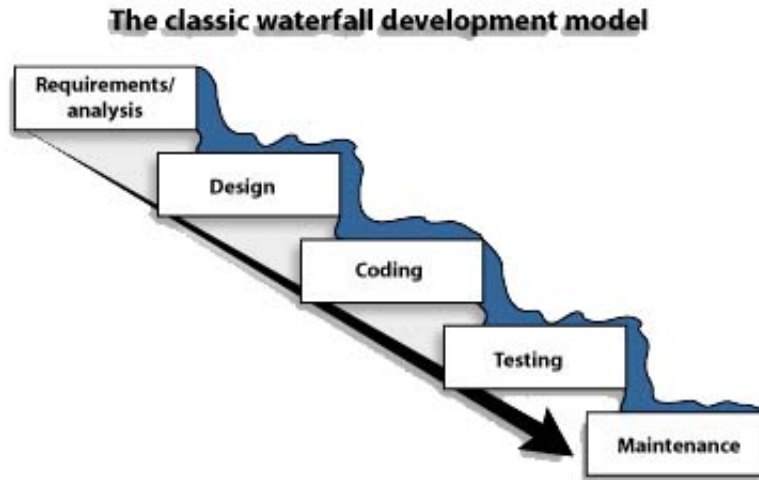


Figure 2: Waterfall model. You should see why it is called waterfall from this picture.

steps are strongly related to documentation of the product, if a team member leaves during the process, we have less risk of losing knowledge of what has been done or what is left to do. New team members will simply need to read the requirements, the produced models and the software architecture.

This process is also one of the easiest to understand, every step of the project is well documented and one can easily jump in any project using this methodology and be ready to participate in a very short time.

7 Prototyping

Sometimes, you want to produce a product as soon as possible so you can conquer the market sooner. In that case, the waterfall approach is not going to be very useful. The prototyping process though, can help you towards this goal. [8]

The idea is to produce some prototype as soon as possible, test it and then, iterate over it again and again until the final product is fully func-

tional. It is a trial-and-error process. This works especially well when the final product requirements are not fully known. By presenting a prototype to the client, he is able to give feedback and explain the following most wanted features. Like for the waterfall model, we have a plan for the whole process: The whole product is divided into as many sub-product as needed. Each of those division will be prototyped on top of the last ones until we get the final product.

- System requirements are defined in as much details as possible.
- A first design is created.
- The first prototype is created.
- Client tests/reviews the prototype.
- Developers change the prototype accordingly.
- We repeat those steps as many times as needed.
- The final product is constructed based on the last prototype.

Using this model, the probability to deliver a product that does not meet the client requirements is very small as he can give a feedback throughout the whole development process. But there are some drawbacks to that.

Focusing on small sub-problems/sub-features distracts the developers from conducting a good and in depth analysis. This is also due to the lack of fully specified requirements from the start. Adding more and more features on top of existing ones can quickly create a complicated product both difficult to maintain and improve.

8 Agile

Agile Software Development is based on twelve principles: [9]

- Customer satisfaction by early and continuous delivery of valuable software

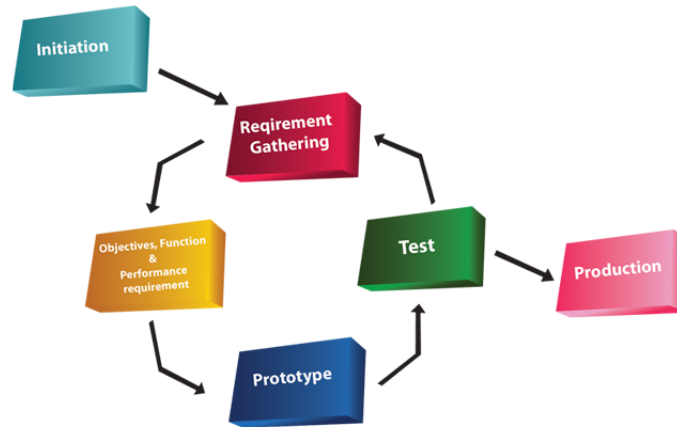


Figure 3: Schema of the prototyping process.

- Welcome changing requirements, even in late development
- Working software is delivered frequently (weeks rather than months)
- Close, daily cooperation between business people and developers
- Projects are built around motivated individuals, who should be trusted
- Face-to-face conversation is the best form of communication (co-location)
- Working software is the principal measure of progress
- Sustainable development, able to maintain a constant pace
- Continuous attention to technical excellence and good design
- Simplicity or the art of maximizing the amount of work not done is essential
- Best architectures, requirements, and designs emerge from self-organizing teams
- The team reflects on how to become more effective, and adjusts accordingly

There exist many different frameworks that complies to those principles. One of the most commonly used is Scrum [10] 4. With this framework, we work using sprints. A sprint is the basic unit of development in Scrum. The team fixes the time-frame during which the sprint must be done and then, fixes the scope of work that needs to be done during a Sprint Planning event.

At the end of each day, the team holds a Daily Scrum to answer the following questions:

- What did I do?
- What will I do to meet the sprint goals?
- Is there anything that block the team progress?

Finally, at the end of the sprint period, the team holds a Sprint Retrospective to answer the following questions:

- What went well?
- What could be improved?

This allows for a very dynamic development process in which no one gets stuck too long on ineffective or impossible processes.

9 Validation and verification

Now that we have settled down our requirements specifications, we need to introduces the validation and verification topic. Validation and verification is what we use to make sure that our software is meeting the previously specified requirements. In short, that our software is exactly what we specified. Failing to this would produce a software that will maybe not meet the clients requirements and, therefore, be of little or no use to him.

Both concepts are closely related but are completely independent. Software validation is a procedure used to ensure that the software meets the user's needs and that the specifications were correct while software verification is a procedure used to ensure that the software follows the specified requirements [11]. Following is a more formal definition:

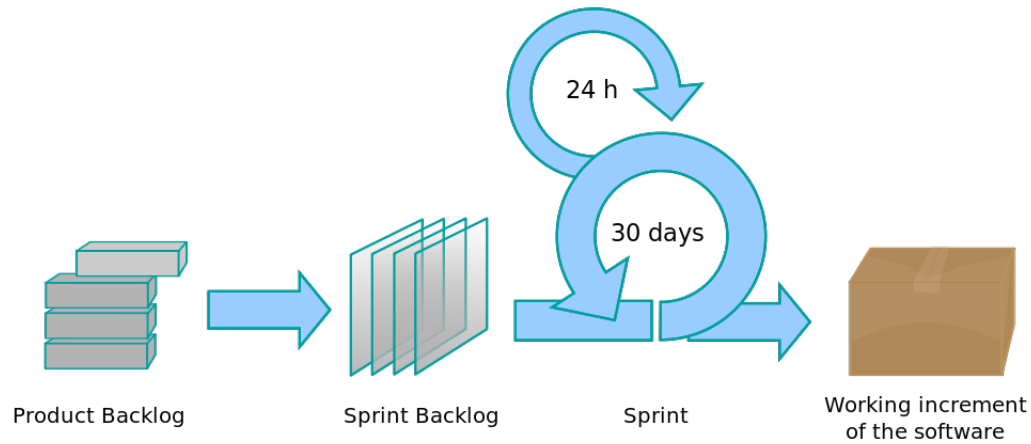


Figure 4: Schema of the Scrum Framework. A popular development process. This is only an example, the sprint duration is not always 30 days.

- **Validation** is the confirmation by examination and through provision of objective evidence that the requirements for a specific intended use or application have been fulfilled [12]
- **Verification** is the confirmation by examination and through provision of objective evidence that specified requirements have been fulfilled [12]

Simply put, validation is answering to «Are we trying to build the right thing?» while verification is answering to «Have we made what we were trying to make?». Of course, in our case, we are more interested in the verification part and we will leave the validation part out of this work. We will assume that the requirements are correct and therefore that if we follow them, the built software will be the one our client wants.

10 Code coverage

Code coverage is a metrics used in software testing that basically tells you how much of your code has been tested. It can be computed on many different criteria like how many subroutines have been called, how many lines of

code have been interpreted and so on.

The idea behind this metric usage is that, if all of your code is tested, then there is probably no bugs left, or at least, there is a good probability that we will encounter fewer bugs in a completely tested software than in an untested one. Getting a good code coverage is therefore a key point in testing as it improve the software quality by reducing the probability of faults left in it [13].

You can compute code coverage differently based on the testing requirements. Here is a non exhaustive list of some criteria you can use to compute it:

- **Function coverage** Percentage of defined functions called during testing.
- **Statement coverage** Percentage of statements called during testing.
- **Branch coverage** Percentage of branching possibility executed during testing.

Using one or several code coverage criteria, you can define a threshold used to certify that the product is unlikely to contains fault. For example, you can requires that 95% of defined functions are called during tests.

11 Black-box and white-box testing

Black-box testing and white-box are two terms used by software testers to describe how they approach their testing [14]. They describe two of the most common used techniques in software testing.

With black-box testing, also known as functional testing, we consider the software as a black-box and we have no knowledge of what is inside of it nor on how it works. This is the opposite of white-box testing where the whole software is known from the inside, all the code is accessible and we have a deep knowledge of how it works.

Both of those approaches have their own advantages and drawbacks. In white-box testing, the tester is testing functionalities that are already developed and he can miss needed features that have been forgotten. He has a very

good code coverage as he can produce test cases for every specific details by reading the code. White-box provides an internal perspective of the software.

Black-box testing, on the other hand, provides another point-of-view and does not require a deep knowledge of the code to operate. Test cases are designed following the requirements and not the code itself so if all the tests are successful and that all the requirements are entirely covered by the designed test, we have a proof that our software meets the requirements.

White-box testing is more or less done by developers themselves while black-box testing can be performed by anyone with poor programming skills. Code coverage can not be used while doing black-box testing for the obvious reason that you don't know what is done internally so you don't know what part of the software code is interpreted.

11.1 Grey-box testing

There is also a third option there which is called «Grey-Box testing ». We are doing Grey-Box testing when we do both Black-Box testing and White-Box testing. In Black-box testing, you don't have knowledge of the internal structure of code while in White-box testing, you do have this knowledge. Grey-box testing is when you do have a limited knowledge of the internal structure of code. You can get an idea of this with the following scenario: Let's say we want to check a sign-in feature for a service where you have a client software and a server that provides the service. If you have access to the source code for the client software, testing the sign-in feature using this knowledge is called Grey-box testing as you only have limited knowledge of the underlying structure of code.

12 Static and Dynamic testing

Two other terms used in software testing are static and dynamic testing. Static testing is the process of reviewing something that is not running. For example, some compilers (like GCC) perform static testing by finding unaffected variables, unused variables, dead code, syntax errors and so on. Dynamic testing, on the other hand, is performed while the program is running [15].

13 Type of testing

There exists a lot of different types of software testing, explaining them all is way out of the boundaries of this work so we have chosen 6 of them that are closely related and can be spanned on a whole software development process [16].

13.1 Unit testing

Scope: White-box testing

Who: Coder

Unit testing is a software testing method by which individual units of source code, sets of one or more computer program modules together with associated control data, usage procedures, and operating procedures, are tested to determine whether they are fit for use. The goal is to isolate each part of the program and show that the individual parts are correct [17].

In a more practical point-of-view, it's about isolating each function or method in the code and test their behavior for a wide range of input values. A lot of faults can stay undetected after this step. One of the reason of this being that people who write unit-test have a deep knowledge of the underlying code. This knowledge introduces biases and influences them when writing unit-test so it is easy to miss use cases because the programmer assume unconsciously that they are working. Also, if those who write the unit-test are the same ones that wrote the code that is being tested, the test code is likely to be as faulty as the code being tested itself!

13.2 Integration testing

Scope: Black-box and white-box testing

Who: Coder

Integration testing is the phase in software testing in which individual software modules are combined and tested as a group. It occurs after unit testing and before validation testing. Integration testing takes as its input modules that have been unit tested, groups them in larger aggregates, applies tests defined in an integration test plan to those aggregates, and delivers as

Listing 1: Brief example of what unit testing is (C pseudocode)

```
/**
 * @brief Reads a file content
 * @param name Name of the file
 * @param buffer Buffer for the file content
 * @return 0 and buffer is filled with the file content.
 * -1 if file does not exists
 * -2 if file can't be read
 */
int readFile(const char* name, const char* buffer);

// Tests fake file name
assert(readFile("doesnotexist.txt", buffer) == -1);

// Tests file can't be read
assert(readFile("busyFile.txt", buffer) == -2);

// Tests file read success
writeFile("file.txt", "some text");
assert(readFile("file.txt", buffer) == 0);
assert(buffer == "some text");
```

its output the integrated system ready for system testing [18].

A good example for this would be a serial communication library. You have a module with several basic functions/blocs for serial devices manipulation like:

1. Sending a byte over a serial port
2. Receiving a byte using the serial port
3. Opening a serial port
4. Closing a serial port

An integration test could be done by checking by having a device that sends data through the serial port and expects the same data as a result. In this example you would have to use all the basic blocs that determines the module features.

13.3 Functional testing

Scope: Black-box testing

Who: Independent tester

Functional testing is about testing each functionality of the whole system as described in the requirements.

Functional testing tests a slice of functionality of the whole system. Functional testing does not imply that you are testing a function/method of your module or class. If the functionalities wanted are:

1. Sending invoices to clients
2. Creating invoices
3. Adding clients

Then you will have test cases that try to add clients, another one that try to send invoices and acknowledges that the client, preferably a fake one, received it and a last test case that will create various different invoices.

13.4 System testing

Scope: Black-box testing

Who: Independent tester

System testing involves putting the new program in many different environments to ensure the program works in typical customer environments with various versions and types of operating systems and/or applications [16]. System testing is testing conducted on a complete, integrated system to evaluate the system compliance with its specified requirements [12].

This is very relevant in computer science where every computer is built from different component and can have many different operating systems. To perform system testing, you will usually run the program on different operating systems (Unix based systems or DOS based systems) and also on different hardware configuration. Of course, testing every possible combination is not possible so some exotic systems can still prove themselves unable to run the program after this steps. Of course, if your program is built to run on a specific predefined system, this testing will be really straightforward.

A basic example of system testing would be two computer, one running a Unix based operating system and the other running Windows for example (any version will do). In this case, the system test would be to run the program in both environment (Unix based OS and Windows) and check that, given the same inputs, both of them produces the same results or at least, similar enough ones. In other words, verify that they both works as expected.

13.5 Acceptance testing

Scope: Black-box testing

Who: Client

Acceptance testing is formal testing conducted to determine whether or not a system satisfies its acceptance criteria (the criteria the system must satisfy to be accepted by a customer) and to enable the customer to determine whether or not to accept the system. More formally, the system is tested to verify that it meets the given specifications or contract.

Usually, the user will provide some test or use case for each desired functionality. You can add your own test to those but at least the client test must be satisfied before trying to deploy the product otherwise the client will probably not even try the product.

13.6 Regression testing

Scope: Black-box and white-box testing

Who: Coder

Regression testing is a type of software testing used to ensure that a software that was previously developed and tested still meets the previously defined requirements after a modification has been introduced into it such as a bug fixes or enhancements. The purpose is to make sure that the newly introduced modifications do not damage any previously-working functionality.

Practically speaking, we will only add new test when we are adding new functionalities and we will remove test only when the functionality covered is no longer needed or maintained. Then, if all the tests are still running without error on our new software, we will assume that previously developed functionality still works.

13.7 Beta testing

Scope: Black-box testing

Who: Client

This step is performed when the software is fully or well developed. The software is shipped to a limited amount of users so that they can test it as they wish. The purpose is to detect fault by providing inputs that testers have not thought about because of their advanced knowledge of the software and its purpose.

14 Requirements-based testing

Requirements-based testing is a testing approach in which test cases, conditions and data are derived from requirements [19]. Those test can be per-

formed from the start of the development process and to the end of it [20]. Of course, if the requirements are not good, those test will be of little use. Good requirements are critical but requirements checking is, again, out of our scope. So we will simply assume that we have good requirements for this work.

Requirements-based testing is exactly what we are trying to do with black-box testing. For each given requirements stating, for example, that we want to produce some result Y when you give some input X , we want to produce a test case that maps the input X to our software and gets the output Y' . Comparing Y and Y' will allow us to decide whether or not, there is a fault and/or decide if the requirement is met or not. If not, our software is not built correctly and we need to work more on it before shipping it for the client.

All the previously described type of testing are not necessarily requirements-based. For example, unit testing is about the code. We want to make sure that each part of the code work well but satisfying that does not imply that requirements are met! For a 3D modeling software, we can have a code that compute square roots, if the code works well, that does not imply that our software allows the user to design houses for example.

15 Conclusion

You should now have a good idea of why testing is of crucial importance in any software development process as well as some pointers on what needs to be tested, validated and verified all along the development process. Given requirements, the software development process should produce a software that meet those requirements. Let's now introduce the core aspect of this thesis, model checking.

Part III

Formal Verification and Model Checking

Our goal is to be able to verify that any kind of product, software or hardware, is working as expected. Formal verification can help us to achieve this goal. It is a process that allows us to check whether or not a given model satisfies given properties. You can, for example, create a model of a given software and verify that it does not contain bugs. To be able to do that, both the model and the specification must be formally described in a mathematical language. The model is an abstraction of the real system which can have some properties. The requirements we want to check must be expressed as properties which can then be checked against the model using FSM that we will now introduce.

16 Finite State Machine

An FSM is a mathematical model of computation. It is a mathematical structure used to provide an abstract description of the behavior of a system. This mathematical structure is composed of a set of states. An FSM can be in exactly one of a finite number of states at any given time. The FSM can change from one state to another in response to some external inputs. The function that maps state and inputs to states is called a transition. An FSM is defined by its set of states, its initial state, the input alphabet and a set of transitions [21].

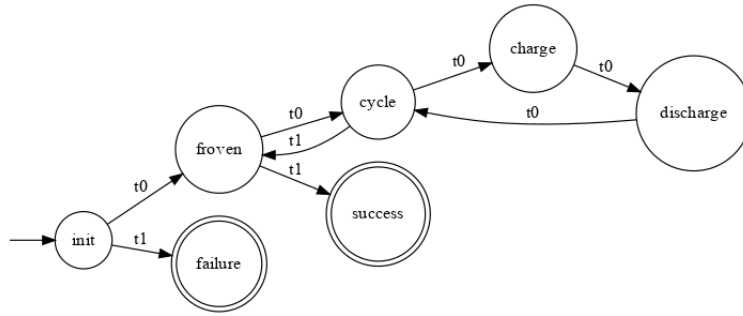


Figure 5: Example of an FSM shown as a graph.

The input alphabet is the set of every symbol the FSM can receive in any given state. For example, an input alphabet could be a list of keyboard's key if we consider an FSM that simulates the behavior of a text editor. The key points here are that you need to have a finite set of states and a finite set of transitions.

Definition 16.1. Finite alphabet. A finite alphabet Σ is a finite set of symbols.

Definition 16.2. State-transition function. A state-transition function δ is a mapping $\delta : S \times \Sigma \rightarrow S$ that maps each state s with a symbol a from an input alphabet Σ and another state s' .¹

¹This hold only for deterministic FSM, we'll see later how to express δ for non-deterministic FSM and what non-deterministic/deterministic FSM means.

Definition 16.3. Finite State Machine. an FSM is a quintuple $(\Sigma, S, s_0, \delta, F)$, where:

- Σ is the finite input alphabet.
- S is the set of states.
- $s_0 \in S$ is the initial state.
- δ is the state-transition function: $\delta : S \times \Sigma \rightarrow S$.
- $F \subseteq S$ is the set of final states.

Because of those rules, a transition function can start and end in the same state, thus creating a loop on the state. δ is conventionally allowed to be a partial function. This means that $\delta(s, e)$ does not needs to be defined for every possible combination of $s \in S$ and $e \in \Sigma$. This allows us to have a finite δ transition function even when the input alphabet Σ is not totally known or infinite. In our case, this allows to describe a FSM by only specifying transition function for input we are actually interested in which can be helpful if we have a huge input alphabet.²

Definition 16.4. Finite word. A finite word $\sigma = \sigma_1\sigma_2...\sigma_n$ is a finite sequence of symbols from Σ : $\forall i : \sigma_i \in \Sigma$.

A FSM can be run over a finite word from the input alphabet in order to accept or reject it. This is formally defined as follows:

Definition 16.5. Run. A run r of the FSM $\mathcal{A} = (\Sigma, S, s_0, \Delta, F)$ over a finite word $\sigma = \sigma_1\sigma_2...\sigma_n$ over the alphabet Σ is a sequence of states and alphabet symbols

$$r : \bar{s} = s_0 \xrightarrow{\sigma_1} s_1 \xrightarrow{\sigma_2} s_2 \xrightarrow{\sigma_3} \dots \xrightarrow{\sigma_n} s_{n+1}$$

where $\forall i \geq 1 : (s_{i-1}, s_i, \sigma_i) \in \Delta$

Definition 16.6. Accepting run. A run r of the FSM $\mathcal{A} = (\Sigma, S, s_0, \Delta, F)$ over a finite word $\sigma = \sigma_1\sigma_2...\sigma_n$ over the alphabet Σ is an accepting run if:

$$s_{n+1} \in F$$

²As you'll see later, we will have to describe FSM that have a huge input alphabet, this saves us a lot of time by only requiring us to describes what happen for symbols that actually are of interest for us.

$$r : s_0 \xrightarrow{\sigma_1} s_1 \xrightarrow{\sigma_2} s_2 \xrightarrow{\sigma_3} \dots \xrightarrow{\sigma_n} s_{n+1}$$

where $\forall i \geq 1 : (s_{i-1}, s_i, \sigma_i) \in \Delta$

Definition 16.7. Rejecting run. A run r is a rejecting run if it is not an accepting run.

We can extend the definition of FSM by splitting the final states in two distinct set: success states and failure states.

Definition 16.8. (Extended) Finite State Machine. A Extended Finite State Machine (EFSM) is a tuple $(\Sigma, S, s_0, \delta, W, F)$, where:

- Σ is the finite input alphabet.
- S is the set of state.
- $s_0 \in S$ is the initial state.
- δ is the state-transition function.
- $W \subseteq S$ is the set of final success states.
- $F \subseteq S$
 F is the set of final failure states.

This extension of finite state machine doesn't introduce any more complexity. Take the original definition where a finite state machine FSM is a tuple $(\Sigma, S, s_0, \delta, A)$. In this definition, the set of final state A is equivalent to $W \cup F$ from our extended definition.

With this new definition in mind, we define a run to be a *successful run* and *failed run*: Every run is said to fail if it is not a successful run. Now, we define a successful run as a run where at least one state is a success state and no previous state is a failure state:

Definition 16.9. Successful run. A run r of the EFSM $\mathcal{A} = (\Sigma, S, s_0, \Delta, W, F)$ over a finite word $\sigma = \sigma_1\sigma_2\dots\sigma_n$ over the alphabet Σ is a successful run if:

$$\exists s_i \in \bar{s} \mid s_i \in W$$

and

$$\forall j < i : s_j \notin F$$

Definition 16.10. Failure run. A run r of the EFSM $\mathcal{A} = (\Sigma, S, s_0, \Delta, W, F)$ over a finite word $\sigma = \sigma_1\sigma_2\dots\sigma_n$ over the alphabet Σ is a failure run if it is not a successful run.

16.1 Description of Finite State Machine using graphs

Having a more human friendly way of presenting EFSM is possible using graphs. To show an EFSM as a graph, we use a directed empty graph G . Then, we create a node for every state of our EFSM. Now we add an edge for every transition defined in δ with the starting point being the current node and the endpoint is the node corresponding to the goal of the transition. Finally, we add an edge starting from nowhere and pointing to the node representing the initial state.

Every state s in W or F is displayed with a double circle. We label those in W with "*success*" and those in F with "*failure*". This gives us a graph we can display like in the Figure 5. For comprehension sake, we have also labeled each node with a brief state name and every transition with an unique name based on the starting node.

17 Acceptors

An acceptor takes a sequence of inputs from the input alphabet and runs an FSM on it. Every state of the acceptor is either an accept state or a reject state. When the computation ends in an accepting state, we say that the input is accepted and the input is not accepted otherwise (Figure 6). The set of sequence of input symbols accepted by an acceptor is called a regular language.

18 Language

We define a word over a given alphabet as a sequence of symbols from that alphabet. As you already know, a FSM is defined for an input alphabet and all sequence of symbols from that alphabet are words. We also define a language as a set of words.

Definition 18.1. Language. A language L over an alphabet Σ is a subset of Σ^* , a set of word over the alphabet.

An FSM can accept or reject words, we will call the set of accepted words a regular language over the input alphabet of the FSM.

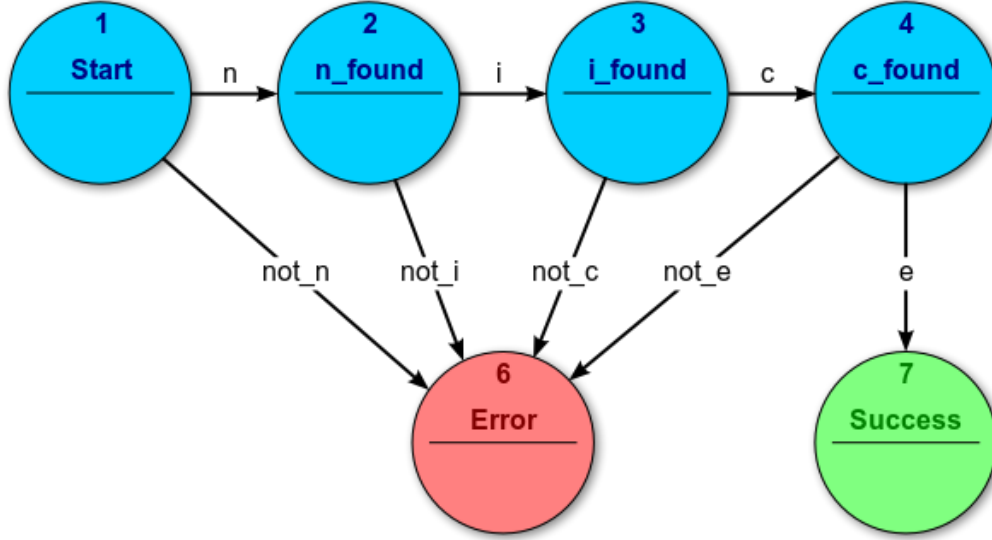


Figure 6: Example of acceptor for the string 'nice' [21]

Definition 18.2. Regular Language. A language $L \subseteq \Sigma^*$ is regular if all word $w \in L$ are accepted by an FSM A . [22]

We define the language recognized by an EFSM as follows:

Definition 18.3. Language of an EFSM. The language $L \subseteq \Sigma^*$ recognized by an EFSM is the set of all words that produce successful run for the EFSM.

19 Non-Determinism

A Non-Deterministic Finite Automaton (NFA) is a kind of FSM in which transitions do not requires the reading of input symbols. We can take transitions from one state to another without looking at the input tape. The decision of taking such a transition is done in a non-deterministic way and there can be several transition that are valid from the same state for the same input symbols. For example, in the Figure 7, reading symbol 1 while in state p can give two outcomes: either we go in state q or we stay in state p . [23]

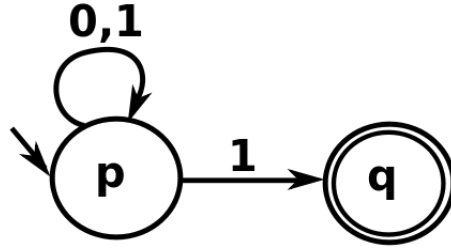


Figure 7: Example of a NFA. In state p , reading the symbol 1 can lead to p or q [24]

Formally speaking, we represent a NFA with a 5-tuple, $(Q, \Sigma, \Delta, q_0, F)$ where:

- Q is the finite set of states.
- Σ is the input alphabet.
- Δ is the transition-function $\Delta : Q \times \Sigma \rightarrow P(Q)$ where $P(Q)$ is the power-set of Q .
- q_0 is the initial state with $q_0 \in Q$.
- F is the set of accepting states where $F \subseteq Q$.

In an NFA, the input alphabet Σ usually contains a special symbol for representing a transition that can be taken without reading the next input symbol on the tape. This symbol is ϵ .

A run of a NFA produces several set of transitions instead of only one like it did for a FSM. From this, we say a run is accepting if when the last input symbol is consumed, there is at least one of the produced set of transition that ends in an accepting state. Similarly, we define a rejecting run as a run that do not produce a sequence of transition that ends in an accepting state.

It is known that we can construct an equivalent Deterministic Finite Automaton (DFA) from a NFA and they recognizes the same language. [25]

20 Alternating Finite Automaton

An Alternating Finite Automaton (AFA) is a 6-tuple, $(S(\exists), S(\forall), \Sigma, \delta, P_0, F)$, where:

- $S(\exists)$ is a finite set of existential states
- $S(\forall)$ is a finite set of universal states
- Σ is a finite set of input symbols
- δ is a state-transition function $\delta : (S(\exists) \cup S(\forall)) \times (\Sigma \cup \{\epsilon\}) \rightarrow 2^{S(\exists) \cup S(\forall)}$
- P_0 is the initial state with $P_0 \in (S(\exists) \cup S(\forall))$
- F is the set of final state with $F \subseteq (S(\exists) \cup S(\forall))$

In an AFA, transition are more complex than those we encountered in traditional FSM. The transition we are working with can lead to more than one state! This is used to represent logical AND and OR with transitions (see figure 8).

To implement this automaton, we need to be able to store the system's current state and restore it later so we can do multiple run and back-track when needed for every existential and universal transition we have. When we take an AND transition, we need to run a second automaton in parallel for each state the transition leads to and when we encounter an OR transition, we behave like a non-deterministic automaton by taking one of the possibilities non-deterministically. Depending on the number of those transitions, this increase the memory usage of our automaton as well as the time needed to complete the evaluation of all possible paths.

The computation of such an automaton is a tree. And deciding whether or not an AFA accepts a word w if there exists a run tree on w such that every path ends in an accepting state. [26]

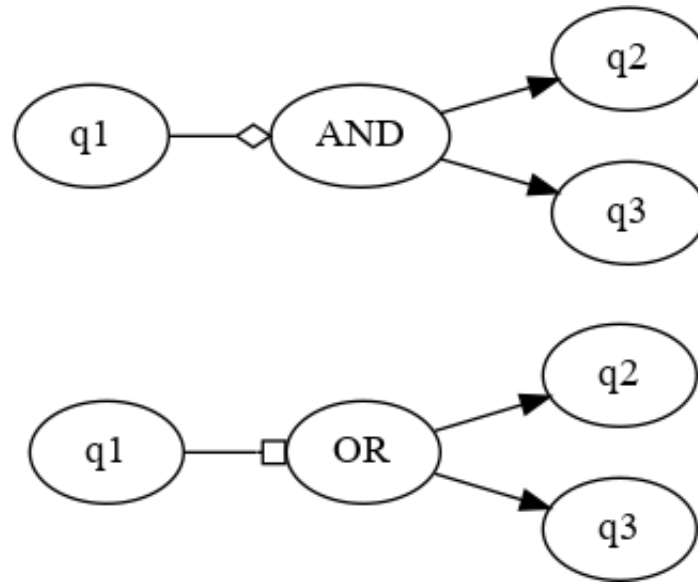


Figure 8: Example of a AFA.

21 Timed Automata

In the previous FSM, we do not have any time measure. The only time order we have is the sequence of input we use with the FSM but we don't know how many time, in second for example, is elapsed between two consecutive symbols from the word used as the FSM input. How can you manage to produce an FSM that should fail anytime this time interval is greater than thirty seconds for example? You can't without introducing time units into the FSM. This is the motivation behind Timed Automata (TA). To define a timed automata, we need some more definitions. A timed automata works with timed sequences, timed words and timed languages.

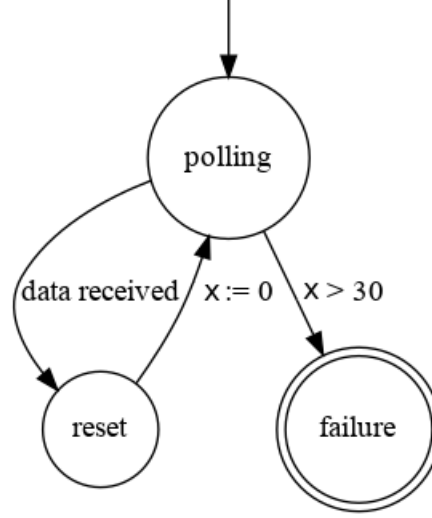


Figure 9: Example of a timeout timed automaton. x being one of the clocks that can be used

For a given input alphabet Σ , a timed language is defined as a set of timed words over Σ . A timed word over Σ is a pair (σ, τ) where σ is a word over Σ and τ is a timed sequence. Lastly, a timed sequence is a sequence of real time values increasing monotonically. [27] The goal is to map each symbol from the timed word σ to a particular time from the timed sequence τ . It allows us to measure the time between each input symbol.

Definition 21.1. Timed sequence. A timed sequence is an infinite sequence of values increasing monotonically $\tau = \tau_1\tau_2\dots$ where $\tau \in \mathbb{R}$ and $\forall i : \tau_i > 0$ and $\forall i \in \mathbb{N} : \tau_i < \tau_{i+1}$.

Definition 21.2. Timed word. A timed word over an alphabet Σ is a pair (σ, τ) where $\sigma = \sigma_1\sigma_2\dots$ is an infinite word over Σ and τ is a timed sequence.

This is used to map every input symbol of σ to one value of τ . σ_i is mapped to τ_i for all $i \in \mathbb{N}$. For example, with an input alphabet being $\Sigma = a, B$, instead of having a word $aBaaBaBB$, you have a timed word like the one in Figure 10.

Definition 21.3. Timed language. A timed language over an alphabet Σ is a set of timed words over Σ .

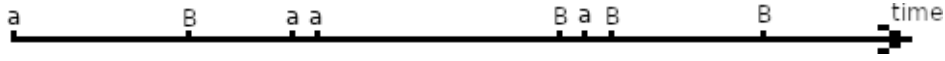


Figure 10: Example of a timed word from the input alphabet $\Sigma = a, B$. The space between each input symbol is the time elapsed

We also need a transition table that can read timed words. We want the transition to consider the input symbol from the input alphabet and the time associated to it when it makes a transition choice. To do so, we associate a set of clocks to our transition table and for each transition, we associate a clock constraint to it and require that the transition may be taken only if the current values of the clocks satisfy this constraint. A clock can be set to zero simultaneously with any transition.

Definition 21.4. Clock constraint. A clock constraint is defined inductively by

$$\delta := x \leq c \mid c \leq x \mid \neg\delta \mid \delta_1 \wedge \delta_2$$

with $x \in X$ and $c \in \mathbb{Q}$. X being a set of clocks.

Definition 21.5. Clock interpretation. A clock interpretation v is a mapping $X \rightarrow \mathbb{R}$, with X being a set of clocks.

A clock interpretation v over X satisfies a clock constraint δ over X iff δ evaluates to true using the values given by v .

To use this type of automaton, we need to define a time unit that will be used for the clock interpretation. Each passed time unit, we increase the clock interpretation of every clock by some fixed increment. A simple example is to have a set of clock that are incremented by one every passing second. Here, the time unit is the second and the increment is 1. Using the same time unit and increment, we can interpret the example given in Figure 9 as follows:

- When data is received, we reset the clock interpretation of the clock x
- If we do not receive data after 30 seconds, the run is over.

The clock constraint here is $x > 30$. This automata can be used to ensure that a data is received at least every 30 seconds.

Definition 21.6. Timed transition table. A timed transition table \mathcal{A} is a quintuple (Σ, S, s_0, C, E) , where:

- Σ is the input alphabet.
- S is the set of state.
- $s_0 \in S$ is the initial state.
- C is a finite set of clocks
- $E \subseteq S \times S \times \Sigma \times 2^C \times \Phi(C)$ gives the set of transitions where $\Phi(C)$ is the set of clock constraint over C . A transition from the state s to s' on input symbol a is represented by a quintuple $(s, s', a, \lambda, \delta)$ where λ is the set of clock to be set to zero and δ is the clock constraint over C .

We now have all the needed tools to define properly the type of timed automaton we'll need for this thesis:

Definition 21.7. Timed automaton. A TA \mathcal{T} is a tuple (\mathcal{A}, W, F) where \mathcal{A} is a timed transition table, $W \subseteq S(\mathcal{A})$ (with $S(\mathcal{A})$ being the set of state of \mathcal{A}) is the set of *success* states and $F \subseteq S(\mathcal{A})$ W is the set of *failure* states. We will only consider deterministic timed automaton in this thesis as [25] shows that we can transform any NFA into a DFA.

Definition 21.8. Run. We define a run r of a TA $\mathcal{T} = (\mathcal{A}, W, F)$, where \mathcal{A} is a transition table (Σ, S, s_0, C, E) , over a timed word (σ, τ) as the sequence

$$(\bar{s}, \bar{v}) : (s_0, v_0) \xrightarrow[\tau_1]{\sigma_1} (s_1, v_1) \xrightarrow[\tau_2]{\sigma_2} (s_2, v_2) \xrightarrow[\tau_3]{\sigma_3} \dots$$

with $s_i \in S$ and v_i is a clock interpretation $C \rightarrow \mathbb{N}_0 \forall i \geq 0$.

- s_0 is the initial state of \mathcal{A} .
- $\forall x \in C : v_0(x) = 0$
- $\forall i \geq 1 : \exists e \in E$ of the form $(s_{i-1}, s_i, \sigma_i, \lambda_i, \delta_i)$ so that $(v_{i-1} + \tau_i - \tau_{i-1})$ satisfies δ_i and v_i equals $[\lambda_i \mapsto 0](v_{i-1} + \tau_i - \tau_{i-1})$ with $\tau_0 = 0$. [27]

Intuitively, a run is the sequence of states and clock interpretations characterizing the *run*/processing of a given timed word to a given timed automata. For each tuple of state and clock interpretation, the clock interpretation is:

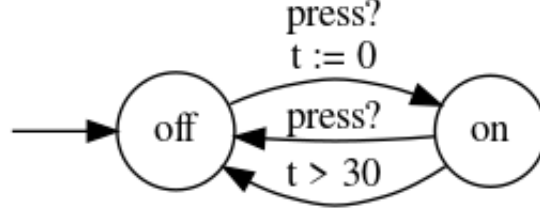


Figure 11: Example of a timed automata. This simple example shows a possible lamp controller that shutdown the lamp after 30 seconds.

- 0 for every clock if it is the first of the sequence or
- the clock interpretation in the sequence + the amount of time unit elapsed with every clock in λ being reset.

Definition 21.9. Accepting run. A run r of a TA $\mathcal{T} = (\mathcal{A}, W, F)$, where \mathcal{A} is a transition table (Σ, S, s_0, C, E) , over a timed word (σ, τ) is accepting if for s_n the last state of \bar{s} :

$$s_n \in W \bigcup F$$

Definition 21.10. Rejecting run. A run r is a rejecting run if it is not an accepting run.

Using this new definition of run, we can define the language accepted by a TA as the set of timed words whose run over TA are accepting:

Definition 21.11. Timed language. Given a TA $\mathcal{T} = (\mathcal{A}, W, F)$, where \mathcal{A} is a transition table (Σ, S, s_0, C, E) , the language $L \subseteq \Sigma^*$ recognized by the TA is the set of timed words $w \in \Sigma^*$ for which the run of w over the TA is accepting.

We also add the definition of successful and failing runs for future use. Given a TA (\mathcal{A}, W, F) and a run r , we say it is a successful run if at least one state of the run is a success state and no previous state to this one is a failure state.

Definition 21.12. Success run. A run r over a TA \mathcal{T} is a success run if

$$\exists s_i \in \bar{s} \mid s_i \in W$$

and

$$\forall j < i : s_j \notin F$$

.

Definition 21.13. Failure run. A run r over a TA \mathcal{T} is a failure run if it is not a success run.

We will use those definition to allows us to use non finite timed words later.

22 Markov model

Markov Model is a stochastic model used to model randomly changing systems [28] [29]. The difference between a deterministic FSM and a Markov Model is simply that the Markov Model take transitions randomly based on given probabilities.

This particular type of Markov Model is the simplest one. Probabilities are not influenced by previous state of the system. We will not introduce the other types of Markov Model here as we don't need them later on.

23 Applications

One of the first question one should ask is how is model checking and formal verification used in the everyday life or at least, for some projects. The following examples of what is currently used in this domain where taken from the most used tools at the time this thesis was written. Unfortunately, this choice is rather subjective as metrics about each possible software usage is not always available but the only thing that interest us is to know how they works and what they have in common.

23.1 UPPAL

The first software we came across while searching for such applications was UPPAL. UPPAL is a rather complete tool to work on model checking and formal verification. With it, one can model a system, simulate and verify it.

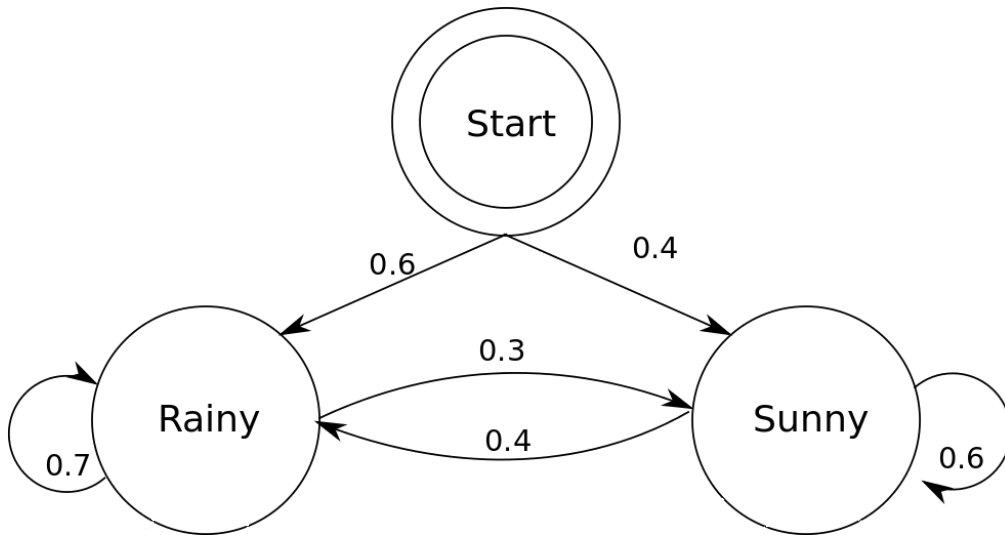


Figure 12: Example of a Markov Model. It shows the probabilities for the weather to be sunny or rainy.

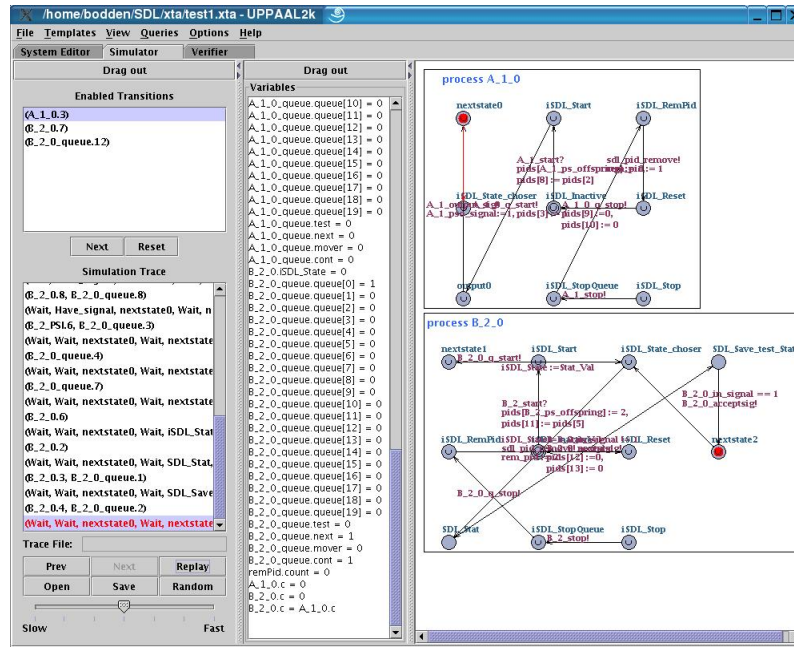


Figure 13: Main window of UPPAAL for the simulation part. You can see the execution trace in the lower left corner of the window as well as the model of the system on the right, shown as a TA

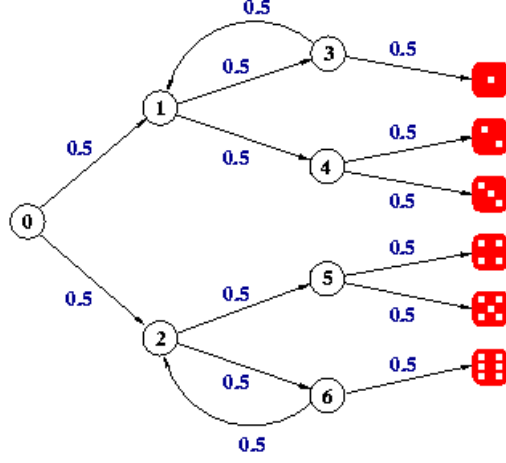


Figure 14: Example of model used by PRISM. This model was generated using the lines of code in figure 23.2.

All the three steps in only one tool [30] [31] [32].

UPPAL was a great source of inspiration for some features of our own tool. We were specifically interested by the simulation tools which allows to navigate through the execution steps and visualizing what is going on with the model of our system. Also, UPPAL uses TAs which comforted us in our choice of such tool.

23.2 PRISM

We where also interested by PRISM, which is a tool for modeling and verification of probabilistic systems (see Section 22). PRISM supports different kind of probabilistic models like Discrete-Time Markov chain (DTMC), Markov decision process (MDP), Probabilistic Automaton (PA), continuous-time Markov Chain (CTMC) and Probabilistic Timed Automaton (PTA).³ [33] [34]

PRISM uses a uniform modeling language for all the probabilistic models

³Probabilistic Automaton are Markov model that also supports deterministic transitions.

Listing 2: "Dice model in PRISM"

```
dtmc

module die

// local state
s : [0..7] init 0;
// value of the die
d : [0..6] init 0;

[] s=0 -> 0.5 : (s'=1) + 0.5 : (s'=2);
[] s=1 -> 0.5 : (s'=3) + 0.5 : (s'=4);
[] s=2 -> 0.5 : (s'=5) + 0.5 : (s'=6);
[] s=3 -> 0.5 : (s'=1) + 0.5 : (s'=7) & (d'=1);
[] s=4 -> 0.5 : (s'=7) & (d'=2) + 0.5 : (s'=7) & (d'=3);
[] s=5 -> 0.5 : (s'=7) & (d'=4) + 0.5 : (s'=7) & (d'=5);
[] s=6 -> 0.5 : (s'=2) + 0.5 : (s'=7) & (d'=6);
[] s=7 -> (s'=7);

endmodule
```

that it supports. This is a textual language, based on guarded command notation. See Figure 14, this model was generated using the following lines of code:

Again, this tools uses FSM to model different systems.

24 Conclusion

You should now have a good understanding of FSM and some of the various modifications we can add to them. We still have to introduce the background of this work before beginning to mix software development and FSM but you should already have a small idea of what possibilities such combination have to offers. We will later explain how we used the FSM introduced here as a Domain-specific language (DSL) that we uses to describes the testing we want to conduct.

Part IV

Case study

Now that you know what software testing and FSM are all about, lets introduce the case that is of interest to us, namely, Nursery! I'll first introduce the Railnova enterprise and the system we will work with all along this thesis. Then I will explain why this work is of critical importance for them and generally speaking, for every software development process.

25 Railnova

This work is done in partnership with Railnova. Railnova is a technology company that provides telematics and software solutions to the railway industry. Their headquarter is located in Brussels and they count approximately 30 employees. Some of them coming from the ULB/VUB.

The main goal of this work is to develop a better testing suite for their embedded software called Railster-OS. Of course, the developed solution will not be limited to a particular software and our scope is wider than that. Our solution will be considered successful if it meets the following requirements:

- The system can be used by non-developers
- It is a time saving technology
- The solution reduces costs

25.1 Existing system

Railster-OS is a software developed at Railnova which purpose is to collect and stream data from a train to a server. This data is then processed to give various insights about the train status. Thus allowing Railnova's client to manage their fleet with ease.

The Railster-OS is executed on a dedicated hardware also built by Railnova which is called RUG. Those RUG are mainly installed in trains in Germany, France, Belgium, Netherlands and Spain at the time this work was written. More specifically, the RUG is installed in the engine of trains.

This dedicated hardware is used to fetch data from the train's equipment. Those data are then stored in Railnova databases and are used for several applications like maintenance prediction, fuel monitoring, equipment monitoring and so on. We can access information like remaining fuel (if it is not an electrical engine), the total running time since last power on of the engine, where is the engine located, current speed if the engine is moving and all the data that the client wants to access remotely.

Every engine is more or less unique. They can use a wide range of different component and thus have different parameters like their maximal speed when loaded or not, the maximal weight they can carry or even the number of wheels the engine uses. Because of that Railster-OS needs to accept a wide range of different configuration so we don't stream fuel remaining if we are on an electrical engine for example. We can also have two kinds of motors that give information about their speed without using the same type of cable or protocol. To achieve this flexibility, we allow Railster-OS to start only a subset of its internal software using some configuration files. All this software's modules, which we call daemons, are small applications used to interface with a given protocol. For example, we have one daemon that is dedicated to GPS location's information. The set of daemon we use, which is defined in the configuration files, defines what type of engine we can handle and what operations we can do.

To retrieve all those information, we need to use different communication protocols and wires because engines built by different companies will have different behaviors and communication protocols. All of them, we will call interfaces. Each of those interface needs proper test that relies on the used communication protocol with the underlying equipment, we want to be able to describe our needs, explains what data should be fetched by a specific interface, when we should fetch it. As there is a lot of interface, it will take time to implement proper testing for all of them so we will only cover a subset of them in this thesis but we will provide a generic way of implementing test for them all.

25.2 Railster Universal Gateway

As stated before, Railster-OS is running on RUG. Those hardware are built by Railnova. Actually, the majority of them are in their first version (RUG1) but the RUG2 is on its way. It is important to keep that in mind to understand why we tried to keep this work as generic as possible. Indeed, in a year, maybe less, the majority of the product used will be RUG2 so if the system can only work for a dedicated and specific hardware, we will need to start over all this work for the RUG2.

Railster-OS is being developed incrementally since the start of the company. It means that the software becomes harder and harder to maintain

with all the new features coming in and the old ones being deprecated or deleted. Actually, there is more than 10 different versions of the software in production and those are not the only existing versions.

It is frequent to encounter new bugs after the release of a new version that were supposed to be fixed since earlier versions simply because the new changes re-introduced the problem. This is mainly because Railnova is a young company, a start-up, and the work priority are very end-user scheduled. If a system stops working, a quick fix is developed and pushed to production as fast as possible to avoid losing too much money but the cost on the long run for this operation can be greater than what we saved by fixing the problem quickly.

Say that the problem is fixed by a quick and dirty patch. Then, one year later maybe, a major problem arises because all the fixes deployed are preventing us from adding wanted features. Then, the whole system is to be re-developed from the start. That was what happened when they tried to put Railster-OS on the RUG2. RUG1 was developed the quick and dirty way to be on the market as soon as possible. So a lot of conception errors were made. The software had been modified to a point where it needed all those conception errors from the RUG1 to operate correctly.

25.3 Short story

Some of the RUG were experiencing troubles during the end of fall 2016. 8% of the fleet had to be replaced by the team for unknown reasons. After a bit of testing, some members of the team thought that it could come from an unexpected electrical over-consumption of the RUG while connecting to a GPS antenna. To validate this hypothesis, some testing had to be done. It was the end of summer so the outside temperature was going down so the team thought that maybe it had something to do with that.

The test procedure was defined as follow: First, set the temperature of a thermionic chamber to -10 Celsius to replicate the outside temperature recorded when we experienced the failures. Put a RUG in it and connect to the serial communication port of the system. Once we are connected, we issue some command to manually choose which GPS protocol to use out of the four of them supported and for each of them, we manually set the emitting power of the GPRS chip in order to reproduce the problem seen on the

practical field.

Those test were made, approximately 30 for different temperatures of the thermionic chamber, different band and different emitting power. Those took one person an entire day of work! That's because the thermionic chamber needs an hour to set a fixed temperature, the system needs 5 minutes to reboot when we set the band to use and we wait 10 minutes for each test to be sure the problem did not appear (or did). After each test, if we want to do another one, we need to wait for the system to cool down as each time we boot it up, it produces heat.

And those tests are just to prove the initial hypothesis. Once this was proved, we wanted to know for sure, what emitting power we could use for each RUG (not only for one) depending on the band used and the external temperature. This would need hundreds of tests if not a thousand on a lot more than just one RUG to be able to make enough data and say for sure which levels are safe, which levels are risky and which levels are forbidden to avoid problems in the real world.

And that's without mentioning that the thermionic chamber makes a lot of noise to work, wasting an entire room as people can not work long in such a noisy environment.

This is where the Nursery project comes in handy. The goal of it is to provide an environment fully automatized to realize all those testing when we want, like during the night so the room stays silent during the day for people to work in, without needing one or more persons to take weeks or years of works.

25.4 The Railnova Way

At Railnova, development in general is lead by the Agile framework. Because of that, software and hardware developed tend to welcome changes and new features are added every week as well as small bug fixes or minor modifications here and there.

This tend to make it difficult to keep track of what is working and what is not during the life-cycle of any project. This is where Nursery becomes

useful. By providing a tool to check whether or not features are broken after modifications have been applied would be a great time saver. Testing everything manually is a tedious work and waiting for things to break in production environment is a very bad idea like we saw previously.

Usually, when one of the RUG fails in production, the product is called back to the Railnova HQ for Fleet Quality Management (FQM). FQM consists of several phases. During the first one also called "fingerprinting", we try to identify the root cause of the failure. Then, we re-direct the product either to "Debug HW" for hardware debugging if the root cause is hardware linked or to "Debug SW" if the root cause is software linked. After those steps, the problem should be solved. The product is sent to "Fixing" to apply other pending modifications that did not necessitate an immediate return of the product. Finally, the product is sent to "Flash & Validate" to re-initialize it completely and flag it as "ready to ship".

26 Conclusion

This section should have given you a good idea of why testing is so important at Railnova and in virtually any enterprise. Assuming everything works is just not sufficient and can lead to big trouble with clients.

Part V

Contribution

All the problems described above provides with the necessary information to complete this work. We know what can go wrong, we know what we need and we know that a proper validation of all the developed product is a key for success of any given product on the long run. So having a global view of what software testing is and what is the concrete problem we are dealing with, we will try to design a testing process that can be applied to it and possibly to any other given problem that needs black-box testing. One of our main goal is also to design a tool that will be useful and easy to use but we want it to be powerful enough to be able to handle every test case we might think of.

27 Requirements analysis

First of all, let's take back our software development concepts introduced during the first chapter of this thesis. We have described seven types of testing and now that we know what we are working on, let's try to summarize what type of testing we really need:

- Unit and integration testing: Those kind of testing are great to test the code but that does not give us much information in terms of requirements. Knowing that all functions work separately is not enough to tell that requirements are met as the program behavior results from the interaction of all those function one with another.
- System testing: We do want to make sure that our interfaces can run on different hardware. In our case, we want to be able to validate our software for at least RUG1 and RUG2.
- Functional testing: Testing that the software fulfills the requirements is, of course, our main preoccupation. So this type of testing is of main importance.
- Regression testing: Being able to certify that a new feature or a bug-fix does not break the whole system is a key point too. We don't want to re-introduce old bugs or break what is already working well.
- Acceptance testing: Automatizing client testing is not really in the scope of this thesis. Besides, we assumed earlier that the requirements are good and well validated.

Summed up, those are the 3 main requirements we absolutely want in our solution:

- Functional testing
- System testing
- Regression testing

We now want to know what we need to satisfy those requirements. We will use them to rate the developed solution in Part VI. Let us now introduce some concept that will be used to formalize our following ideas:

Definition 27.1. Requirements. Requirements R is a set of requirements defined by our client(s). Keep in mind that we can be our own client.

Definition 27.2. Testing. A test t is used to map a particular requirement $r \in R$ to a satisfaction value:

$$t : r \rightarrow \{True, False\}$$

When mapped to *True*, we say that the requirement is fulfilled and the requirement is not fulfilled when mapped to *False*.

This definition is still incomplete, we need to know how the mapping is done and that is the question we are going to answer in this chapter. Also, keep in mind that when we talk about "our software" or "our program", we want to refer to the software being tested. In our case, our software refers to Railster-OS which is the Unix based operating system we want to test.

27.1 Functional testing

Given a set of requirements, we want to make sure every one of them is fulfilled, using the definition we stated earlier, this is pretty simple to formalize:

Definition 27.3. Functional testing. Functional testing is used to ensure that the following condition holds:

$$\forall r \in R : t(r) = True$$

27.2 System testing

Now, you know what system testing is about. We want to verify if our program can run on more than one system. At Railnova, the practical case we had at hand was about two different hardware, RUG1 and RUG2, both of them handles inputs and outputs differently and we wanted to make sure that our custom Unix based OS would run as expected on both of them.

Of course, the newest version, RUG2 had more features than the RUG1 and this lead us to our first observation: Different systems can have the same capabilities but they generally don't and given that all capabilities should be tested, see functional testing if you don't recall why, the set of tests we should

run on a given system is system dependent. Different systems can have all their tests in common if they have the same capabilities, none or some test in common if they don't

Again, let's take a practical example: The RUG2 can handle more communication protocols than the RUG1 and can process more information in the same amount of time. So our custom OS should be able to handle those new information when run on the RUG2 but this is not required when run on the RUG1. To fulfill this need, we now know that our solution will have to provide a way of defining the subset of all existing requirement to be tested on a particular system.

Definition 27.4. Systems. *Systems* is the set of all possible environment on which we want our program to run.

Definition 27.5. System. A system *System* \in *Systems* a particular environment on which we want our program to behave as expected

A concrete example of two different systems is a smartphone and a computer. Both are used for different purposes but have some common features (display, user interface, processing power, ...).

Definition 27.6. System requirements. The subset of requirement that are required for a given *System* is $R(\textit{System})$

Definition 27.7. Testing. We can now extend our definition of test by including the system on which the test is done:

$$T : r, \textit{system} \rightarrow \{True, False\}$$

Definition 27.8. System testing So now, system testing is simply making sure that, given any *System* \in *Systems*, the following condition holds:

$$\forall r \in R(\textit{System}) : t(r, \textit{system}) = True$$

Or simply put, for every requirement of our system, we want to have a test that can evaluate to *true* if the requirement is met and *false* otherwise and we want all those test to evaluate to *true*.

27.3 Regression testing

In regression testing, we want to make sure that the previous testing are, in the worst case, as efficient as before. If we only managed to fulfill a subset of the needed requirements, we want this subset of requirement still fulfilled in the newest revision of our software.

Definition 27.9. Revision. We are going to iterate on the tested software to improve it gradually. A revision V of the software s is a mapping of the software to :

$$V : s \rightarrow \mathbb{N}_0$$

with the following conditions:

- s is the latest software revision
- s_0 is the first software revision and $V(s_0) = 0$
- $\forall x \in \mathbb{R} : V(s_x) > V(s_{x-1})$

Using that, lets extend our test definition to include the software revision:

Definition 27.10. Testing. Given a requirement r , a system s and a software s_x :

$$T : r, system, V(s_x) \rightarrow \{True, False\}$$

Having defined those notations, we now have:

Definition 27.11. Regression testing Verify that, given any $System \in Systems$ and a given software revision x , the following condition holds:

$$\forall r \in R(System) : t(r, system, V(s_{x-1})) \rightarrow t(r, system, V(s_x))$$

Basically, what worked before must still work now.

Remark 27.1. Satisfying the functional testing is sufficient. We don't need to explicitly check the previous version's requirements for divergences as all requirements are mandatory because of functional testing.

28 Requirements satisfaction

We now have a good idea of what we want to do, checking various requirements of a given software to make sure they are all satisfied on every different system used. But how to know when a requirement is fulfilled or not? In the chapter dedicated to them, we introduced the notion of failure and accepting states. If we manage to map all the interactions of our system with an input alphabet, we can easily imagine that a run of a given FSM extended with the concept of failure and accepting state can produce the desired mapping for a given requirement. The underlying idea would be to create an FSM for every requirement, run them using the defined input alphabet of the software's interactions and check that the resulting mapping is only made of *True* values. If it is the case, we have successfully created a program that satisfies all the given requirements!

All we have to do is to find the best FSM type that suits our needs and to formalize the software interactions in a way that allows us to capture and manage them.

29 Interfaces

In our practical case, we need to know how the tested program is performing in order to test its requirements. To do that, we need to analyze the output it produces. In this context, outputs can be data, reports, logs, screenshots and many other things as long as they are produced by the analyzed program. At Railnova, the RUG communicate with data servers, it produces particular messages that are saved in Railnova databases. It also can be accessed using a serial communication. Using this serial communication, we can fetch a lot of information from the RUG operating system. We want our solution to interface with such data stream. Based on this quick analysis, we can think of separate program whose purpose is to continuously check for new data and fetch them into our solution as FSM's inputs. Let's call those program "Output Providers". So the idea is to setup a set an output providers that will provide information about how the program is performing. We can imagine a lot of other use cases for those output providers, for example, we can have an output providers that look at a screen whose output is coming from our program and push the screen status into our solution.

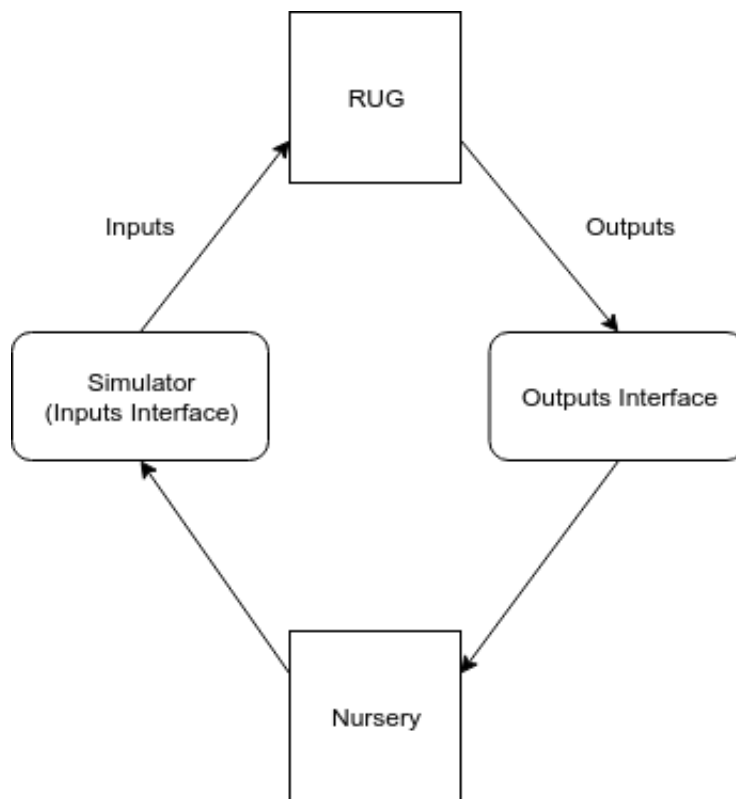


Figure 15: This is how we use the interface to intercepts outputs from the RUG and simulate the desired environment.

So, is interfacing outputs from our system enough to build a viable solution? We figure out that it is not. Let's say you want to check that the software produces different outputs based on the room temperature. If you don't have the room temperature, you will not be able to check data correctness. That's why we also need to fetch inputs of the tested program. We also need to control the system's inputs to simulate a desired environment. We can send data to the RUG the same way any train's equipment would. Figure 15 shows an overview of this concept. Unfortunately, this was not considered early enough in the development process of our solution so the name "Output Provider" was also used for those in the final solution.

29.1 Output(/Inputs) providers

The goal for those is to fetch or produce data that describes the state of the program or the various variables that can affect its processing/behavior. We decided to allow the user to create any output providers needed. Those provider will then provide data in the form of key-value dictionary with up to date data in them like the three dictionary in Listing 3.

Each provider being identified, it is then easy in any test to use the data produced by knowing which output provider produces it and which key is used to identify the data. Lets use some more formal definitions for this:

Definition 29.1. Dictionary. A Dictionary D , in this context, is a mapping of key $k \in K$, where K is the set $K(D)$ of all the keys produced by D , and discrete values $v \in V$ where V is the set of values $V(D, k)$ that can be assigned to a given key k in this Dictionary:

$$D : k \rightarrow v$$

Two dictionary are of the same type if they share the same set of key.

Definition 29.2. Output/Input providers. An Output/Input Provider P is a user defined plugin that produces exactly one type of Dictionary D . From now on, we will call them Output Provider (O.P.).

So with those definitions, we can have several Output/Input Providers producing the same type of data (Dictionary) like in Listing 4.

An example for this would be a temperature sensor, one could read the sensor value on a screen or directly on the electrical wire. In this case, we

Listing 3: Example of a data dictionary received at different time.

```
# Dictionary 1 at t=5
{
  "tandelta_runtime": 5,
  "tandelta_query_failed": 0,
  "tandelta_bad_reply": 1,
  "tandelta_var_oil_temp": 14.5,
  "tandelta_var_ambient_temp": 42.4,
  "tandelta_var_oil_condition": 92
}

# Dictionary 1 at t=15
{
  "tandelta_runtime": 15,
  "tandelta_query_failed": 2,
  "tandelta_bad_reply": 4,
  "tandelta_var_oil_temp": 14.5,
  "tandelta_var_ambient_temp": 42.5,
  "tandelta_var_oil_condition": 91
}

# Dictionary 1 at t=50
{
  "tandelta_runtime": 50,
  "tandelta_query_failed": 2,
  "tandelta_bad_reply": 4,
  "tandelta_var_oil_temp": 14.4,
  "tandelta_var_ambient_temp": 42.4,
  "tandelta_var_oil_condition": 93
}
```

Listing 4: Example of a dictionary that contains the same keys. Each one of them being identified by the name preceding the curly bracket

```
"rear_captor": {  
    "temp": 5,  
    "humidity": 6  
}  
  
"front_captor": {  
    "temp": 25,  
    "humidity": 0  
}
```

would have one O.P. that would fetch the sensor value from the screen and produce a dictionary with only one key $K = "sensor"$ and another O.P. would do the same but reading the value from the wire.

But now, what if we want another sensor, let's say an oil sensor, the set of value that this sensor can be mapped to will probably not be the same and how does one differentiate between one or the other? We could use the O.P. as an identification for the Dictionary but instead of that, we decided to add a custom data to all our dictionaries to identify them one from the other:

Definition 29.3. Dictionary (extended). A Dictionary D , in this context, is an identification $i \in \mathbb{N}$ and a mapping of key $k \in K$, where K is the set $K(i)$ of all the keys produced by a given Dictionary identified as i , and discrete values $v \in V$ where V is the set of values $V(i, k)$ that can be assigned to a given key k from a given Dictionary identified as i :

$$D : k \rightarrow v$$

This now implies that all Dictionary that shares the same identification should produces the same set of keys with the same set of boundaries for their values. A stronger assertion is that each identification of Dictionary denotes a particular context of data. In the previous example, we would now have a new O.P. that produces the same set of keys only with a different identification for its Dictionary.

30 Framework

We still need a way for the user to describe how the testing will be done. We decided to describes that using an FSM for each requirement testing and to call such FSM a scenario, we will refine this definition later after having explained which FSM we will use:

Definition 30.1. Scenario. A scenario is an FSM whose goal is to perform a test.

In theoretical FSM, the number of states is bounded above by the system's internal memory. If we want to test every possible state by explicitly stating what should happen in every memory configuration, it would gives us 2^x states to analyze (where x is the system's internal memory expressed in bit). Let's say that this memory can be around two to eight gigabits and this is raising every year. So we would have to describe what should happen in $2^{8 \cdot 10^9}$ different states. We don't say that this is not possible but if we can do the same using a model of how the system should behave (a scenario) with less than 25 states for example, it should be easier for any human being.

So to address this problem, we need to abstract the state definition. We'll aggregate formal states into a more complex structure that we will still call a state but for the user, it will just be something like "initialization state", "error state", "processing data state" and so on. A state will simply be a set of computation that needs to be applied. We want to be able to react when leaving or entering a state, we are now programming so we will probably need to free resources or allocate them for example. It would also be nice to be able to repeat some operation when we receive some input symbol. Of course, this behavior could be achieved using more states and transition but lets try to keep it simple, fewer states and transitions means less complexity, code and more readability. So we introduce three optional functions, one for the computation needed when we reach the state, one for when we leave the state and the last one for the computation we need to do every time a new symbol is read.

Speaking of input symbols, we talked about data providers in section 29.1. We want to use them to get input symbols. So let's define our input alphabet as a set of dictionary:

Definition 30.2. Input alphabet. The input alphabet we will be working with is a set Σ of dictionaries D .

To choose the type of FSM that best suits our needs, we need some kind of ranking system based on what we learn in the chapter *Formal Verification and Model Checking* III. We decided to use the following criteria:

- We want to make sure that a particular FSM type is possible to apply in our case. If it is an abstract FSM that uses infinite memory or another paradigms that is impossible to implement on modern computer, we know that we can discard it.
- How easy it is to describe a system's behavior with such FSM? If it takes too much time or if we find it too difficult for non-technical people to describe a system's behavior using that particular FSM type, we know that it will probably be deprecated over time and won't be used as it should be.
- If the chosen FSM introduces restriction on what can be described, those restriction must not prevent us from doing every testing needed. And if it introduces new possibilities that allows us to describe more system's behaviors, the complexity that it introduces into the system should be worth it.

We can summarize with the following questions:

- Is it possible to implement?
- Is it user friendly?
- Does this introduces restrictions?

Now to the choice of FSM model, why don't we simply pick the most simple case of FSM? The reasons are very simple, take this case: You want to be able to detect a failure that happen if you receive a given data more than 10 seconds after some given data. How can you do this? Or even simpler, you want to detect a failure if you didn't receive any data for 30 seconds.

With a basic FSM that do not consider time, we would have to add a new input symbol to be used when no input data is received and assume that you

use a reference time unit and that each time unit passed, you should receive a data or the newly introduced input symbol when no data is received. We need to add states for each seconds passed, so if we want to wait 30 seconds, we need 30 states with 30 transitions for each seconds and 30 transitions for the event of "a data has been received" that will go back to initial state. And all of this needs to be written by someone. You can easily see why this is impracticable. So clearly, in this case, FSM are too limited if we want to handle this in a simple and practicable way, that is, by limiting the number of states the user will need to use. And of course, this is no longer a basic FSM so clearly, those are not enough for us.

Now with the AFA, the complexity of implementation required by this paradigm is important, we need to run multiple automaton, construct execution trees and analyze them. Compared to a simple FSM implementation, this is a lot of work. So is it worth it? To answer this question, we need to find practical cases where such a paradigm would become indispensable and such case should be really difficult to implement into a classic FSM to justify choosing AFA as our main choice for the implementation of Nursery. Based on the concrete needs we had when we analyzed this possibility, we choose not to consider it as a good solution because none of the cases we had prepared (see section 3) needed such complexity and using this would have made it difficult to design test by inexperienced users. This also follows from the fact that we wanted to avoid dealing with concurrency.

There is a wide variety of FSM, there isn't any limit on how many FSM type you can think of but when we started implementing our solution, the best FSM that our 3 ranking questions left us with was timed automaton. Those FSM are really well adapted for our use case. Being able to manipulate time directly in our transitions and state gives us a lot of ease to write the code and its also very powerful too! With it, we are able, for example, to send data with a variable interval between them and then receive data accordingly. You can also decide to create a clock for timeout that will be reset each time you receive a given data. There are really a lot of use case and some more will be described later on.

This choice of tool is also motivated by the fact that writing and understanding automaton is a very easy process. Designing a User Interface (UI) to create automata and generating valid scenario out of it is a relatively

easy process so this could be a way to meet the strong requirement that our solution shall be easy to use for not seasoned programmers.

Let's now refine a bit our definitions of a scenario:

Definition 30.3. Scenario. A scenario is a timed automaton whose goal is to perform a test.

We also want to improve the input alphabet we will be working on, if we include the clocks in it, we will be able to perform operation every given time interval:

Definition 30.4. Input alphabet. The input alphabet we will be working with is a set Σ of dictionary D and a set of clocks C .

At each time step, we will receive a set of dictionary for every input received at this time and the set of clock of the TA updated with the current time step.

31 Application

Let's take a very simple example of a TA used to test the behavior of a software used to fetch data from a serial equipment using a simple protocol where the equipment can only reply to received commands and the reply can only be the room temperature reading or an error message.

To test this, we need to design a TA that will simulate the equipment behavior. That is what we did in Figure 16. Here, we start in the state *idle_1*, we wait for any command from the software being tested. Upon entering the state, we send a query to be received. If no such query is received within a given delay, we go to the state *failure*, the test is over and the software failed it. Otherwise, we go to *reply_temp* state and we reply with a room temperature. Then, we test the software output by checking if it contains the desired temperature. If it does not, the test is failed and if it does, we go to the state *idle_2*. Here, the behavior is the same as in state *idle_1* except that we now send an error reply. In state *reply_error*, we finally check that the error is received by the software being tested. If it isn't, we failed otherwise the test is over and succeed. We implies that the clock t is reset each time we change the current state. We now have an automata that is failing under the following conditions:

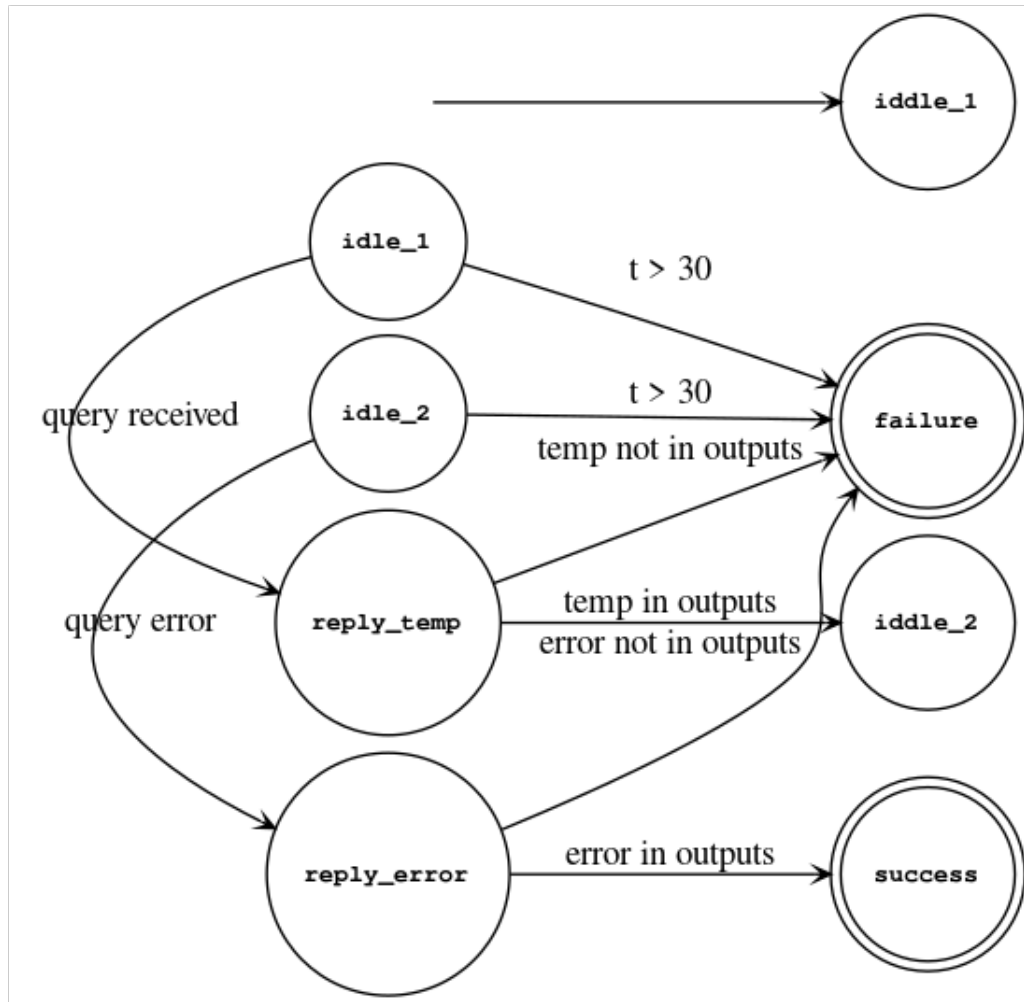


Figure 16: Simple TA that shows how to test the simple equipment described in Section 31.

- No command received
- Temperature does not exists or is not correct
- Errors not processed

In this TA, we use outputs provider to fetch the outputs from the software. This is what allows us to test whether or not the errors and temperature are produced by the software. But we also use them to read commands from the software. Inputs providers are used to send replies to the software, in this case, using serial communications.

32 Conclusion

We introduced the concept of portability when we talked about system testing and versioning when we talked about regression testing and with the third testing, functional testing, we stated that we want the software to run accordingly to our requirement on any system and for every revision of it and we also explained why we wanted to use time automaton. All of those aspect and tools will be used in the following chapter to explain how the final system works, how we are going to design TA and how to use interfaces.

Part VI

Results

This part is a in depth description of how the implemented system works, how it has been developed and what it can and cannot do. This is a more technical part dedicated to the implementation where we will not talk a lot about the mathematical concept behind it as those have already been explained before.

33 Domain-specific language

We start by explaining how we created the DSL used to describes our scenario in Python. All the code produced will be Python code but we took the liberty to talk about DSL to describes the functions and methods presented in this section even if it is still Python which is a General-purpose language (GPL). We want to allow the user to do the following operations:

- Create a scenario
- Add states to scenario
- Add transition to states
- Set success and failure states
- Set initial state
- Set enter/leave processing for every states
- Set update processing for every states

As we use Timed automata, we also need to uses clocks:

- Add/Remove/Set/Reset clocks
- Get given clocks time
- Trigger transition using clocks

Regarding the data sources, we want our transitions to be able to react on such data and to explicitly specify which kind of data our scenario will be working with. We should now distinguish between two kind of data, either the data is a discrete value, like e-mails, you want to be notified when you receive one or the data is continuous like the room temperature. We want to be able to get both of those data types:

- Set data sources to be used
- Receive discrete data as they appear from data source
- Ask for continuous data reading from data source

33.1 Create a scenario

Creating a scenario is done by creating a subclass of **AbstractScenario** like so:

Listing 5: "Creating a scenario"

```
from .AbstractScenario import AbstractScenario

class Scenario(AbstractScenario):
    def __init__(self, settings={}):
        AbstractScenario.__init__(self, settings=settings)
```

33.2 Add states to scenario

When adding a state to a scenario, you can also give it several functions that will be used for update, enter and leave trigger. You can also specify if it is a failure/success state. We satisfy the following requirement with one line of code:

- Add states to scenario
- Set success and failure states
- Set enter/leave processing for every states
- Set update processing for every states

Enter, leave and update processing are specified using user defined functions. They give the ability to fine tune what happens when we enter a given state, when we leave a state and what happens while we stay in a given state for every time unit.

Listing 6: "Add states"

```
state = self.add_state("name", enter=self.on_enter, update=self
    ↪ .on_update, leave=self.on_leave, status=state_status)
```

Listing 7: "Various usage of self.add_state"

```
from .AbstractScenario import AbstractScenario

NORMAL = 0
SUCCESS = 1
FAILURE = -1

class Scenario(AbstractScenario):
    def __init__(self, settings={}):
        AbstractScenario.__init__(self, settings=settings)

        init = self.add_state("init", enter=self.on_enter,
                               ↪ update=None, leave=None, status=NORMAL)
        failure = self.add_state("failure", enter=None, update=
                               ↪ None, leave=None, status=FAILURE)

        # Equivalent without handler
        success = self.add_state("success", status=SUCCESS)

        # Normal state with no handler
        foo = self.add_state("foo")

        # Only one update handler
        bar = self.add_state("bar", update=self.on_update)

    def on_enter(self):
        # Do some stuff
        pass

    def on_update(self, t, d):
        # Do some stuff
        pass
```

In the listing 6, `state_status` is 0 by default, a value of `-1` indicates a failure state and a value of 1 indicates a success state. Code starting with `self.on_` are function defined elsewhere, you can use whatever name you want

to name them. The function definition of any update handler should take t and d as inputs: t being the clock of the current state. This clock is reset when we enter the state and d is a possibly empty array of dictionaries given by the data sources used (we will define that later).

33.3 Add transition to state

To do so, use the function **add_transition** from any starting state and give it the goal state to which we should transition in case of success and a transition handler. The handler should return True if the transition must be taken and False otherwise. It takes t , the state clock as an input (see section 33.5 for the state clock) and d a list of dictionaries given by the data sources used (again, this will be defined later).

Listing 8: "Add transition to any state"

```
# Example using a python lambda function
from_state.add_transition(goal_state, lambda t, d: t > 60)

# Example using a function defined elsewhere
from_state.add_transition(failure_state, self.
    ↪ transition_handler)
```

33.4 Set initial state

Using a states previously created, a simple call to **self.tsm.set_initial_state** allows us to set the initial state.

Listing 9: "Set the initial state"

```
self.tsm.set_initial_state(state)
```

33.5 Manipulate clocks

We have a global clock used for every state. This clock is reset every time we enter a new state. We can also create new clocks when needed using the

functions in listing 10. The main clock is used as the t parameter for update handlers and transition functions. This satisfies the following points:

- Add/Remove/Set/Reset clocks
- Get given clocks time
- Trigger transition using clocks

Listing 10: "Set the initial state"

```
# Main clock. This clock value is the t parameter of update
  ↪ handlers and transitions
self.clock.get_elapsed()

clock1 = Clock()
clock1.start()
sleep(1)
clock1.get_elapsed() # should give 1
clock1.reset()
clock1.get_elapsed() # should give 0
clock1.stop()
```

This feature allows us to create clocks that are not dependent of any state. You could see the states clock as a clock that is reset at every transition. More formally, this clock is always in the set of reset clocks for the transition function: Given a TA $\mathcal{T} = (\mathcal{A}, W, F)$ where $\mathcal{A} = (\Sigma, S, s_0, C, E)$, we call the state clock m with $m \in C$. This clock will be used as the state clock and $\forall t \in \mathcal{A} : m \in t(\lambda)$ where $t(\lambda)$ is the λ set of clocks to be reset with this transition.

The additional clock we can create are not related to any specific state by default. They work like any clock as defined in TA definition.

33.6 Manipulate data sources

Here we want to satisfy the last three requirements:

- Set data sources to be used
- Receive discrete data as they appears from data source
- Ask for continuous data reading from data source

First, any scenario needs to states which data sources will be used. For a continuous data source, we will receive a data dictionary from a given data sources for every call of `self.ask_data("DATA_SOURCE")` where **DATA_SOURCE** is the name of the data source. For discrete data, the *d* parameter for update handler and transition will be a dictionary every time new data is available without further function calls.

Listing 11: "Set data source"

```
# Set data sources
self.l_data_type.append("EMAIL")
self.l_data_type.append("ROOM_TEMP")

# Asking data
self.ask_data("ROOM_TEMP")

# Checking data
if d is not None and d['type'] == "ROOM_TEMP":
    # Process
    pass
```

33.7 Determinism

A question you should be asking yourself now is, is this system deterministic? The answer is not yet. In the current state, if we have two transition that are satisfied from the same state at the same time, we do not know which one should be taken. To solve this problem, we mapped every transition to a integer in \mathbb{N}_0 always growing for each added transition. Transition are then evaluated from the lowest integer value to the biggest. The first succeeding transition will then be taken and we completely ignore the following ones. When designing scenarios, one should keep that in mind otherwise some unexpected cases can appear. Every time the scenario is used, this mapping

should stay the same. It can only be modified by modifying the source code itself. This is why every scenario should be versioned too. At Railnova, we use GitHub for that purpose. It is not guaranteed that two different versions of a scenario will have the same behavior but for a given version number, every time the scenario is used, it will have the same behavior.

34 Nursery

As we already mentioned earlier, we use Python as the programming language. Most internal tools used at Railnova are written in Python because it is fast and easy to prototype ideas with it. Most of the employees there have at least basic knowledge of it which is not necessarily the case with other languages such as C or C++. We wanted the system to be used by everyone with limited computer science knowledge so we decided to use Python too.

34.1 Architecture

Nursery works on top of two main features, logging and reporting. Logging allows us to keep track of Nursery outputs. For every test, a file is created to log every one of them. Each line of output has an additional property which is the level of urgency of the output. The possible levels are the following:

- **DEBUG.** Used to output values that are not vital to understand what is going on but are of absolute necessity for debugging purposes.
- **INFO.** The default output level. Simple information messages.
- **WARNING.** Used for warnings. When an expected error occurs which is not breaking the system.
- **ERROR.** Error messages. For the messages that should not happen.
- **CRITICAL.** When the system is broken, this kind of message should be used.

The goal of this logging feature is to produce files that contain every single information needed to identify the reasons of test failure and contain all the tests parameters. A lot of time was put into this feature to allow for useful context related information for every line of output which, we find, is

a key component for Nursery.

On top of this logging facility, we added a reporting feature. This one is intended to update bug trackers, product tracking facilities or anything else needing information about the status of a particular hardware or software. Typically, this is used to update a product tracking server, Redmine, used at Railnova to keep an history of what happened in fingerprinting. When a test is done, the results is added on Redmine by saying: "Test <name> success/failure on RUG <serial number>", of course, other information are added such as the test version, test criteria and why the test failed. This can also be used to keep track of the status of a production line. At every stage of the production line, we can update the status of the product being build with this reporting tool.

Each type of reporting is described by a Python file. Adding one is a very easy process that any developer can achieve. To match the correct reporting to the test being performed, we specify a product type. Those are predefined in a configuration file that will be described later on. Each product type maps to a particular reporting type. For example, if we say that we want to perform a test on a product of type "RailsterOS", on the software then, we can have a reporting that will send reports by mails or send them on a bug tracker.

34.2 Configuration

The whole systems is based on one configuration file. This file contains all available test for Nursery. It also contains all output providers, all batch of tests and the list of all product that can be tested matched with their corresponding reporting. An output provider is a script that everyone can write used to provide data to the test. We used it, for example, to gather data in an online database. Those data are then send to each test as we receive them. A batch of test is just a list of test that needs to be performed in the given order.

Like we say earlier, we also have a concept of product type. Each possible product type is mapped to a given reporting file using a code similar to the one shown in the listing 15. The list of "tests batches" are listed using the code of listing 14 and finally, each test is defined using a code similar to the

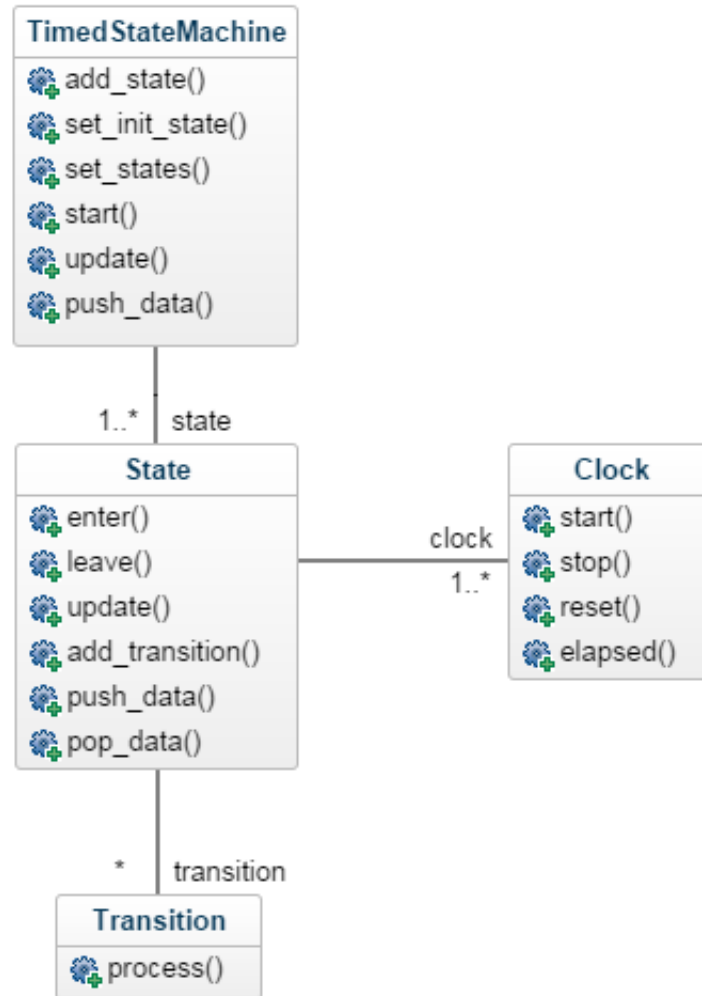


Figure 17: Class diagram of the timed automata implementation. Each scenario is a timed automata and each test is composed of one or many scenario.

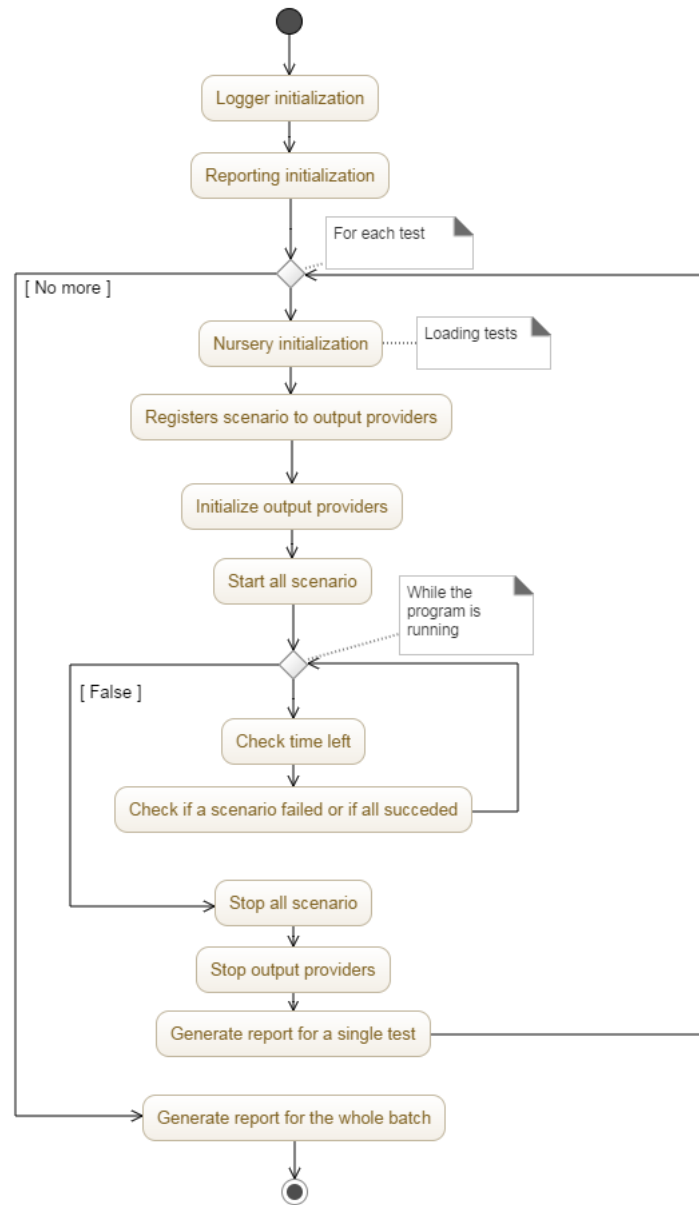


Figure 18: Activity diagram of the whole test processing. A batch of test is simply a list of one or more tests.

Listing 12: "Output provider configuration"

```
masterlist_op = [  
    { "type": "WANESY", "name": "WanesyProvider" },  
    { "type": "SERIAL_WANESY", "name": "SerialWanesyProvider"  
      ↪ },  
]
```

listing 13.

Several parameters are needed to add a test. **name** and **description** are simply used to display documentation about them. **l_product** is a list of product to which the test applied. This is purely used to warn the user if he tries to perform a test meant for RailsterOS on a battery pack for example. The duration specify the maximum duration of the test in second. After that, the test fails automatically. Finally, the **l_scenario** parameter is the most important one. It specifies all the FSM that are run in parallel to obtain the desired behavior. Most of the test used only one FSM because it proved difficult to conceive test thinking about multiple FSM at once. A typical case where it is useful to run multiple FSM at once is for timeout check. One of them checks continuously for timeout between data gathered by the output provider and the other one performs the test without having to manage this problem.

34.3 Command line tools

To run Nursery, the user can use a command line tool taking several inputs. Those inputs are the following:

- product: Type of hardware tested
- serial: RUG serial number to fetch information with output providers. Needed only for RUG testing.
- password: Password, if any, for serial connection.
- log: Level of information displayed in the log files, can be "critical", "warning", "info", "error" or "debug"

Listing 13: "Tests configuration"

```
l_test = [
  # Efficiency Vs Current
  { "name": "efficiency_vs_current",
    "l_product": ["battpack"],
    "description": "Test the efficiency against the current
    ↪ ",
    "duration": 24*3600,
    "l_scenario": [
      {"name": "EfficiencyVersusCurrent", "settings": {}},
    ], },

  # CAT test
  { "name": "cat",
    "description": "CAT simulator",
    "duration": 500,
    "l_scenario": [
      {"name": "ReplaySimulator", "settings": {
        "filename": "cat.simulator",
        "csv_delimiter": ",",
        "muted": True,
      }},
      {"name": "CATCheck", "settings": {}},
      {"name": "TimeoutCheck", "settings": {}},
    ], },

  # Charge Battery Pack
  { "name": "chargebattpack",
    "description": "Charge fully the battery pack",
    "duration": 7200,
    "l_scenario": [
      {"name": "ChargeBattPack", "settings": {}},
    ], },
]
```

Listing 14: "Batch of tests configuration"

```
l_batch = [
  {
    "name": "fingerprint",
    "l_test": [
      "serial",
      "symshow",
      "twctrl",
      "ifconfig",
      "qstat",
      "1970check",
      "gps",
      "timeout",
    ]
  },
]
```

- quiet: Used to remove console outputs
- batch: Batch name to be performed. This override the **test** parameter
- test: Name of the test to be performed
- logpath: Specify where to put the resulting log file
- checkmaster: used to verify the configuration file. Checking if no errors where introduced
- report: Indicate whether or not we want to generate a report. This allows us to disable the reporting feature
- fqm: Shortcut to run a batch named "fingerprint" and generating reports for those tests

34.4 Web-administration

A web-administration tool was also developed. Allowing users to manage testing easily. All features covered by the command-line tool are available

Listing 15: "Products configuration"

```
l_product_type = [
  {
    "type": "none",
    "description": ""
      Applied to all hardware/software. Mainly used to
      ↪ disable reporting
    "",
    "reporting": "NoneReporting",
  },
  {
    "type": "rug1",
    "description": ""
      Railster Universal Gateway 1
    "",
    "reporting": "RugReporting",
  },
  {
    "type": "ros",
    "description": ""
      Railster-OS. Used for software testing
    "",
    "reporting": "NoneReporting",
  },
  {
    "type": "battpack",
    "description": ""
      Testing for the battery pack
    "",
    "reporting": "NoneReporting",
  },
]
```

Nursery - shitpi1	Status	Configuration	Test	Batch	Logs	Trace
24-05 09:26:54	DEBUG	[Nursery]	End			
24-05 09:26:54	INFO	[Nursery]	Told to quit: SerialWanesyProvider			
24-05 09:26:54	INFO	[Nursery]	Told to quit: ChargeBattPack			
24-05 09:26:53	INFO	[Nursery]	All scenario succeeded ! End of simulation...			
24-05 09:26:53	DEBUG	Time: 54				
24-05 09:26:53	INFO		Request to files/upload			
24-05 09:26:53	INFO	[ChargeBattPack]	Closing			
24-05 09:26:53	INFO	[ChargeBattPack]	Success			
24-05 09:26:53	INFO	[ChargeBattPack]	Leaving state: charging			
24-05 09:26:53	DEBUG	[charging]	Data pop SERIAL_WANESY			
24-05 09:26:53	DEBUG	[charging]	Data pushed SERIAL_WANESY			
24-05 09:26:52	DEBUG	Time: 53				
24-05 09:26:51	DEBUG	Time: 52				
24-05 09:26:51	INFO	[ChargeBattPack]	Entering state: charging			
24-05 09:26:51	INFO	[ChargeBattPack]	Leaving state: symshow1			
24-05 09:26:51	INFO	[ChargeBattPack]	3253, 3252, 3234, 14745334			
24-05 09:26:51	DEBUG	[symshow1]	Data pop SERIAL_WANESY			
24-05 09:26:51	DEBUG	[symshow1]	Data pushed SERIAL_WANESY			
24-05 09:26:50	DEBUG	Time: 51				
24-05 09:26:49	DEBUG	Time: 50				
24-05 09:26:49	DEBUG		Prompt for Serial Wanesy available			
24-05 09:26:48	DEBUG	Time: 49				
24-05 09:26:47	DEBUG	Time: 48				
24-05 09:26:46	DEBUG	Time: 47				
24-05 09:26:45	DEBUG	Time: 46				
24-05 09:26:44	DEBUG		Attempt 2 for login			

Figure 19: Main interface for the web-admin tool. Here we can see the last log file generated.

Log files

Clear all				
Serial	Batch	Test	Time	Action
342		chargebattpack	2017-05-24 09:25:33	View Trace Delete
342		chargebattpack	2017-05-24 09:18:03	View Trace Delete
342		chargebattpack	2017-05-24 09:16:18	View Trace Delete
342		chargebattpack	2017-05-24 09:11:15	View Trace Delete
342		chargebattpack	2017-05-19 14:33:59	View Trace Delete
342		chargebattpack	2017-05-19 11:57:07	View Trace Delete
342		chargebattpack	2017-05-19 11:56:56	View Trace Delete

Figure 20: The log page displaying all logs generated and some information about them.

through the web-administration tool. We also added three others feature, the first one is the ability to browse all available tests and their documentation (figure 21) and the second one is the ability to see how a test was processed by displaying a automatically generated graph of the test and highlighting nodes of the graph depending on the current step selected in the trace file displayed next to the graph (figure 22). Finally, the ability to manage log files is a feature that is not covered by the command line tool (figure 20). All those features makes the web-administration tool a lot easier to use than the command line tool.

Part VII

Conclusion

Putting the complexity as a limitation gave us a software that can run on any modern system. The simulation is hosted on Raspberry Pi which are low end system. This allowed us to build a lot of Nursery system for a really cheap price, Raspberry Pi costing around fifteen euros. We made around sixteen Nursery system separated into two areas. Height of them dedicated for RailsterOS verification and height of them used mainly for fingerprinting. So, using the work done in this thesis, we are now able to fingerprint the faulty RUG way faster. We need only one guy to analyze up to height of them in parallel using the Nursery systems we just talked about. The same thing hold true for RailsterOS testing. We can now leave newly developed version of it running on Nursery for days and get a report of all the errors that happened afterwards instead of waiting for errors to happen into the production environment. The total count of line of code in Python is about 7000 and 17 tests where developed with more to come.

My personal analysis lead me to think that the success of Nursery is based on the following key point:

- Will it be used by other users?
- Will it save time or make life easier for some people?
- Can we gain money from it?

Clearly, detecting fault earlier allows us to save money and time. No need to call back some hardware and less fault in production means less loss of money. We do not longer need to invest days of analyze for fingerprinting, all it takes is to plug the faulty RUG into Nursery and click a button. This could easily take up to 3 hours per RUG where now, we can do it in just one day of work for height RUG using more intensive testing cases! Lastly, some guys of the hardware team and from the supply chain were introduced with Nursery. We showed them how to write test for Nursery and they loved the way things were made and how simple it was. Their test were done in approximately one hour. This lead us to think that, with proper formations

and documentation, other users could gladly use Nursery.

I think that Nursery is a key asset that will become more and more used inside of Railnova. We even talked about using it for production lines in the future. But a lot of work is yet to be done on the accessibility aspect of it. Proper documentation needs to be written, some ideas are expected to ease a bit more the writing of tests like adding a generic state allowing us to create a transition from "any state" to one particular state without needing to repeat this transition to every other states and there's probably a lot more thing we didn't think about.

It is important to notice that while Nursery is useful to check that software requirements are met, it is still important to perform unit testing and integration testing based on a code coverage criterion. Nursery use is limited to functional testing, system testing, regression testing and acceptance testing.

A Abstraction of Inputs/Outputs

This part of the work is dedicated on describing how we managed to write simple piece of software that assure the link between Nursery and the real world/RUG.

A.1 Outputs

Let us start with the outputs. By outputs we mean all the data produced by the RUG or, more generally speaking, all data produced by the reviewed system. Those have been abstracted with a layer of object called output providers. Their goal is to retrieve outputs from various places and feed them into Nursery as they go.

For example, we can imagine that we want to receive some data each time the system sends data to a server, each time the system produce a sound, etc. All of this is possible as we simply need to write a script that detects that and use some kind of API to feed them through Nursery.

A.1.1 How does it works

So, internally, each output provider defines a type of data it provides. For example, lets say we want to send data to nursery each time someone put a message on Twitter, we could name our data type: "TWITTER". Then, each test that is interested in data of type "TWITTER" will be subscribed to our output provider. It also allows us to ignore output providers for which no test are interested in. Practically speaking, the only output providers running for a given test will be those that provide a data type useful for the test.

Our provider is bundled with a mean of communication with all scenario that are subscribed to the data type it provide. With it, we can send data to every listener with one simple call. All the communication process is managed internally in Nursery. In our provider, we have a function that allows us to broadcast data to all listener. With this approach, we can write output providers for any type of data we can think of, all we have to do is defining the data type we provide and which output provider provides it in a configuration file and then implement this output provider, that's it, now any

scenario can listen to this particular data type.

One limitation though is that every data type can be produced by at most one output provider. On the other hand, every output provider can produce as much data type we want if this becomes a requirement at some point.

A.1.2 Wanesy

The first data type we have develop is the WANESY data type. Data of type WANESY are triggered when we receive a new pack of data sent by Railster-OS to our server online. The method to retrieve them is simply to poll a JSON generated list of message by HTTP and check timestamp of generated messages, when a newer timestamp appears, we get the message and send it into Nursery.

Another more effective way of doing this would be by direct connection to the database but this would probably introduce security issues and is not really required for our use case. Note that it is still feasible in future version of this work.

A.1.3 Log

This data type is more of Grey-Box testing as it requires a direct serial connection into the studied system so we assume that we are working on some kind of RUG and that we know where the log are located on it. Nevertheless, this data type ("LOG") is useful to provide more complex information wen fault arise and also help us to design more intrusive tests.

So even if they are a bit out of scope here, they do not invalidate the fact that our system can work on Black-Box testing.

To design those test, as we previously stated, we simply initiate a serial connection over the RUG and then download the file located in the log folder. Then, our script process it and send new lines into Nursery.

A.1.4 Miscellaneous

As we talked in sub-section A.1.3, we can initiate a serial communication with the RUG. Using this serial communication, we can send command to it in an Unix environment so it's easy for us to check a lot of properties for our tests needs. Of course, this is clearly white-box testing, we know how the system is supposed to work so we can ask it if some key file are in place, if all environment variables seems to be correct, do some statistics on some values to be sure they have correct values all along our tests and things like that.

A.2 Inputs

Some of our test make use of external devices to produces inputs that are feeded to the RUG or to the hardware being tested. Those devices are, for example, a power module that can charge the battery of our RUG, thermionic chamber that can manipulate the current temperature to stress test our hardware in extreme conditions and things like that.

We also have more direct inputs like the Ethernet connection, we want to be able to disconnect or re-connect the tested process or hardware.

A.2.1 Power Management

To control the power status, we need to send a command to a Wago module like the one of the figure 23. Those modules are connected via an Ethernet connection thus we need to open a TCP port to communicate with it and then send a command to select which power supply we want to control and what we want to do (power on or power off).

A.2.2 Connectivity

Controlling the connectivity is not something we did but this is easily done by using some advanced switch. We simply need to communicate with it and send commands to deactivate the Ethernet port to which the RUG is connected.



Figure 23: Module used to manage the power status of a RUG

A.3 The generic way

Some techniques we mentioned, like in sub-section A.1.3 and sub-section A.1.4, use some kind of white-box testing. As one could expect, those techniques can only be used for one given device. We cannot initiate a serial connection with every process or hardware we want and all of them don't have the same Unix environment to dialog with.

But even the ones that applies to black-box testing can only be specific to some restraint set of process or hardware. Here, our techniques are all specific for RUG hardware. So every time we need to test a new kind of process or hardware, we will need to write some code to retrieve outputs and provides inputs for it. That's what we kept in mind all along this project by designing what we called O.P..

A.3.1 Outputs

O.P. were designed to provides asynchronous capabilities to our tests. Polling Inputs/Outputs (I/O) is a process that takes a lot of times compared to the execution time of our test. With O.P., we get the data when they are ready and we don't have to wait for them by freezing all the test. Two ways of

retrieving data were implemented.

The most trivial way is simply to send data periodically to all test that needs it. The test knows he will receive data but he does not know when. The test must be designed accordingly. This is useful to fetch data that are not fixed like a speed, the current time or the remaining fuel for example.

The second way is to ask for data from the test. The call is asynchronous so you simply notify the O.P. that you are ready to receive some data or that you want it to fetch some data right now. This allows us to ask for one-time data like a serial number or other kind of data that are not supposed to change depending on the time.

So the first method is more for data you want to be able to monitor and the second method is more for data you will only need once.

A.3.2 Inputs

For inputs, we simply need some software to which we can send commands so we don't need so much abstraction as for the outputs. We are not in an asynchronous system. The way to go we adopted was simply to develop some software that accepts command and send inputs to whatever we want.

One example was a software we used that can control a thermionic chamber. With it, we were able to control the temperature in which our RUG was evolving. And of course, we can use it for other systems too, and we did it to test other pieces of hardware.

Another example, more specific, was a software used to control a device that could apply different electric loads to any device we connected with it. This was useful for some hardware test that had nothing to do with the RUG.

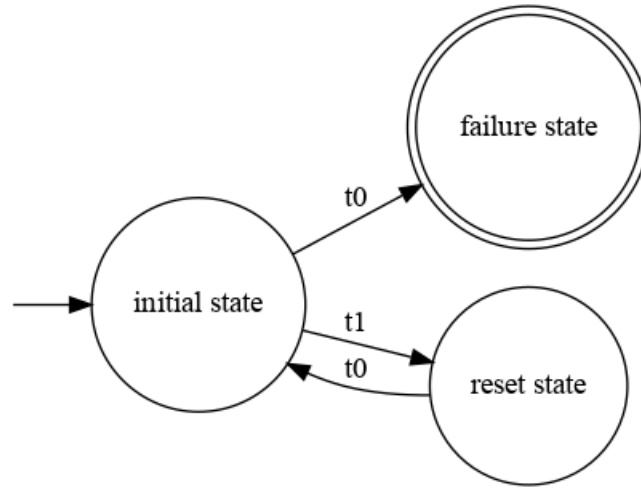


Figure 24: FSM for the timeout check

B Test cases

Here we will give explanation and details on some practical cases we had to deal with.

B.1 Timeout check

This test was designed to make sure that the RUG is sending information to our servers. To do that, we need an O.P. that will fetch all messages received from the studied RUG and fetch them to our FSM. Our FSM will then check that data are received at least every 30 seconds.

The figure 24 shows how we designed this test. Transition are, of course, complex so we only show a simplified name for them. They are quite obvious given the rest of the diagram.

B.2 Date check

This test verify that the system uses the correct date. With an incorrect date set, the messages sent by it to the Railnova servers will be discarded resulting in a loss of communication from this device.

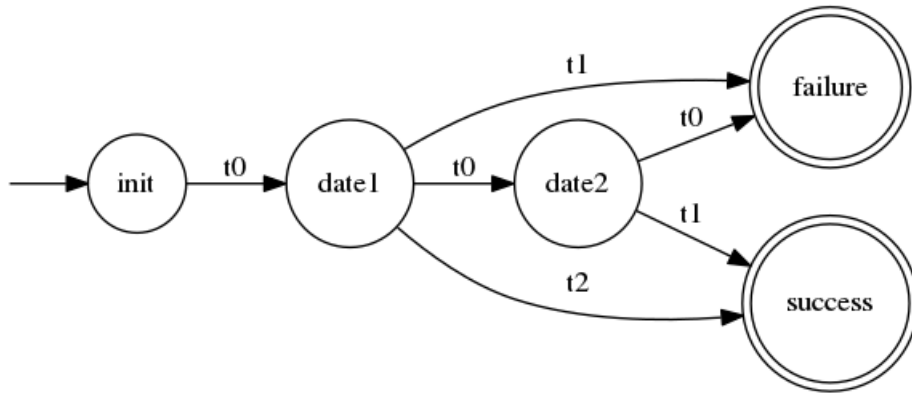


Figure 25: FSM for the date check

This test uses the serial connection and make uses of Unix commands directly on the system thus this is not a black-box testing processes.

The most complicated part here is to get the serial connection, a lot of troubles can arise while trying to log-in the system. Once we have a valid serial connection, we can simply issue a "date" command and parse the result. The date is updated through GPS/GSM antenna communication and this process can take up to an hour so if the date is not set right (if the date appears to be in 1970), we simply wait an hour and then, issue the test again.

1970 is the date you get when the timestamp is not set. This is because of how Unix and timestamp systems works.

B.3 Battery cycle

We made some test that where not related to the RUG. One of those was able to charge a battery and then discharge it. The goal of this was measure the electrical levels all along those steps to determine whether or not the battery is working.

As shown in the figure 26, we made several cycle of charge/discharge. This particular test was designed for a piece of hardware and not for a software. This is a sufficient proof to show that our system work as well for a software

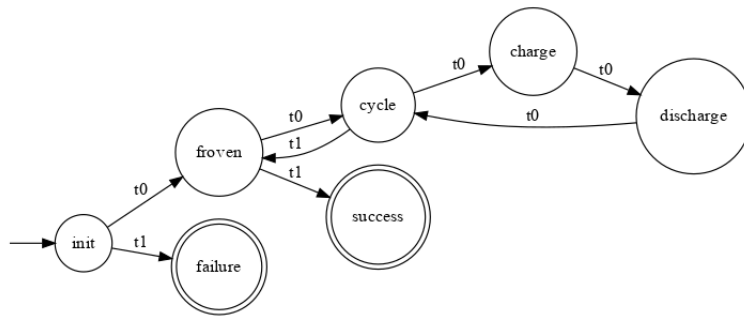


Figure 26: FSM for the battery cycle

than it does for hardware.

References

- [1] “The economics of testing,” http://www.riceconsulting.com/public_pdf/STBC-WM.pdf, [Online; accessed 12-April-2017].
- [2] “Software tesing - economics,” https://en.wikipedia.org/wiki/Software_testing#Economics, [Online; accessed 12-April-2017].
- [3] T. S. Chow, “Testing software design modeled by finite-state machines,” *IEEE Trans. Softw. Eng.*, vol. 4, no. 3, pp. 178–187, May 1978. [Online]. Available: <http://dx.doi.org/10.1109/TSE.1978.231496>
- [4] M. Fantinato and M. Jino, “Applying extended finite state machines in software testing of interactive systems,” in *Interactive Systems. Design, Specification, and Verification*, J. A. Jorge, N. Jardim Nunes, and J. Falcão e Cunha, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 2003, pp. 34–45.
- [5] “Software development,” https://en.wikipedia.org/wiki/Software_development, [Online; accessed 19-December-2016].
- [6] H. Mooz and K. Forsberg, “4.4.3 a visual explanation of development methods and strategies including the waterfall, spiral, vee, vee+, and vee++ models,” *INCOSE International Symposium*, vol. 11, no. 1, pp. 610–617, 2001. [Online]. Available: <http://dx.doi.org/10.1002/j.2334-5837.2001.tb02348.x>
- [7] B. W. Boehm, “Software process management: Lessons learned from history,” *ICSE ’87 Proceedings of the 9th international conference on Software Engineering*, pp. 296–298, 1987.
- [8] “Software prototyping,” https://en.wikipedia.org/wiki/Software_prototyping, [Online; accessed 17-May-2018].
- [9] “Principles behind the agile manifesto,” <http://agilemanifesto.org/principles.html>, [Online; accessed 17-May-2018].
- [10] “What is scrum? an agile framework for completing complex projects,” <https://www.scrumalliance.org/why-scrum>, [Online; accessed 29-April-2017].

- [11] “Software verification and validation,” https://en.wikipedia.org/wiki/Software_verification_and_validation, [Online; accessed 21-December-2016].
- [12] “Ieee standard glossary of software engineering terminology,” *IEEE Std 610.12-1990*, pp. 1–84, Dec 1990.
- [13] A. W. Larry Brader, Howie Hilliker, “Testing for continuous delivery with visual studio 2012,” <https://msdn.microsoft.com/en-us/library/jj159344.aspx>, [Online; accessed 31-October-2016].
- [14] R. Patton, *Software Testing (2Nd Edition)*. Indianapolis, IN, USA: Sams, 2005.
- [15] “Difference between static testing and dynamic testing,” <http://www.softwaretestingclass.com/difference-between-static-testing-and-dynamic-testing/>, [Online; accessed 17-May-2018].
- [16] L. Williams, “Testing overview and black-box testing techniques,” pp. 1–26, 2006.
- [17] A. K. Dorota Huizinga, *Automated Defect Prevention: Best Practices in Software Management*. Wiley-IEEE Computer Society Press, September 2007.
- [18] —, *Testing in Software Development*, ser. British Computer Society Monographs in Informatics. Cambridge University Press, February 1987.
- [19] “Requirement based testing,” https://www.ibm.com/support/knowledgecenter/SSJJ9R_5.0.2/com.ibm.rational.test.qm.doc/topics/c_reqbased_tst.html, [Online; accessed 12-April-2017].
- [20] “Requirements based testing process overview,” pp. 1–18, 2009.
- [21] “Finite-state machine,” https://en.wikipedia.org/wiki/Finite-state_machine, [Online; accessed 22-March-2017].
- [22] S. Yu, “Handbook of formal languages, vol. 1,” G. Rozenberg and A. Salomaa, Eds. Berlin, Heidelberg: Springer-Verlag, 1997,

- ch. Regular Languages, pp. 41–110. [Online]. Available: <http://dl.acm.org/citation.cfm?id=267846.267848>
- [23] D. S. M. O. Rabin, “Finite automata and their decision problems,” *IBM Journal*, pp. 1–12, April 1959.
 - [24] “Nondeterministic finite automaton,” https://en.wikipedia.org/wiki/Nondeterministic_finite_automaton, [Online; accessed 16-April-2017].
 - [25] K. Salomaa and S. Yu, “Nfa to dfa transformation for finite languages,” in *Automata Implementation*, D. Raymond, D. Wood, and S. Yu, Eds. Berlin, Heidelberg: Springer Berlin Heidelberg, 1997, pp. 149–158.
 - [26] “Alternate finite automaton,” https://en.wikipedia.org/wiki/Alternating_finite_automaton, [Online; accessed 16-April-2017].
 - [27] R. Alur and D. L. Dill, “A theory of timed automata,” *Theoretical Computer Science*, vol. 126, no. 2, pp. 183 – 235, 1994. [Online]. Available: <http://www.sciencedirect.com/science/article/pii/0304397594900108>
 - [28] L. Rabiner and B. Juang, “An introduction to hidden markov models,” *IEEE ASSP Magazine*, vol. 3, no. 1, pp. 4–16, Jan 1986.
 - [29] “What is scrum? an agile framework for completing complex projects,” https://en.wikipedia.org/wiki/Markov_model, [Online; accessed 10-May-2017].
 - [30] J. Bengtsson, K. Larsen, F. Larsson, P. Pettersson, and W. Yi, *UPPAAL — a tool suite for automatic verification of real-time systems*. Berlin, Heidelberg: Springer Berlin Heidelberg, 1996, pp. 232–243. [Online]. Available: <http://dx.doi.org/10.1007/BFb0020949>
 - [31] G. Behrmann, A. David, and K. G. Larsen, *A Tutorial on Uppaal*. Berlin, Heidelberg: Springer Berlin Heidelberg, 2004, pp. 200–236. [Online]. Available: http://dx.doi.org/10.1007/978-3-540-30080-9_7
 - [32] K. G. Larsen, P. Pettersson, and W. Yi, “Uppaal in a nutshell,” *International Journal on Software Tools for Technology Transfer*, vol. 1, no. 1, pp. 134–152, 1997. [Online]. Available: <http://dx.doi.org/10.1007/s100090050010>

- [33] M. Kwiatkowska, G. Norman, and D. Parker, *PRISM 4.0: Verification of Probabilistic Real-Time Systems*. Berlin, Heidelberg: Springer Berlin Heidelberg, 2011, pp. 585–591. [Online]. Available: http://dx.doi.org/10.1007/978-3-642-22110-1_47
- [34] A. Hinton, M. Kwiatkowska, G. Norman, and D. Parker, *PRISM: A Tool for Automatic Verification of Probabilistic Systems*. Berlin, Heidelberg: Springer Berlin Heidelberg, 2006, pp. 441–444. [Online]. Available: http://dx.doi.org/10.1007/11691372_29

Glossary

- AFA** Alternating Finite Automaton. 37, 38, 65
- CTMC** continuous-time Markov Chain. 45
- daemons** Sub-programs running inside Railster-OS adding functionalities to it. 49
- DFA** Deterministic Finite Automaton. 36, 41
- DSL** Domain-specific language. 46, 70
- DTMC** Discrete-Time Markov chain. 45
- EFSM** Extended Finite State Machine. 33–35
- FQM** Fleet Quality Management. 52
- FSM** Finite State Machine. 9, 13, 30–38, 43, 46, 47, 58, 63–65, 80, 94–96
- GPL** General-purpose language. 70
- I/O** Inputs/Outputs. 92
- MDP** Markov decision process. 45
- NFA** Non-Deterministic Finite Automaton. 35, 36, 41
- O.P.** Output Provider. 60, 62, 92–94
- PA** Probabilistic Automaton. 45
- PTA** Probabilistic Timed Automaton. 45
- Railster-OS** A set of sub-program running on the RUG defining the behavior of it. 10, 11, 48–50, 55, 90, 101
- RUG** Railster Universal Gateway. 11, 48–52, 58, 60, 80, 89–95, 101
- TA** Timed Automata. 38, 41–43, 66–68, 74
- UI** User Interface. 65