Sayfa1

TA7W ICSP <- TX1 VIN RX0 GND -> (TD12/P20) (TD11/P21[EKLE]) <-RST RST -> <-(TD12/P20) <-GND 5V -> (TD12/P40) * SQL (TD12/P14) D2 A7 -> * TONE OUT (RA2/P10) <-D3 **ARDUINO** A6 -> * MUTE_1 (TD11/P22) <-D4 NANO A5 -> I2C_CLK (TD12/P2) D5 V3 * TUS INTR (TD12/P12) <-A4 -> I2C DATA (TD12/P3) D6 <-A3 -> * PLL SEC (TD12/P8) D7 A2 -> PLL SEC (TD11/P5) (TD9/P5-P9) * SPI_DATA (TD12/P5) <-D8 A1 -> POWER_CONTROL (TD11/P2) * SPI_CLK (TD12/P6) D9 <--> POWER ON-OFF (TD12/P9) PTT_OUT (J2/P3) <-D10 REF -> * PTT_IN (TD12/P1) <- D11 3V3 -> * BS0 (TD11/P27) <- D12 D13 -> BS1 (TD11/P28) **USB** TA2GY ICSP VIN -> <- TX1 RX0 GND -> (TD12/P20) (TD11/P21[EKLE]) <-<-RST RST -> (TD12/P20) <-GND 5V -> (TD12/P40) SQL (TD12/P14) <-D2 Α7 -> **ARDUINO** * TONE OUT (RA2/P10) <-D3 A6 -> * TUS INTR (TD12/P12) <-D4 NANO A5 -> * PTT OUT (J2/P3) <- D5 V3 A4 -> POWER CONTROL (TD11/P2) * PLL SEC (TD12/P8) <-D6 A3 -> PLL SEC (TD11/P5) (TD9/P5-P9) * MUTE 1 (TD11/P22) D7 A2 -> POWER_ON-OFF (TD12/P9) <-D8 * SPI CLK (TD12/P6) -> I2C_DATA (TD12/P3) BS1 (TD11/P28) <-D9 A0 -> I2C_CLK (TD12/P2) * SPI_DATA (TD12/P5) REF -> <-D10 * BS0 (TD11/P27) <-D11 3V3 -> D13 -> * PTT_IN (TD12/P1) <- D12 USB TA7W -> TA2GY D2 -> D2 A0 -> A2 A1 -> A4 D3 -> D3 D4 -> D7 A2 -> A3 A4 -> A1 D5 -> D4 A5 -> A0 D7 -> D6 D8 -> D10 D9 -> D8 D10 -> D5 D11 -> D12 D12 -> D11 D13 -> D9