

Unit - 3  
part - c

1. Write a program to toggle all the bits of every 200ms. Assume crystal frequency is 11.0592 MHz and the system is using DS89C420/30/40/50.

; Tested for DS89C420 of 11.0592 MHz.

MOV A, #55H

AGAIN: MOV PI, A

ACALL DELAY - 200 m

CPL A

SJMP AGAIN

;----- Time delay

DELAY - 200 m;

MOV R5, #9

HERE 1: MOV R4, #242

HERE 2: MOV R3, #255

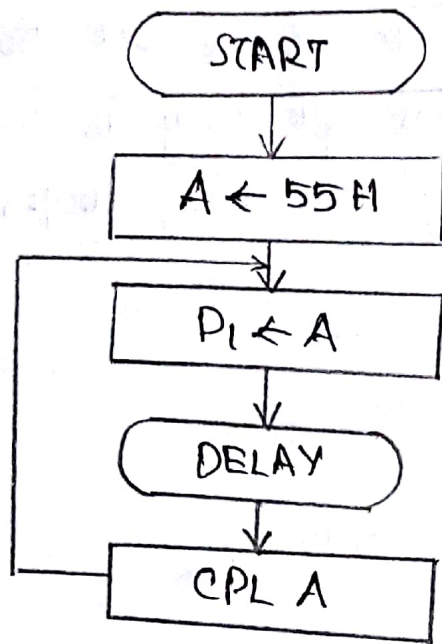
HERE 3: DJNZ R3, HERE3

DJNZ R4, HERE 2

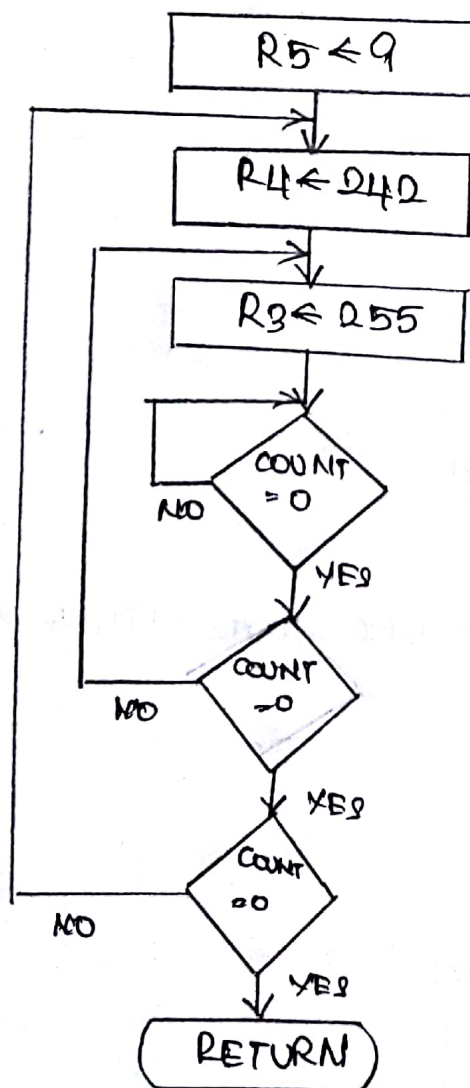
DJNZ R5, HERE1

RET

Delay  $9 \times 242 \times 255 \times 4 \mu\text{s} \times 90 \text{ns} = 199,940 \mu\text{s}$



DELAY:



## PART-C

### UNIT-2

4. Develop a program using the ADI instruction to add two hexadecimal numbers 3AH and 48H and to display the answer at the output port.

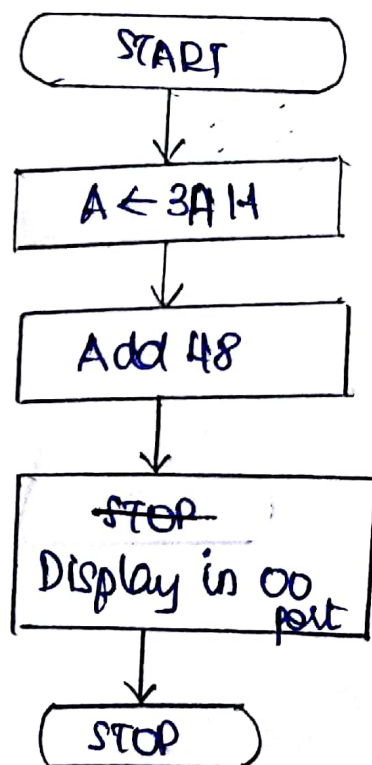
```

MVI A, 3AH
ADD 48H
OUT 00H
HLT
    
```

```

3A-0011 1010
48-0100 1000 (+)
-----
0100010010
    
```

### FLOWCHART:



9. Write a program to transfer 50 bytes of data from memory location starting from 2000H to 3000H using the memory pointer concept in 8085 microprocessor.

```
LXI H, 2000H
LXI D, 3000H
MVI C, 32H (= 50)
Repeat MOV A, M
      STAX D
      INX H
      INX D
      DCR C
      JNZ Repeat
      HLT.
```

10 i) Assume the SP register contains 2099H, register B contains 32H and register C contains 57H. Write the instructions to save the contents of the BC register pair on the stack and specify the register contents (SP, B and C) after execution

LXI SP, 2099H

LXI B, 3257H

PUSH B

HLT

AFTER EXECUTION:

$SP = SP - 2 = 2097H$

$B = 32H$

$C = 57H$

MEMORY

2098 : 32H (B)

2097 : 57H (C)



ii) FIND THE TWO'S COMPLEMENT OF A 16bit DATA WITH  
EXAMPLE

```
LXI B,2345H
MOV A,C
CMA
MOV E,A
MOV A,B
CMA
MOV D,A
LXI H,0001H
DAD D
SHLD 2000H
HLT
```

OPERATION:

23  
B  
0010 0011

45  
C  
0100 0101

CMA  
D  
1101 1100  
H  
0000 0000

E  
1011 1010  
L  
0000 0001

DAD D

1101 1100  
2001H  
↓  
DC

1011 1011  
2000H  
↓  
BB