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## QuantumLeap - Virtual Platform Simulation Acceleration

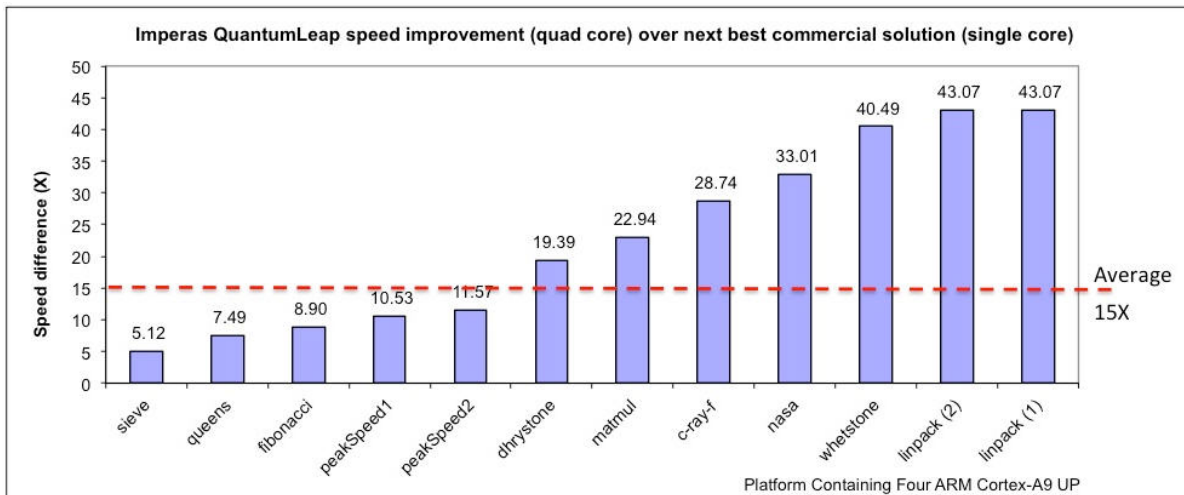
### Overview

QuantumLeap is a parallel simulation performance accelerator that leverages a new synchronization algorithm to provide the fastest virtual platform software execution speed available. The execution performance of this new technology has been measured on average at 15 times faster than the nearest commercial solution using standard benchmarks.

Many current System-on-Chip (SoC) hardware platforms, for example mobile and server devices, incorporate multi-core embedded processors coupled with hardware accelerators, all executing in parallel. The performance of existing, single-threaded virtual platform simulators does not adequately scale for these SoCs, creating a barrier to efficient virtual platform-based software development.

QuantumLeap eliminates this barrier by allocating the simulated cores across all the processors in a host machine. By ensuring the efficient synchronization of these cores, near linear scaling of the simulation across the multiple host processors has been observed, with the impact of inter-core communication kept to a minimum. Furthermore, QuantumLeap provides a transparent use model, with no change required to the software-under-test, the virtual platform models or the development environment, while ensuring fully deterministic simulation execution.

The execution performance of this new technology has been measured on average at 15 times faster than the nearest commercial solution using standard benchmarks. Simulation performance of great than 16 Billion Instructions-Per-Second has been observed for a virtual platform using an OVP model of a multi-core ARM Cortex-A9 running on a standard 3.4 GHz quad core host PC.



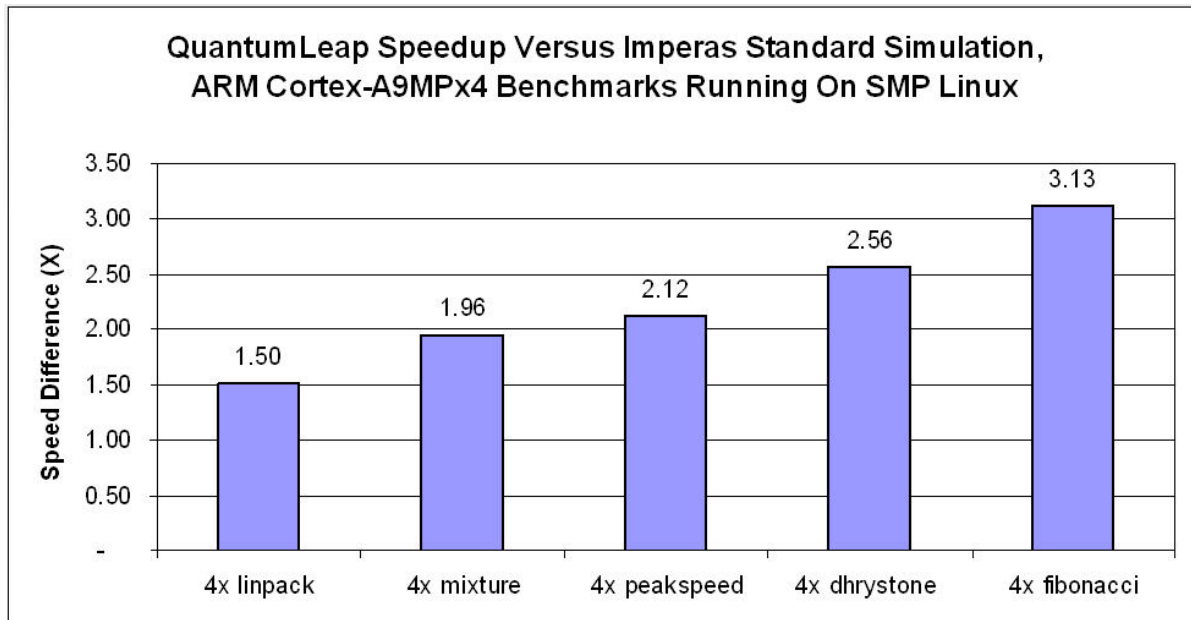
QuantumLeap is available as an option to the C\*DEV, S\*DEV, M\*DEV and M\*SDK products.

## Imperas Simulation Technology Performance Innovation

Imperas' simulation technology is based on a Just-In-Time (JIT) Code Morphing engine that streams simulation execution to maximize performance, providing the fastest software processor models available today. The company's ToolMorphing™ algorithm further extends this capability by neutralizing the performance impact of the running of software verification tools within simulation.

The QuantumLeap accelerator extends this approach to include parallel engine execution, spreading a virtual platform simulation across multiple host PC processors. QuantumLeap's proprietary synchronization algorithm minimizes the impact of communication between the parallel cores, allowing them to scale across available host PC processors as much as possible.

QuantumLeap operates on platforms that incorporate both Asymmetric Multi-Processing (AMP) and Symmetric Multi-Processing (SMP) schemes. It has been designed to effectively handle the inter-core communication overhead challenges associated with the most complex SMP processor architectures including, for example, devices based on the state-of-the-art ARM Cortex-A57 and Imagination MIPS Warrior P5600 multi-core processors.



QuantumLeap allows full access to the Imperas Multi-core Software Development Kit (M\*SDK) verification capabilities. The accelerator includes the ToolMorphing and SlipStreamer™ capabilities, such that the full tool suite operates with minimal impact to performance and no adverse affect on simulated software operation. QuantumLeap also operates with the full Open Virtual Platforms™ (OVP™) model library, allowing the entire, 125+ range of processor models to be accelerated.

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