

## **ASSIGNMENT**

Course Code : CSE 323

Course Title : Operating System

Topic : SPRING- 2024 Question solve

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## Ans: to the aves no: 1(0)

To solve the question based on the Bonkers Algorithm: Given,

Total Instances: A=14, B=10, C=13, D=12

(1)

Process	A	llo c	atio	n	M	0×1	mur	n		Nee	ed		A	rail	able	
	A	В	C	D	A	B	C	D	A	B	C	D	A	В	C	D
Po	0	2	3	0	1		5			2					8	
$\rho_{l}$	3	1	0	l	5	2	0	2	2	1	0	1	7	7	11	10
$\rho_{L}$	1	0	1	0	1	2	2	2	0	2	0	2	10	8	П	11
$\rho_3$	0	0	0	1				1							12	
P4	2	1	1	0				1					11		12	
P5	l	1	0	0				1	_	i			•		13	
	7	5	5	2									14	10		12
													1	10	13	12

Here, Need = Maximum - Allocation

, safe sequence; Po > P1 -> P2 -> P3 -> P4 -> P5

Ans:

(11)

Gliven Request are!

Po (13,5, 3,1)

P1 (0,1,0,0)

Now we have to calculate is those request are immediate granted on not ,

conditions for granting a Request:

one pequest from process Po for (13,5,3,1):-

1. Request < need!

Request [Po] & Need [P]

- · Need [Po]=(1,2,2,0)
- · Request[p.] = (13,5,3,1)

For this condition pequest not statisfied.

2. Request < Available!

Request [Po] & Arailable [Po]

Also for this condition request not satisfied.

Now another request from  $P_1(0,1,0,0)$ :where both condition are satisfied.

1. Request < need!

Request [Pi] < Need [Pi]

- · Need[Pi] = (2,1,0,1)
- · Request [Pi] = (0,1,0,0)

2. Request < Available:

Request [Pi] < Available [Pi]

- · Amilable[P] = (7,5,8,10)
- . Request[Pi] = (0,1,0,0)

Now we have to calculate safe sequence for process Pi using Bankers Algorithm!

Process	Al	lo co	to	n	M	oxin	าบท	l	N	eed			Av	Avoilable				
, , , ,	A	B			A	В	c	D	A	В	0	P	A	В	C	D		
Po		2			1	4	5	0	١	2	2	0	¥	4	8	10		
Pi	3			١	5	2	0	2	2	0	0	1	ヌ	6	11	10		
PZ	1	0	1	0	1	2	١	2	0	2	0	2	10	8	(1	11		
P3	0	0	0	1	0	١	0	ı	0	Ţ	0	0	11	8	12	11		
Pq	2	I	l	0	3	2	3	1	1	1	2	١	11	8	12	12		
P5	1	1	0	0	2	2	)	1	1	1	1	1	13	9	13	12		
	Z	6	5	2	-							-4	14	10	13	1 2		

safe sequence! Po > PI > P2 > P3 > P4 > P5

so, here for process P1. It will be granted immediately.

Ans: to the aves no! 1(b)

Given that,

Resource nodes!

- · Ri(Laptops) = 3 instances
- · Rz(Routers) = 2 Instances
- · Ro (Hand Drives) = 2 instances

Process nodes; A, B, C, D

Now we have to create a Resource Allocation graph from given instances & process:

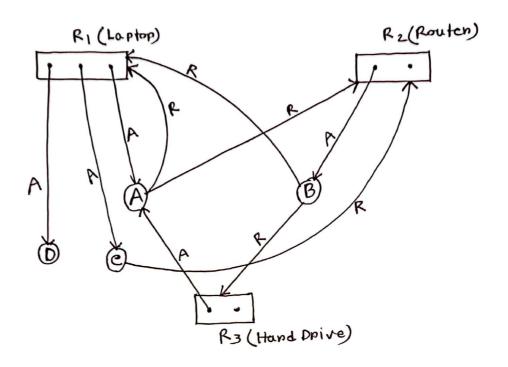


Figure: Resource Allocation graph

### Ans: to the aves no! 1(c)

Given Resources are Ro, RI, R2, R3, R4, R5. Which have multiple instances. And each of the process was on ossigned by those resources or requested to resources.

Now, If all processes can complete, the system is safe, and we provide the safe sequence.

If any process cannot complete due to insufficient resources, the system is unsafe.

50, At first we can start from any of the process where he can completed.

#### steps!

1. Po Processt Here po is assigned one instance from R1 and requested one mone instance from R2. 50 both condition are satisfied, for thatis why the Po process was complete and freed up the resources.

2. Process! Here Pr is assigned one instance from Ro and pequest another instance to Ro. And also request another from Rs. where the condition are fulfilled so the process are complete and freed up resources.

30 This way the processes will complete. How we see the completion in table.

	A	110	cat	e			Rea	vest	-		eon	men	+ A	vaibble	
Process	Ro		R2	Rз	Rq	Ro	$R_1$	R2	R3	$R_{\dot{t}}$	Ro	R1	R2	R3 R4	
Po	0	t	0	0	0	0	0	1	σ	0	2	0	0	00	
Pı	0	0	0	0	1	ı	0	1	0	0	20	10	0	0 1	
PL	1	0	0	0	0	1	0	O	1	0	2	10	1	1 1	
ρ3	1	c	)	0	1	0	0	0	0	1	3	0	1	1 1	
P4	0	0	(	0	0	0	0	t	1	0	40	1	2	1 2	
ρ5	0	0	C	) (	0	0	0	0	0	0	4	10	20	1 2	
											4	1	2	2 2	ì

safe sequence: Po > R > P2 > P3 > P4 > P5 Ars:

## Ans: to the aves no 12(a)

Dynamic partitioning or variable sized partitioning is a memory management scheme where the memory is divided into partitions based on the sizes of incoming processes.

#### Given.

Total physical memony: 8 GB

Partition 1: 2 GB

Partition 21 3 GB

partition 31 1 GB

unallocated: 8GB-(2+3+1)GB = 2GB

In dynamic pantitioning it works with 4 types of partitioning!

#### 1. First fit!

suppose some process will applie in first fit
partitioning with some sizes then it partitions like;

			Photos
Pantition	Memory (81B)	process (GB)	$P_1 \rightarrow 1$ $P_2 \rightarrow 1.5$
1	2->1	PI	
2	3 →1.5	P2	P3 → 2
3	1	External Fragmentation	P4 -> 2.5
un- Allocated	2 GB	////	

process

2. Next fit! In next fit it will take process after next allocation memory.

Paptition	Memory	Process
1	2 -> 1	PI
2	3 71.5	P2
3	1	External f.
un- Allocated	2	1///

P2 ->1.5 P3 >2 P4 -> 2.5

3. Best Fit;

where the memory sized is available it will put the process. where minimum woste process

partition	Memons	process
1	2→1	PZ
2	3-31.5	P3
3	1 -> 0-5	Pı
M- Allocated	2	1//////

PI -> 0.5 P2->1 P3 -> 1.5 P4 -> 2

4. Worst Pit!

where the memory is in the most high size, then here the process will completed. process

Partition	Memony (913)	Process
1	2005	P3
2	3+2.5+1.5	P1, P2
3	1	P4
Un- Allocatted	2	////

P1 → 0.5 P2 -> 1 P3 ->1.5 pq -> 1

so, As this way the Pynamic pantitioning are works.

#### Arms: to the Ques not 2 (b)

In peal-life scenarios, the performance of computing devices can vary due to how efficiently memory management techniques are implemented.

# 1. Insufficient physical Memory (RAM):

when the devices RAM is insufficient to handle all active processes, it leads to throsting, where the system spends more time swapping data between RAM and the disk nather than executing tosus.

pasing: Divides memony into fixed-size pages and maps them to frames in physical memony.

## page peplacement Algorithms!

- · LRU! Replace the page that has not been used for the largest time.
- · FiFOI Replaces the oldest page. Simple but may lead to suboptimal penformance.

#### 2. Fragmentation

frogmentation occurs when memory is poorly managed, leading to either!

- · Internal Fragmentation
- · External Fragmentation

Dynamic partitioning: Allocales memony bosed on process
size, but it can lead to external fragmentation.

#### Algorithm:

Best Fit: Allocates the smallest suitable block. Reduces internal fragmentation but increases external fragmentation.

3. Poonly optimized vintual memony usage!

vintual memory allows a system to use disk

space as an extention of RAM. Excessive reliance
on vintual memory slows down the system.

Demand paging; Loads poses into memony only when they are required overvse of virtual memony can lead to page faults.

page that will not be used for the longest time in the future

# 4. Deadlock or Resource storvation !-

process may enter a state of Deadlock if they one waiting for memory or other resources indefinitely.

Devolock Avoidance; Allocates resources such that cincullar wait is avoided.

Bankers Algorithm! predicts whether a resource allocation will leave the system in a safe state. prevents deadlock but may delay processes.

### Ans: to the aves not 3(a)

To analyze the page peplacement sequence for the siven page perence string using LRU and optimal algorithms, we need to simulate the behaviour of each algorithm step by step.

Simulation for LRU Algorithm 1-

	-	•		,		•	E	,		2	4	5	6	2	1	3	Z	ヌ	0	9
ροσο F <sub>1</sub>	1	2	3	4	1	1	5	1	5	3	3	3	6	6	6	3	3	3	<b>@</b> 3	9
Fo	•	9	1	2	4	4	)	9	1	1	4	4	4	2	2	2	7	ヌ	7	Z
F2		_	2	3	3	9	•	2	9	2	2	5	5	5	ı	ı	١	ı	0	0
	F	F	F	F	F	F	F	Н	Н	F	F	F	F	F	۴	F	F	Н	F	F

### · Total pose faults (LRU): 17

### simulation for optimal Algorithm;

					+															
pose	١	2	3	4	)	2	5	ı	2	3	4	5	6	2	1	3	X	X	0	9
F1	1	1	1	1	1	١	١	١	1	3	4	4	6	6	1	3	ヌ	7	0	9
F2												2								
F3			3	4	4	4	5	5	5	5	5	5	5	5	5	5	5	5	5	5 F
-	F	F	P	F	H	Н	F	H	H	F	F	H	F	14	F	F	F	$\mathcal{H}$	F	F

### . Total page Faults (optimal): 13

The optimal algorithm performs better than LRU because it has prior knowledge of future references, allowing it to minimize page faults effectively.

Ans.

### Ans: to the over nol 3(b)

Here are the general strategies to enhance both LRU and optimal Algorithm!

- 1. Increase physical memony! Adding more physical memony neduces the need for frequent page neplacements, benefiting both algorithms.
- 2. Reduce wonking set size! optimize applications to minimize their wonking set, neducing page replacement frequency.
- 3. Use Multi-Level caches; Implement multi-level caching to hardle most memony negvests without frequent page faults.
- 4. optimize vintual memony management;
  - · Adjust the page size to match typical wonkload putterns.
  - . use prefetching to load pages into memory in anticipation of tuture requests.
- 5. Monitor and Adopt workload Behaviour!
  use sistem monitoring tools to deket and adapt
  to changes in workload patenns dynamically.

## Trade-offs and practical constraints 1-

### 1. complexity vs. performance:

- · optimal enhancements, such as predictive algorithms, increase computational complexity but can significantly improve performance
- · LRU approximations like clock neduce complexity but may occasionally make suboptimal peplocements.

# 2. Handware vs. software cost!

- · Handware ossisted LRU implementations require specialized handware, increasing costs.
- · Software-only solutions, while cheapen, are computations

By implementing these strategies, the efficiency of both LRV and optimal algorithms can be improved, addressing their limitations while balancing performance and resource usage.