

Bomb Lab (Done)

```
1
2 bomb:      file format elf64-x86-64
3
4
5 Disassembly of section .init:
6
7 0000000000400ac0 <_init>:
8   400ac0: 48 83 ec 08          sub    $0x8,%rsp
9   400ac4: e8 f3 01 00 00      callq 400cbc <call_gmon_start>
10  400ac9: 48 83 c4 08          add    $0x8,%rsp
11  400acd: c3                  retq
12
13 Disassembly of section .text:
14
15 0000000000400c90 <_start>:
16  400c90: 31 ed              xor    %ebp,%ebp
17  400c92: 49 89 d1          mov    %rdx,%r9
18  400c95: 5e              pop    %rsi
19  400c96: 48 89 e2          mov    %rsp,%rdx
20  400c99: 48 83 e4 f0      and    $0xffffffffffffffff,%rsp
21  400c9d: 50              push   %rax
22  400c9e: 54              push   %rsp
23  400c9f: 49 c7 c0 a0 22 40 00 mov    $0x4022a0,%r8
24  400ca6: 48 c7 c1 10 22 40 00 mov    $0x402210,%rcx
25  400cad: 48 c7 c7 a0 0d 40 00 mov    $0x400da0,%rdi
26  400cb4: e8 b7 fe ff ff      callq 400b70 <__libc_start_main@plt>
27  400cb9: f4              hlt
28  400cba: 90              nop
29  400cbb: 90              nop
30
31 0000000000400cbc <call_gmon_start>:
32  400cbc: 48 83 ec 08          sub    $0x8,%rsp
33  400cc0: 48 8b 05 19 23 20 00 mov    0x202319(%rip),%rax      # 602fe0 <__gmon_start__>
34  400cc7: 48 85 c0          test   %rax,%rax
35  400cca: 74 02          je     400cce <call_gmon_start+0x12>
36  400ccc: ff d0          callq  *%rax
37  400cce: 48 83 c4 08          add    $0x8,%rsp
38  400cd2: c3              retq
39  400cd3: 90              nop
40  400cd4: 90              nop
41  400cd5: 90              nop
42  400cd6: 90              nop
43  400cd7: 90              nop
44  400cd8: 90              nop
45  400cd9: 90              nop
46  400cda: 90              nop
47  400cdb: 90              nop
48  400cdc: 90              nop
49  400cdd: 90              nop
50  400cde: 90              nop
51  400cdf: 90              nop
52
53 0000000000400ce0 <deregister_tm_clones>:
54  400ce0: b8 47 37 60 00      mov    $0x603747,%eax
55  400ce5: 55              push   %rbp
56  400ce6: 48 2d 40 37 60 00      sub    $0x603740,%rax
57  400cec: 48 83 f8 0e          cmp    $0xe,%rax
58  400cf0: 48 89 e5          mov    %rsp,%rbp
59  400cf3: 77 02          ja     400cf7 <deregister_tm_clones+0x17>
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60 400cf5: 5d                pop    %rbp
61 400cf6: c3                retq
62 400cf7: b8 00 00 00 00    mov     $0x0,%eax
63 400cfc: 48 85 c0           test    %rax,%rax
64 400cff: 74 f4             je      400cf5 <deregister_tm_clones+0x15>
65 400d01: 5d                pop     %rbp
66 400d02: bf 40 37 60 00    mov     $0x603740,%edi
67 400d07: ff e0             jmpq    *%rax
68 400d09: 0f 1f 80 00 00 00 nopl    0x0(%rax)
69
70 0000000000400d10 <register_tm_clones>:
71 400d10: b8 40 37 60 00    mov     $0x603740,%eax
72 400d15: 55                push    %rbp
73 400d16: 48 2d 40 37 60 00 sub     $0x603740,%rax
74 400d1c: 48 c1 f8 03       sar     $0x3,%rax
75 400d20: 48 89 e5           mov     %rsp,%rbp
76 400d23: 48 89 c2           mov     %rax,%rdx
77 400d26: 48 c1 ea 3f       shr     $0x3f,%rdx
78 400d2a: 48 01 d0           add     %rdx,%rax
79 400d2d: 48 d1 f8           sar     %rax
80 400d30: 75 02             jne     400d34 <register_tm_clones+0x24>
81 400d32: 5d                pop     %rbp
82 400d33: c3                retq
83 400d34: ba 00 00 00 00    mov     $0x0,%edx
84 400d39: 48 85 d2           test    %rdx,%rdx
85 400d3c: 74 f4             je      400d32 <register_tm_clones+0x22>
86 400d3e: 5d                pop     %rbp
87 400d3f: 48 89 c6           mov     %rax,%rsi
88 400d42: bf 40 37 60 00    mov     $0x603740,%edi
89 400d47: ff e2             jmpq    *%rdx
90 400d49: 0f 1f 80 00 00 00 nopl    0x0(%rax)
91
92 0000000000400d50 <__do_global_dtors_aux>:
93 400d50: 80 3d 01 2a 20 00 cmpb     $0x0,0x202a01(%rip)        # 603758 <completed.6976>
94 400d57: 75 11             jne     400d6a <__do_global_dtors_aux+0x1a>
95 400d59: 55                push    %rbp
96 400d5a: 48 89 e5           mov     %rsp,%rbp
97 400d5d: e8 7e ff ff ff    callq   400ce0 <deregister_tm_clones>
98 400d62: 5d                pop     %rbp
99 400d63: c6 05 ee 29 20 00 movb     $0x1,0x2029ee(%rip)        # 603758 <completed.6976>
100 400d6a: f3 c3             repz retq
101 400d6c: 0f 1f 40 00       nopl    0x0(%rax)
102
103 0000000000400d70 <frame_dummy>:
104 400d70: 48 83 3d 90 20 00 cmpq     $0x0,0x202090(%rip)        # 602e08 <__JCR_END__>
105 400d77: 00
106 400d78: 74 1e             je      400d98 <frame_dummy+0x28>
107 400d7a: b8 00 00 00 00    mov     $0x0,%eax
108 400d7f: 48 85 c0           test    %rax,%rax
109 400d82: 74 14             je      400d98 <frame_dummy+0x28>
110 400d84: 55                push    %rbp
111 400d85: bf 08 2e 60 00    mov     $0x602e08,%edi
112 400d8a: 48 89 e5           mov     %rsp,%rbp
113 400d8d: ff d0             callq   *%rax
114 400d8f: 5d                pop     %rbp
115 400d90: e9 7b ff ff ff    jmpq    400d10 <register_tm_clones>
116 400d95: 0f 1f 00          nopl    (%rax)
117 400d98: e9 73 ff ff ff    jmpq    400d10 <register_tm_clones>
118 400d9d: 90                nop
119 400d9e: 90                nop
120 400d9f: 90                nop
121
122 0000000000400da0 <main>:

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123 400da0: 53          push    %rbx
124 400da1: 83 ff 01    cmp     $0x1,%edi
125 400da4: 75 10       jne     400db6 <main+0x16>
126 400da6: 48 8b 05 9b 29 20 00 mov     0x20299b(%rip),%rax    # 603748
    <stdin@@GLIBC_2.2.5>
127 400dad: 48 89 05 b4 29 20 00 mov     %rax,0x2029b4(%rip)    # 603768 <infile>
128 400db4: eb 63       jmp     400e19 <main+0x79>
129 400db6: 48 89 f3    mov     %rsi,%rbx
130 400db9: 83 ff 02    cmp     $0x2,%edi
131 400dbc: 75 3a       jne     400df8 <main+0x58>
132 400dbe: 48 8b 7e 08 mov     0x8(%rsi),%rdi
133 400dc2: be b4 22 40 00 mov     $0x4022b4,%esi
134 400dc7: e8 44 fe ff ff callq   400c10 <fopen@plt>
135 400dcc: 48 89 05 95 29 20 00 mov     %rax,0x202995(%rip)    # 603768 <infile>
136 400dd3: 48 85 c0    test    %rax,%rax
137 400dd6: 75 41       jne     400e19 <main+0x79>
138 400dd8: 48 8b 4b 08 mov     0x8(%rbx),%rcx
139 400ddc: 48 8b 13    mov     (%rbx),%rdx
140 400ddf: be b6 22 40 00 mov     $0x4022b6,%esi
141 400de4: bf 01 00 00 00 mov     $0x1,%edi
142 400de9: e8 12 fe ff ff callq   400c00 <__printf_chk@plt>
143 400dee: bf 08 00 00 00 mov     $0x8,%edi
144 400df3: e8 28 fe ff ff callq   400c20 <exit@plt>
145 400df8: 48 8b 16    mov     (%rsi),%rdx
146 400dfb: be d3 22 40 00 mov     $0x4022d3,%esi
147 400e00: bf 01 00 00 00 mov     $0x1,%edi
148 400e05: b8 00 00 00 00 mov     $0x0,%eax
149 400e0a: e8 f1 fd ff ff callq   400c00 <__printf_chk@plt>
150 400e0f: bf 08 00 00 00 mov     $0x8,%edi
151 400e14: e8 07 fe ff ff callq   400c20 <exit@plt>
152 400e19: e8 84 05 00 00 callq   4013a2 <initialize_bomb>
153 400e1e: bf 38 23 40 00 mov     $0x402338,%edi
154 400e23: e8 e8 fc ff ff callq   400b10 <puts@plt>
155 400e28: bf 78 23 40 00 mov     $0x402378,%edi
156 400e2d: e8 de fc ff ff callq   400b10 <puts@plt>
157 400e32: e8 67 06 00 00 callq   40149e <read_line>
158 400e37: 48 89 c7    mov     %rax,%rdi
159 400e3a: e8 a1 00 00 00 callq   400ee0 <phase_1>
160 400e3f: e8 80 07 00 00 callq   4015c4 <phase_defused>
161 400e44: bf a8 23 40 00 mov     $0x4023a8,%edi
162 400e49: e8 c2 fc ff ff callq   400b10 <puts@plt>
163 400e4e: e8 4b 06 00 00 callq   40149e <read_line>
164 400e53: 48 89 c7    mov     %rax,%rdi
165 400e56: e8 a1 00 00 00 callq   400efc <phase_2>
166 400e5b: e8 64 07 00 00 callq   4015c4 <phase_defused>
167 400e60: bf ed 22 40 00 mov     $0x4022ed,%edi
168 400e65: e8 a6 fc ff ff callq   400b10 <puts@plt>
169 400e6a: e8 2f 06 00 00 callq   40149e <read_line>
170 400e6f: 48 89 c7    mov     %rax,%rdi
171 400e72: e8 cc 00 00 00 callq   400f43 <phase_3>
172 400e77: e8 48 07 00 00 callq   4015c4 <phase_defused>
173 400e7c: bf 0b 23 40 00 mov     $0x40230b,%edi
174 400e81: e8 8a fc ff ff callq   400b10 <puts@plt>
175 400e86: e8 13 06 00 00 callq   40149e <read_line>
176 400e8b: 48 89 c7    mov     %rax,%rdi
177 400e8e: e8 79 01 00 00 callq   40100c <phase_4>
178 400e93: e8 2c 07 00 00 callq   4015c4 <phase_defused>
179 400e98: bf d8 23 40 00 mov     $0x4023d8,%edi
180 400e9d: e8 6e fc ff ff callq   400b10 <puts@plt>
181 400ea2: e8 f7 05 00 00 callq   40149e <read_line>
182 400ea7: 48 89 c7    mov     %rax,%rdi
183 400eaa: e8 b3 01 00 00 callq   401062 <phase_5>
184 400eaf: e8 10 07 00 00 callq   4015c4 <phase_defused>

```

跳过 phase-1

直接打印 "Phase 1 defused..."

1 2 4 8 16 32

```

185 400eb4: bf 1a 23 40 00    mov     $0x40231a,%edi
186 400eb9: e8 52 fc ff ff    callq  400b10 <puts@plt>
187 400ebe: e8 db 05 00 00    callq  40149e <read_line>
188 400ec3: 48 89 c7          mov     %rax,%rdi
189 400ec6: e8 29 02 00 00    callq  4010f4 <phase_6>
190 400ecb: e8 f4 06 00 00    callq  4015c4 <phase_defused>
191 400ed0: b8 00 00 00 00    mov     $0x0,%eax
192 400ed5: 5b              pop     %rbx
193 400ed6: c3              retq
194 400ed7: 90              nop
195 400ed8: 90              nop
196 400ed9: 90              nop
197 400eda: 90              nop
198 400edb: 90              nop
199 400edc: 90              nop
200 400edd: 90              nop
201 400ede: 90              nop
202 400edf: 90              nop

```

```

204 00000000 400ee0 <phase_1>:
205 400ee0: 48 83 ec 08      sub     $0x8,%rsp
206 400ee4: be 00 24 40 00    mov     $0x402400,%esi
207 400ee9: e8 4a 04 00 00    callq  401338 <strings_not_equal>
208 400eee: 85 c0            test    %eax,%eax
209 400ef0: 74 05            je      400ef7 <phase_1+0x17>
210 400ef2: e8 43 05 00 00    callq  40143a <explode_bomb>
211 400ef7: 48 83 c4 08      add     $0x8,%rsp
212 400efb: c3              retq

```

```

214 00000000 400efc <phase_2>:
215 400efc: 55              push    %rbp
216 400efd: 53              push    %rbx
217 400efe: 48 83 ec 28      sub     $0x28,%rsp
218 400f02: 48 89 e6          mov     %rsp,%rsi
219 400f05: e8 52 05 00 00    callq  40145c <read_six_numbers>
220 400f0a: 83 3c 24 01      cmpl    $0x1,(%rsp)
221 400f0e: 74 20            je      400f30 <phase_2+0x34>
222 400f10: e8 25 05 00 00    callq  40143a <explode_bomb>
223 400f15: eb 19            jmp     400f30 <phase_2+0x34>

```

```

224 400f17: 8b 43 fc          mov     -0x4(%rbx),%eax
225 400f1a: 01 c0            add     %eax,%eax
226 400f1c: 39 03            cmp     %eax,(%rbx)
227 400f1e: 74 05            je      400f25 <phase_2+0x29>
228 400f20: e8 15 05 00 00    callq  40143a <explode_bomb>
229 400f25: 48 83 c3 04      add     $0x4,%rbx
230 400f29: 48 39 eb          cmp     %rbp,%rbx
231 400f2c: 75 e9            jne     400f17 <phase_2+0x1b>
232 400f2e: eb 0c            jmp     400f3c <phase_2+0x40>
233 400f30: 48 8d 5c 24 04    lea     0x4(%rsp),%rbx
234 400f35: 48 8d 6c 24 18    lea     0x18(%rsp),%rbp
235 400f3a: eb db            jmp     400f17 <phase_2+0x1b>
236 400f3c: 48 83 c4 28      add     $0x28,%rsp
237 400f40: 5b              pop     %rbx
238 400f41: 5d              pop     %rbp
239 400f42: c3              retq

```

```

241 00000000 400f43 <phase_3>:
242 400f43: 48 83 ec 18      sub     $0x18,%rsp
243 400f47: 48 8d 4c 24 0c    lea     0xc(%rsp),%rcx
244 400f4c: 48 8d 54 24 08    lea     0x8(%rsp),%rdx
245 400f51: be cf 25 40 00    mov     $0x4025cf,%esi
246 400f56: b8 00 00 00 00    mov     $0x0,%eax
247 400f5b: e8 90 fc ff ff    callq  400bf0 <__isoc99_sscanf@plt>

```

4203520
line 559

L662

*%rbx = 004f4 2x
*%rbx*4 = 004f8 3x
for +4 fc 4x
+4 500 5x
+4 %rdx 6x

B

X2

✓

✓

两参(a, b)

返回值为1 (说明必多于1个数)
一共2参输入

```
248 400f60: 83 f8 01      cmp     $0x1,%eax
249 400f63: 7f 05         jg      400f6a <phase_3+0x27>
250 400f65: e8 d0 04 00 00 callq   40143a <explode_bomb>
251 400f6a: 83 7c 24 08 07 cmpl    $0x7,0x8(%rsp)
252 400f6f: 77 3c         ja      400fad <phase_3+0x6a>
253 400f71: 8b 44 24 08   mov     0x8(%rsp),%eax
254 400f75: ff 24 c5 70 24 40 00 jmpq    *0x402470(,%rax,8)
255 400f7c: b8 cf 00 00 00 mov     $0xcf,%eax
256 400f81: eb 3b         jmp     400f8e <phase_3+0x7b>
257 400f83: b8 c3 02 00 00 mov     $0xc3,%eax
258 400f88: eb 34         jmp     400f8e <phase_3+0x7b>
259 400f8a: b8 00 01 00 00 mov     $0x100,%eax
260 400f8f: eb 2d         jmp     400f8e <phase_3+0x7b>
261 400f91: b8 85 01 00 00 mov     $0x185,%eax
262 400f96: eb 26         jmp     400f8e <phase_3+0x7b>
263 400f98: b8 ce 00 00 00 mov     $0xce,%eax
264 400f9d: eb 1f         jmp     400f8e <phase_3+0x7b>
265 400f9f: b8 aa 02 00 00 mov     $0x2aa,%eax
266 400fa4: eb 18         jmp     400f8e <phase_3+0x7b>
267 400fa6: b8 47 01 00 00 mov     $0x147,%eax
268 400fab: eb 11         jmp     400f8e <phase_3+0x7b>
269 400fad: e8 88 04 00 00 callq   40143a <explode_bomb>
270 400fb2: b8 00 00 00 00 mov     $0x0,%eax
271 400fb7: eb 05         jmp     400f8e <phase_3+0x7b>
272 400fb9: b8 37 01 00 00 mov     $0x137,%eax
273 400fbe: 3b 44 24 0c   cmp     0xc(%rsp),%eax
274 400fc2: 74 05         je      400fc9 <phase_3+0x86>
275 400fc4: e8 71 04 00 00 callq   40143a <explode_bomb>
276 400fc9: 48 83 c4 18   add     $0x18,%rsp
277 400fcd: c3           retq
278
279 0000000000400fce <func4>:
280 400fce: 48 83 ec 08   sub     $0x8,%rsp
281 400fd2: 89 d0         mov     %edx,%eax
282 400fd4: 29 f0         sub     %esi,%eax
283 400fd6: 89 c1         mov     %eax,%ecx
284 400fd8: c1 e9 1f     shr     $0x1f,%ecx
285 400fdb: 01 c8         add     %ecx,%eax
286 400fdd: d1 f8         sar     %eax
287 400fdf: 8d 0c 30     lea     (%rax,%rsi,1),%ecx
288 400fe2: 39 f9         cmp     %edi,%ecx
289 400fe4: 7e 0c         jle     400ff2 <func4+0x24>
290 400fe6: 8d 51 ff     lea     -0x1(%rcx),%edx
291 400fe9: e8 e0 ff ff ff callq   400fce <func4>
292 400fee: 01 c0         add     %eax,%eax
293 400ff0: eb 15         jmp     401007 <func4+0x39>
294 400ff2: b8 00 00 00 00 mov     $0x0,%eax
295 400ff7: 39 f9         cmp     %edi,%ecx
296 400ff9: 7d 0c         jge     401007 <func4+0x39>
297 400ffb: 8d 71 01     lea     0x1(%rcx),%esi
298 400ffe: e8 cb ff ff ff callq   400fce <func4>
299 401003: 8d 44 00 01   lea     0x1(%rax,%rax,1),%eax
300 401007: 48 83 c4 08   add     $0x8,%rsp
301 40100b: c3           retq
302
303 000000000040100c <phase_4>:
304 40100c: 48 83 ec 18   sub     $0x18,%rsp
305 401010: 48 8d 4c 24 0c lea     0xc(%rsp),%rcx
306 401015: 48 8d 54 24 08 lea     0x8(%rsp),%rdx
307 40101a: be cf 25 40 00 mov     $0x4025cf,%esi
308 40101f: b8 00 00 00 00 mov     $0x0,%eax
309 401024: e8 c7 fb ff ff callq   400bf0 <__isoc99_sscanf@plt>
310 401029: 83 f8 02     cmp     $0x2,%eax
```

a < 7
above 无符号大于
像switch语句

$\%eax == * (\%rsp + 0xc) = b$

func4 (rdi, rsi)

ecx <= edi 跳转

ecx >= edi 跳转

→ 输入参数2个

311	40102c: 75 07	jne 401035 <phase_4+0x29> X	
312	40102e: 83 7c 24 08 0e	cmpl \$0xe,0x8(%rsp)	$*(\%rsp+8) \geq \$0xe$
313	401033: 76 05	jbe 40103a <phase_4+0x2e> ✓	
314	401035: e8 00 04 00 00	callq 40143a <explode_bomb> X	
315	40103a: ba 0e 00 00 00	mov \$0xe,%edx	
316	40103f: be 00 00 00 00	mov \$0x0,%esi	
317	401044: 8b 7c 24 08	mov 0x8(%rsp),%edi	
318	401048: e8 81 ff ff ff	callq 400fce <func4>	
B 319	40104d: 85 c0	test %eax,%eax	$\%eax = 0$ 需使1参为0
320	40104f: 75 07	jne 401058 <phase_4+0x4c> X	
321	401051: 83 7c 24 0c 00	cmpl \$0x0,0xc(%rsp)	需使第2参也为0
322	401056: 74 05	je 40105d <phase_4+0x51>	
323	401058: e8 dd 03 00 00	callq 40143a <explode_bomb> X	
324	40105d: 48 83 c4 18	add \$0x18,%rsp	
325	401061: c3	retq	
326			
327	000000000401062 <phase_5>:		
328	401062: 53	push %rbx	
329	401063: 48 83 ec 20	sub \$0x20,%rsp	
330	401067: 48 89 fb	mov %rdi,%rbx	
331	40106a: 64 48 8b 04 25 28 00	mov %fs:0x28,%rax	
332	401071: 00 00		
333	401073: 48 89 44 24 18	mov %rax,0x18(%rsp)	
334	401078: 31 c0	xor %eax,%eax	
B 335	40107a: e8 9c 02 00 00	callq 40131b <string_length>	
336	40107f: 83 f8 06	cmp \$0x6,%eax	$\%eax = \$0x6 \rightarrow$ 字符串共6个字符
337	401082: 74 4e	je 4010d2 <phase_5+0x70> ✓	
338	401084: e8 b1 03 00 00	callq 40143a <explode_bomb> X	
339	401089: eb 47	jmp 4010d2 <phase_5+0x70>	
340	40108b: 0f b6 0c 03	movzbl (%rbx,%rax,1),%ecx	
341	40108f: 88 0c 24	mov %cl,(%rsp)	
342	401092: 48 8b 14 24	mov (%rsp),%rdx	
343	401096: 83 e2 0f	and \$0xf,%edx	
344	401099: 0f b6 92 b0 24 40 00	movzbl 0x4024b0(%rdx),%edx	
345	4010a0: 88 54 04 10	mov %dl,0x10(%rsp,%rax,1)	
346	4010a4: 48 83 c0 01	add \$0x1,%rax	
347	4010a8: 48 83 f8 06	cmp \$0x6,%rax	计数器
348	4010ac: 75 dd	jne 40108b <phase_5+0x29>	
349	4010ae: c6 44 24 16 00	movb \$0x0,0x16(%rsp)	
350	4010b3: be 5e 24 40 00	mov \$0x40245e,%esi	
351	4010b8: 48 8d 7c 24 10	lea 0x10(%rsp),%rdi	
352	4010bd: e8 76 02 00 00	callq 401338 <strings_not_equal>	
353	4010c2: 85 c0	test %eax,%eax	$\%eax == 0$
354	4010c4: 74 13	je 4010d9 <phase_5+0x77> ✓	
355	4010c6: e8 6f 03 00 00	callq 40143a <explode_bomb> X	
356	4010cb: 0f 1f 44 00 00	nopl 0x0(%rax,%rax,1)	
357	4010d0: eb 07	jmp 4010d9 <phase_5+0x77>	
358	4010d2: b8 00 00 00 00	mov \$0x0,%eax	
359	4010d7: eb b2	jmp 40108b <phase_5+0x29>	
360	4010d9: 48 8b 44 24 18	mov 0x18(%rsp),%rax	
361	4010de: 64 48 33 04 25 28 00	xor %fs:0x28,%rax	
362	4010e5: 00 00		
363	4010e7: 74 05	je 4010ee <phase_5+0x8c>	
364	4010e9: e8 42 fa ff ff	callq 400b30 <__stack_chk_fail@plt>	
365	4010ee: 48 83 c4 20	add \$0x20,%rsp	
366	4010f2: 5b	pop %rbx	
367	4010f3: c3	retq	
368			
369	0000000004010f4 <phase_6>:		
370	4010f4: 41 56	push %r14	
371	4010f6: 41 55	push %r13	
372	4010f8: 41 54	push %r12	
373	4010fa: 55	push %rbp	

```

374 4010fb: 53          push    %rbx
375 4010fc: 48 83 ec 50  sub    $0x50,%rsp
376 401100: 49 89 e5      mov     %rsp,%r13
377 401103: 48 89 e6      mov     %rsp,%rsi
378 401106: e8 51 03 00 00 callq   40145c <read_six_numbers> →
379 40110b: 49 89 e6      mov     %rsp,%r14
380 40110e: 41 bc 00 00 00 00 mov     $0x0,%r12d
381 401114: 4c 89 ed      mov     %r13,%rbp
382 401117: 41 8b 45 00    mov     0x0(%r13),%eax
383 40111b: 83 e8 01      sub     $0x1,%eax
384 40111e: 83 f8 05      cmp     $0x5,%eax 第1步 ≥ 6
385 401121: 76 05         jbe     401128 <phase_6+0x34> ✓
386 401123: e8 12 03 00 00 callq   40143a <explode_bomb> X
387 401128: 41 83 c4 01    add     $0x1,%r12d
388 40112c: 41 83 fc 06    cmp     $0x6,%r12d  %r12d = 6
389 401130: 74 21         je      401153 <phase_6+0x5f>
390 401132: 44 89 e3      mov     %r12d,%ebx
391 401135: 48 63 c3      movslq  %ebx,%rax ←
392 401138: 8b 04 84      mov     (%rsp,%rax,4),%eax 2步
393 40113b: 39 45 00      cmp     %eax,0x0(%rbp) 1步
394 40113e: 75 05         jne     401145 <phase_6+0x51> ✓ 第2步 ≠ 第1步
395 401140: e8 f5 02 00 00 callq   40143a <explode_bomb> X
396 401145: 83 c3 01      add     $0x1,%ebx
397 401148: 83 fb 05      cmp     $0x5,%ebx
398 40114b: 7e e8         jle     401135 <phase_6+0x41> → %ebx ≤ 5 跳转
399 40114d: 49 83 c5 04    add     $0x4,%r13
400 401151: eb c1         jmp     401114 <phase_6+0x20>
401 401153: 48 8d 74 24 18 lea     0x18(%rsp),%rsi
402 401158: 4c 89 f0      mov     %r14,%rax
403 40115b: b9 07 00 00 00 mov     $0x7,%ecx
404 401160: 89 ca         mov     %ecx,%edx
405 401162: 2b 10         sub     (%rax),%edx
406 401164: 89 10         mov     %edx,(%rax)
407 401166: 48 83 c0 04    add     $0x4,%rax
408 40116a: 48 39 f0      cmp     %rsi,%rax
409 40116d: 75 f1         jne     401160 <phase_6+0x6c>
410 40116f: be 00 00 00 00 mov     $0x0,%esi → 和7的偏差值 |x-7|
411 401174: eb 21         jmp     401197 <phase_6+0xa3>
412 401176: 48 8b 52 08    mov     0x8(%rdx),%rdx ←
413 40117a: 83 c0 01      add     $0x1,%eax
414 40117d: 39 c8         cmp     %ecx,%eax
415 40117f: 75 f5         jne     401176 <phase_6+0x82>
416 401181: eb 05         jmp     401188 <phase_6+0x94>
417 401183: ba d0 32 60 00 mov     $0x6032d0,%edx ← 0x32d095
418 401188: 48 89 54 74 20 mov     %rdx,0x20(%rsp,%rsi,2): 计数器
419 40118d: 48 83 c6 04    add     $0x4,%rsi 6次
420 401191: 48 83 fe 18    cmp     $0x18,%rsi
421 401195: 74 14         je      4011ab <phase_6+0xb7>
422 401197: 8b 0c 34      mov     (%rsp,%rsi,1),%ecx
423 40119a: 83 f9 01      cmp     $0x1,%ecx
424 40119d: 7e e4         jle     401183 <phase_6+0x8f>
425 40119f: b8 01 00 00 00 mov     $0x1,%eax
426 4011a4: ba d0 32 60 00 mov     $0x6032d0,%edx
427 4011a9: eb cb         jmp     401176 <phase_6+0x82>
428 4011ab: 48 8b 5c 24 20 mov     0x20(%rsp),%rbx ←
429 4011b0: 48 8d 44 24 28 lea     0x28(%rsp),%rax
430 4011b5: 48 8d 74 24 50 lea     0x50(%rsp),%rsi
431 4011ba: 48 89 d9      mov     %rbx,%rcx
432 4011bd: 48 8b 10      mov     (%rax),%rdx
433 4011c0: 48 89 51 08    mov     %rdx,0x8(%rcx)
434 4011c4: 48 83 c0 08    add     $0x8,%rax 计数器
435 4011c8: 48 39 f0      cmp     %rsi,%rax
436 4011cb: 74 05         je      4011d2 <phase_6+0xde>

```

6	5	ee3c0
4	3	ee3b8
2	1	ee3b0

排序算法
Sorting Algs

```

437 4011cd: 48 89 d1      mov    %rdx,%rcx
438 4011d0: eb eb        jmp    4011bd <phase_6+0xc9>
439 4011d2: 48 c7 42 08 00 00 00 movq    $0x0,0x8(%rdx)
440 4011d9: 00
441 4011da: bd 05 00 00 00 mov    $0x5,%ebp
442 4011df: 48 8b 43 08    mov    0x8(%rbx),%rax
443 4011e3: 8b 00        mov    (%rax),%eax
444 4011e5: 39 03        cmp    %eax,(%rbx)
445 4011e7: 7d 05        jge    4011ee <phase_6+0xfa>
446 4011e9: e8 4c 02 00 00 callq  40143a <explode_bomb>
447 4011ee: 48 8b 5b 08    mov    0x8(%rbx),%rbx
448 4011f2: 83 ed 01      sub    $0x1,%ebp 计数器
449 4011f5: 75 e8        jne    4011df <phase_6+0xeb>
450 4011f7: 48 83 c4 50    add    $0x50,%rsp
451 4011fb: 5b          pop    %rbx
452 4011fc: 5d          pop    %rbp
453 4011fd: 41 5c        pop    %r12
454 4011ff: 41 5d        pop    %r13
455 401201: 41 5e        pop    %r14
456 401203: c3          retq

```

```

457
458 000000000401204 <fun7>:
459 401204: 48 83 ec 08    sub    $0x8,%rsp
460 401208: 48 85 ff      test   %rdi,%rdi
461 40120b: 74 2b        je     401238 <fun7+0x34>
462 40120d: 8b 17        mov    (%rdi),%edx
463 40120f: 39 f2        cmp    %esi,%edx
464 401211: 7e 0d        jle    401220 <fun7+0x1c>
465 401213: 48 8b 7f 08    mov    0x8(%rdi),%rdi
466 401217: e8 e8 ff ff ff callq  401204 <fun7>
467 40121c: 01 c0        add    %eax,%eax
468 40121e: eb 1d        jmp    40123d <fun7+0x39>
469 401220: b8 00 00 00 00 mov    $0x0,%eax
470 401225: 39 f2        cmp    %esi,%edx
471 401227: 74 14        je     40123d <fun7+0x39>
472 401229: 48 8b 7f 10    mov    0x10(%rdi),%rdi
473 40122d: e8 d2 ff ff ff callq  401204 <fun7>
474 401232: 8d 44 00 01    lea    0x1(%rax,%rax,1),%eax
475 401236: eb 05        jmp    40123d <fun7+0x39>
476 401238: b8 ff ff ff ff mov    $0xffffffff,%eax
477 40123d: 48 83 c4 08    add    $0x8,%rsp
478 401241: c3          retq
479

```

返回值eax必为2.

rdi != 0

%edx > %esi 输入

%edx != %esi

```

480 000000000401242 <secret_phase>:
481 401242: 53          push   %rbx
482 401243: e8 56 02 00 00 callq  40149e <read_line>
483 401248: ba 0a 00 00 00 mov    $0xa,%edx 10进制
484 40124d: be 00 00 00 00 mov    $0x0,%esi
485 401252: 48 89 c7     mov    %rax,%rdi
486 401255: e8 76 f9 ff ff callq  400bd0 <strtol@plt>
487 40125a: 48 89 c3     mov    %rax,%rbx
488 40125d: 8d 40 ff     lea    -0x1(%rax),%eax
489 401260: 3d e8 03 00 00 cmp    $0x3e8,%eax
490 401265: 76 05        jbe    40126c <secret_phase+0x2a>
491 401267: e8 ce 01 00 00 callq  40143a <explode_bomb>
492 40126c: 89 de        mov    %ebx,%esi
493 40126e: bf f0 30 60 00 mov    $0x6030f0,%edi
494 401273: e8 8c ff ff ff callq  401204 <fun7>
495 401278: 83 f8 02     cmp    $0x2,%eax
496 40127b: 74 05        je     401282 <secret_phase+0x40>
497 40127d: e8 b8 01 00 00 callq  40143a <explode_bomb>
498 401282: bf 38 24 40 00 mov    $0x402438,%edi
499 401287: e8 84 f8 ff ff callq  400b10 <puts@plt>

```

strtol(char *nptr, char **endptr, int base)

%eax < 0x3e8 / 1000

%rax = 2


```

500 40128c: e8 33 03 00 00    callq 4015c4 <phase_defused>
501 401291: 5b                pop    %rbx
502 401292: c3                retq
503 401293: 90                nop
504 401294: 90                nop
505 401295: 90                nop
506 401296: 90                nop
507 401297: 90                nop
508 401298: 90                nop
509 401299: 90                nop
510 40129a: 90                nop
511 40129b: 90                nop
512 40129c: 90                nop
513 40129d: 90                nop
514 40129e: 90                nop
515 40129f: 90                nop
516
517 00000000004012a0 <sig_handler>:
518 4012a0: 48 83 ec 08      sub    $0x8,%rsp
519 4012a4: bf c0 24 40 00   mov    $0x4024c0,%edi
520 4012a9: e8 62 f8 ff ff   callq 400b10 <puts@plt>
521 4012ae: bf 03 00 00 00   mov    $0x3,%edi
522 4012b3: e8 98 f9 ff ff   callq 400c50 <sleep@plt>
523 4012b8: be 82 25 40 00   mov    $0x402582,%esi
524 4012bd: bf 01 00 00 00   mov    $0x1,%edi
525 4012c2: b8 00 00 00 00   mov    $0x0,%eax
526 4012c7: e8 34 f9 ff ff   callq 400c00 <__printf_chk@plt>
527 4012cc: 48 8b 3d 6d 24 20 00 mov    0x20246d(%rip),%rdi    # 603740 <__bss_start>
528 4012d3: e8 08 f9 ff ff   callq 400be0 <fflush@plt>
529 4012d8: bf 01 00 00 00   mov    $0x1,%edi
530 4012dd: e8 6e f9 ff ff   callq 400c50 <sleep@plt>
531 4012e2: bf 8a 25 40 00   mov    $0x40258a,%edi
532 4012e7: e8 24 f8 ff ff   callq 400b10 <puts@plt>
533 4012ec: bf 10 00 00 00   mov    $0x10,%edi
534 4012f1: e8 2a f9 ff ff   callq 400c20 <exit@plt>
535
536 00000000004012f6 <invalid_phase>:
537 4012f6: 48 83 ec 08      sub    $0x8,%rsp
538 4012fa: 48 89 fa          mov    %rdi,%rdx
539 4012fd: be 92 25 40 00   mov    $0x402592,%esi
540 401302: bf 01 00 00 00   mov    $0x1,%edi
541 401307: b8 00 00 00 00   mov    $0x0,%eax
542 40130c: e8 ef f8 ff ff   callq 400c00 <__printf_chk@plt>
543 401311: bf 08 00 00 00   mov    $0x8,%edi
544 401316: e8 05 f9 ff ff   callq 400c20 <exit@plt>
545
546 000000000040131b <string_length>:
547 40131b: 80 3f 00          cmpb   $0x0,(%rdi)
548 40131e: 74 12             je     401332 <string_length+0x17>
549 401320: 48 89 fa          mov    %rdi,%rdx
550 401323: 48 83 c2 01       add    $0x1,%rdx
551 401327: 89 d0             mov    %edx,%eax
552 401329: 29 f8             sub    %edi,%eax
553 40132b: 80 3a 00          cmpb   $0x0,(%rdx)
554 40132e: 75 f3             jne    401323 <string_length+0x8>
555 401330: f3 c3             repz retq
556 401332: b8 00 00 00 00   mov    $0x0,%eax
557 401337: c3                retq
558
559 0000000000401338 <strings_not_equal>:
560 401338: 41 54             push   %r12
561 40133a: 55               push   %rbp
562 40133b: 53               push   %rbx

```

// 如果 %rdi 值为 0, %eax 进 0

```

563 40133c: 48 89 fb      mov    %rdi,%rbx
564 40133f: 48 89 f5      mov    %rsi,%rbp (ans)
565 401342: e8 d4 ff ff ff callq  40131b <string_length> line 546
566 401347: 41 89 c4      mov    %eax,%r12d // %r12d = len(mystring)
567 40134a: 48 89 ef      mov    %rbp,%rdi
568 40134d: e8 c9 ff ff ff callq  40131b <string_length> L546
569 401352: ba 01 00 00 00 mov    $0x1,%edx // %edx = $0x1
570 401357: 41 39 c4      cmp    %eax,%r12d // if (len(mystring) == ans)
571 40135a: 75 3f      jne    40139b <strings_not_equal+0x63> // 52个字符
572 40135c: 0f b6 03      movzbl (%rbx),%eax
573 40135f: 84 c0      test   %al,%al
574 401361: 74 25      je     401388 <strings_not_equal+0x50> ✓ al = 0
575 401363: 3a 45 00      cmp    0x0(%rbp),%al
576 401366: 74 0a      je     401372 <strings_not_equal+0x3a> ✓
577 401368: eb 25      jmp    40138f <strings_not_equal+0x57> X
578 40136a: 3a 45 00      cmp    0x0(%rbp),%al
579 40136d: 0f 1f 00      nopl   (%rax)
580 401370: 75 24      jne    401396 <strings_not_equal+0x5e>
581 401372: 48 83 c3 01   add    $0x1,%rbx
582 401376: 48 83 c5 01   add    $0x1,%rbp
583 40137a: 0f b6 03      movzbl (%rbx),%eax
584 40137d: 84 c0      test   %al,%al
585 40137f: 75 e9      jne    40136a <strings_not_equal+0x32>
586 401381: ba 00 00 00 00 mov    $0x0,%edx // return 0 ✓
587 401386: eb 13      jmp    40139b <strings_not_equal+0x63>
588 401388: ba 00 00 00 00 mov    $0x0,%edx // return 0 ✓
589 40138d: eb 0c      jmp    40139b <strings_not_equal+0x63>
590 40138f: ba 01 00 00 00 mov    $0x1,%edx // return 1 X
591 401394: eb 05      jmp    40139b <strings_not_equal+0x63>
592 401396: ba 01 00 00 00 mov    $0x1,%edx
593 40139b: 89 d0      mov    %edx,%eax // return 1 X (字符串长度不等)
594 40139d: 5b      pop    %rbx
595 40139e: 5d      pop    %rbp
596 40139f: 41 5c      pop    %r12
597 4013a1: c3      retq
598
599 0000000004013a2 <initialize_bomb>:
600 4013a2: 48 83 ec 08      sub    $0x8,%rsp
601 4013a6: be a0 12 40 00   mov    $0x4012a0,%esi
602 4013ab: bf 02 00 00 00   mov    $0x2,%edi
603 4013b0: e8 db f7 ff ff   callq  400b90 <signal@plt>
604 4013b5: 48 83 c4 08      add    $0x8,%rsp
605 4013b9: c3      retq
606
607 0000000004013ba <initialize_bomb_solve>:
608 4013ba: f3 c3      repz retq
609
610 0000000004013bc <blank_line>:
611 4013bc: 55      push   %rbp
612 4013bd: 53      push   %rbx
613 4013be: 48 83 ec 08   sub    $0x8,%rsp
614 4013c2: 48 89 fb      mov    %rdi,%rbx
615 4013c5: eb 17      jmp    4013de <blank_line+0x22>
616 4013c7: e8 94 f8 ff ff callq  400c60 <__ctype_b_loc@plt>
617 4013cc: 48 83 c3 01   add    $0x1,%rbx
618 4013d0: 48 0f be ed   movsbq %bpl,%rbp
619 4013d4: 48 8b 00      mov    (%rax),%rax
620 4013d7: f6 44 68 01 20 testb  $0x20,0x1(%rax,%rbp,2)
621 4013dc: 74 0f      je     4013ed <blank_line+0x31>
622 4013de: 0f b6 2b      movzbl (%rbx),%ebp
623 4013e1: 40 84 ed      test   %bpl,%bpl
624 4013e4: 75 e1      jne    4013c7 <blank_line+0xb>
625 4013e6: b8 01 00 00 00 mov    $0x1,%eax

```

```

626 4013eb: eb 05 jmp 4013f2 <blank_line+0x36>
627 4013ed: b8 00 00 00 00 mov $0x0,%eax
628 4013f2: 48 83 c4 08 add $0x8,%rsp
629 4013f6: 5b pop %rbx
630 4013f7: 5d pop %rbp
631 4013f8: c3 retq
632
633 00000000004013f9 <skip>:
634 4013f9: 53 push %rbx
635 4013fa: 48 63 05 5f 23 20 00 movslq 0x20235f(%rip),%rax # 603760 <num_input_strings>
636 401401: 48 8d 3c 80 lea (%rax,%rax,4),%rdi
637 401405: 48 c1 e7 04 shl $0x4,%rdi
638 401409: 48 81 c7 80 37 60 00 add $0x603780,%rdi
639 401410: 48 8b 15 51 23 20 00 mov 0x202351(%rip),%rdx # 603768 <infile>
640 401417: be 50 00 00 00 mov $0x50,%esi
641 40141c: e8 5f f7 ff ff callq 400b80 <fgets@plt>
642 401421: 48 89 c3 mov %rax,%rbx
643 401424: 48 85 c0 test %rax,%rax
644 401427: 74 0c je 401435 <skip+0x3c>
645 401429: 48 89 c7 mov %rax,%rdi
646 40142c: e8 8b ff ff ff callq 4013bc <blank_line>
647 401431: 85 c0 test %eax,%eax
648 401433: 75 c5 jne 4013fa <skip+0x1>
649 401435: 48 89 d8 mov %rbx,%rax
650 401438: 5b pop %rbx
651 401439: c3 retq
652
653 000000000040143a <explode_bomb>:
654 40143a: 48 83 ec 08 sub $0x8,%rsp
655 40143e: bf a3 25 40 00 mov $0x4025a3,%edi
656 401443: e8 c8 f6 ff ff callq 400b10 <puts@plt>
657 401448: bf ac 25 40 00 mov $0x4025ac,%edi
658 40144d: e8 be f6 ff ff callq 400b10 <puts@plt>
659 401452: bf 08 00 00 00 mov $0x8,%edi
660 401457: e8 c4 f7 ff ff callq 400c20 <exit@plt>
661
662 000000000040145c <read_six_numbers>:
663 40145c: 48 83 ec 18 sub $0x18,%rsp
664 401460: 48 89 f2 mov %rsi,%rdx
665 401463: 48 8d 4e 04 lea 0x4(%rsi),%rcx
666 401467: 48 8d 46 14 lea 0x14(%rsi),%rax
667 40146b: 48 89 44 24 08 mov %rax,0x8(%rsp)
668 401470: 48 8d 46 10 lea 0x10(%rsi),%rax
669 401474: 48 89 04 24 mov %rax,(%rsp)
670 401478: 4c 8d 4e 0c lea 0xc(%rsi),%r9
671 40147c: 4c 8d 46 08 lea 0x8(%rsi),%r8
672 401480: be c3 25 40 00 mov $0x4025c3,%esi
673 401485: b8 00 00 00 00 mov $0x0,%eax
674 40148a: e8 61 f7 ff ff callq 400bf0 <__isoc99_sscanf@plt>
675 40148f: 83 f8 05 cmp $0x5,%eax
676 401492: 7f 05 jg 401499 <read_six_numbers+0x3d>
677 401494: e8 a1 ff ff ff callq 40143a <explode_bomb>
678 401499: 48 83 c4 18 add $0x18,%rsp
679 40149d: c3 retq
680
681 000000000040149e <read_line>:
682 40149e: 48 83 ec 08 sub $0x8,%rsp
683 4014a2: b8 00 00 00 00 mov $0x0,%eax
684 4014a7: e8 4d ff ff ff callq 4013f9 <skip>
685 4014ac: 48 85 c0 test %rax,%rax
686 4014af: 75 6e jne 40151f <read_line+0x81>
687 4014b1: 48 8b 05 90 22 20 00 mov 0x202290(%rip),%rax # 603748

```

<stdin@@GLIBC_2.2.5>

最后一个数大于5

✓
X

```

688 4014b8: 48 39 05 a9 22 20 00 cmp %rax,0x2022a9(%rip) # 603768 <infile>
689 4014bf: 75 14 jne 4014d5 <read_line+0x37>
690 4014c1: bf d5 25 40 00 mov $0x4025d5,%edi
691 4014c6: e8 45 f6 ff ff callq 400b10 <puts@plt>
692 4014cb: bf 08 00 00 00 mov $0x8,%edi
693 4014d0: e8 4b f7 ff ff callq 400c20 <exit@plt>
694 4014d5: bf f3 25 40 00 mov $0x4025f3,%edi
695 4014da: e8 01 f6 ff ff callq 400ae0 <getenv@plt>
696 4014df: 48 85 c0 test %rax,%rax
697 4014e2: 74 0a je 4014ee <read_line+0x50>
698 4014e4: bf 00 00 00 00 mov $0x0,%edi
699 4014e9: e8 32 f7 ff ff callq 400c20 <exit@plt>
700 4014ee: 48 8b 05 53 22 20 00 mov 0x202253(%rip),%rax # 603748
<stdin@@GLIBC_2.2.5>
701 4014f5: 48 89 05 6c 22 20 00 mov %rax,0x20226c(%rip) # 603768 <infile>
702 4014fc: b8 00 00 00 00 mov $0x0,%eax
703 401501: e8 f3 fe ff ff callq 4013f9 <skip>
704 401506: 48 85 c0 test %rax,%rax
705 401509: 75 14 jne 40151f <read_line+0x81>
706 40150b: bf d5 25 40 00 mov $0x4025d5,%edi
707 401510: e8 fb f5 ff ff callq 400b10 <puts@plt>
708 401515: bf 00 00 00 00 mov $0x0,%edi
709 40151a: e8 01 f7 ff ff callq 400c20 <exit@plt>
710 40151f: 8b 15 3b 22 20 00 mov 0x20223b(%rip),%edx # 603760 <num_input_strings>
711 401525: 48 63 c2 movslq %edx,%rax
712 401528: 48 8d 34 80 lea (%rax,%rax,4),%rsi
713 40152c: 48 c1 e6 04 shl $0x4,%rsi
714 401530: 48 81 c6 80 37 60 00 add $0x603780,%rsi
715 401537: 48 89 f7 mov %rsi,%rdi
716 40153a: b8 00 00 00 00 mov $0x0,%eax
717 40153f: 48 c7 c1 ff ff ff ff mov $0xffffffffffffffff,%rcx
718 401546: f2 ae repnz scas %es:(%rdi),%al
719 401548: 48 f7 d1 not %rcx
720 40154b: 48 83 e9 01 sub $0x1,%rcx
721 40154f: 83 f9 4e cmp $0x4e,%ecx
722 401552: 7e 46 jle 40159a <read_line+0xfc>
723 401554: bf fe 25 40 00 mov $0x4025fe,%edi
724 401559: e8 b2 f5 ff ff callq 400b10 <puts@plt>
725 40155e: 8b 05 fc 21 20 00 mov 0x2021fc(%rip),%eax # 603760 <num_input_strings>
726 401564: 8d 50 01 lea 0x1(%rax),%edx
727 401567: 89 15 f3 21 20 00 mov %edx,0x2021f3(%rip) # 603760 <num_input_strings>
728 40156d: 48 98 cltq
729 40156f: 48 6b c0 50 imul $0x50,%rax,%rax
730 401573: 48 bf 2a 2a 2a 74 72 movabs $0x636e7572742a2a2a,%rdi
731 40157a: 75 6e 63
732 40157d: 48 89 b8 80 37 60 00 mov %rdi,0x603780(%rax)
733 401584: 48 bf 61 74 65 64 2a movabs $0x2a2a2a64657461,%rdi
734 40158b: 2a 2a 00
735 40158e: 48 89 b8 88 37 60 00 mov %rdi,0x603788(%rax)
736 401595: e8 a0 fe ff ff callq 40143a <explode_bomb>
737 40159a: 83 e9 01 sub $0x1,%ecx
738 40159d: 48 63 c9 movslq %ecx,%rcx
739 4015a0: 48 63 c2 movslq %edx,%rax
740 4015a3: 48 8d 04 80 lea (%rax,%rax,4),%rax
741 4015a7: 48 c1 e0 04 shl $0x4,%rax
742 4015ab: c6 84 01 80 37 60 00 movb $0x0,0x603780(%rcx,%rax,1)
743 4015b2: 00
744 4015b3: 83 c2 01 add $0x1,%edx
745 4015b6: 89 15 a4 21 20 00 mov %edx,0x2021a4(%rip) # 603760 <num_input_strings>
746 4015bc: 48 89 f0 mov %rsi,%rax
747 4015bf: 48 83 c4 08 add $0x8,%rsp
748 4015c3: c3 retq
749

```

```

750 0000000004015c4 <phase_defused>:
751 4015c4: 48 83 ec 78      sub    $0x78,%rsp
752 4015c8: 64 48 8b 04 25 28 00 mov    %fs:0x28,%rax
753 4015cf: 00 00
754 4015d1: 48 89 44 24 68    mov    %rax,0x68(%rsp)
755 4015d6: 31 c0            xor    %eax,%eax
756 4015d8: 83 3d 81 21 20 00 06 cmpl    $0x6,0x202181(%rip)      # 603760 <num_input_strings>
757 4015df: 75 5e            jne    40163f <phase_defused+0x7b>
758 4015e1: 4c 8d 44 24 10    lea    0x10(%rsp),%r8
759 4015e6: 48 8d 4c 24 0c    lea    0xc(%rsp),%rcx
760 4015eb: 48 8d 54 24 08    lea    0x8(%rsp),%rdx
761 4015f0: be 19 26 40 00    mov    $0x402619,%esi
762 4015f5: bf 70 38 60 00    mov    $0x603870,%edi
763 4015fa: e8 f1 f5 ff ff    callq 400bf0 <__isoc99_sscanf@plt>
764 4015ff: 83 f8 03         cmp    $0x3,%eax
765 401602: 75 31            jne    401635 <phase_defused+0x71> X
766 401604: be 22 26 40 00    mov    $0x402622,%esi
767 401609: 48 8d 7c 24 10    lea    0x10(%rsp),%rdi
768 40160e: e8 25 fd ff ff    callq 401338 <strings_not_equal> X
769 401613: 85 c0            test   %eax,%eax
770 401615: 75 1e            jne    401635 <phase_defused+0x71> X
771 401617: bf f8 24 40 00    mov    $0x4024f8,%edi
772 40161c: e8 ef f4 ff ff    callq 400b10 <puts@plt>
773 401621: bf 20 25 40 00    mov    $0x402520,%edi
774 401626: e8 e5 f4 ff ff    callq 400b10 <puts@plt>
775 40162b: b8 00 00 00 00    mov    $0x0,%eax
776 401630: e8 0d fc ff ff    callq 401242 <secret_phase>
777 401635: bf 58 25 40 00    mov    $0x402558,%edi
778 40163a: e8 d1 f4 ff ff    callq 400b10 <puts@plt>
779 40163f: 48 8b 44 24 68    mov    0x68(%rsp),%rax
780 401644: 64 48 33 04 25 28 00 xor    %fs:0x28,%rax
781 40164b: 00 00
782 40164d: 74 05            je     401654 <phase_defused+0x90>
783 40164f: e8 dc f4 ff ff    callq 400b30 <__stack_chk_fail@plt>
784 401654: 48 83 c4 78      add    $0x78,%rsp
785 401658: c3              retq
786 401659: 90              nop
787 40165a: 90              nop
788 40165b: 90              nop
789 40165c: 90              nop
790 40165d: 90              nop
791 40165e: 90              nop
792 40165f: 90              nop
793
794 000000000401660 <sigalrm_handler>:
795 401660: 48 83 ec 08      sub    $0x8,%rsp
796 401664: b9 00 00 00 00    mov    $0x0,%ecx
797 401669: ba 78 26 40 00    mov    $0x402678,%edx
798 40166e: be 01 00 00 00    mov    $0x1,%esi
799 401673: 48 8b 3d d6 20 20 00 mov    0x2020d6(%rip),%rdi      # 603750
<stderr@@GLIBC_2.2.5>
800 40167a: b8 00 00 00 00    mov    $0x0,%eax
801 40167f: e8 bc f5 ff ff    callq 400c40 <__fprintf_chk@plt>
802 401684: bf 01 00 00 00    mov    $0x1,%edi
803 401689: e8 92 f5 ff ff    callq 400c20 <exit@plt>
804
805 00000000040168e <rio_readlineb>:
806 40168e: 41 57            push   %r15
807 401690: 41 56            push   %r14
808 401692: 41 55            push   %r13
809 401694: 41 54            push   %r12
810 401696: 55              push   %rbp
811 401697: 53              push   %rbx

```

Handwritten notes in the image:

- Next to line 761: `%d %d %s`
- Next to line 766: `"Dr Evil"`
- Next to line 776: `<secret_phase>` (circled)

812	401698: 48 83 ec 38	sub	\$0x38,%rsp
813	40169c: 49 89 f6	mov	%rsi,%r14
814	40169f: 48 89 54 24 18	mov	%rdx,0x18(%rsp)
815	4016a4: 48 83 fa 01	cmp	\$0x1,%rdx
816	4016a8: 0f 86 c9 00 00 00	jbe	401777 <rio_readlineb+0xe9>
817	4016ae: 48 89 fb	mov	%rdi,%rbx
818	4016b1: 41 bd 01 00 00 00	mov	\$0x1,%r13d
819	4016b7: 4c 8d 67 10	lea	0x10(%rdi),%r12
820	4016bb: eb 30	jmp	4016ed <rio_readlineb+0x5f>
821	4016bd: ba 00 20 00 00	mov	\$0x2000,%edx
822	4016c2: 4c 89 e6	mov	%r12,%rsi
823	4016c5: 8b 3b	mov	(%rbx),%edi
824	4016c7: e8 94 f4 ff ff	callq	400b60 <read@plt>
825	4016cc: 89 43 04	mov	%eax,0x4(%rbx)
826	4016cf: 85 c0	test	%eax,%eax
827	4016d1: 79 12	jns	4016e5 <rio_readlineb+0x57>
828	4016d3: e8 18 f4 ff ff	callq	400af0 <__errno_location@plt>
829	4016d8: 83 38 04	cmpl	\$0x4,(%rax)
830	4016db: 74 10	je	4016ed <rio_readlineb+0x5f>
831	4016dd: 0f 1f 00	nopl	(%rax)
832	4016e0: e9 a1 00 00 00	jmpq	401786 <rio_readlineb+0xf8>
833	4016e5: 85 c0	test	%eax,%eax
834	4016e7: 74 71	je	40175a <rio_readlineb+0xcc>
835	4016e9: 4c 89 63 08	mov	%r12,0x8(%rbx)
836	4016ed: 8b 6b 04	mov	0x4(%rbx),%ebp
837	4016f0: 85 ed	test	%ebp,%ebp
838	4016f2: 7e c9	jle	4016bd <rio_readlineb+0x2f>
839	4016f4: 85 ed	test	%ebp,%ebp
840	4016f6: 41 0f 95 c7	setne	%r15b
841	4016fa: 41 0f b6 c7	movzbl	%r15b,%eax
842	4016fe: 89 44 24 0c	mov	%eax,0xc(%rsp)
843	401702: 45 0f b6 ff	movzbl	%r15b,%r15d
844	401706: 48 8b 4b 08	mov	0x8(%rbx),%rcx
845	40170a: 48 89 ce	mov	%rcx,%rsi
846	40170d: b9 01 00 00 00	mov	\$0x1,%ecx
847	401712: 4c 89 fa	mov	%r15,%rdx
848	401715: 48 89 74 24 10	mov	%rsi,0x10(%rsp)
849	40171a: 48 8d 7c 24 2f	lea	0x2f(%rsp),%rdi
850	40171f: e8 9c f4 ff ff	callq	400bc0 <__memcpy_chk@plt>
851	401724: 4c 03 7c 24 10	add	0x10(%rsp),%r15
852	401729: 4c 89 7b 08	mov	%r15,0x8(%rbx)
853	40172d: 8b 44 24 0c	mov	0xc(%rsp),%eax
854	401731: 29 c5	sub	%eax,%ebp
855	401733: 89 6b 04	mov	%ebp,0x4(%rbx)
856	401736: 83 f8 01	cmp	\$0x1,%eax
857	401739: 75 13	jne	40174e <rio_readlineb+0xc0>
858	40173b: 49 83 c6 01	add	\$0x1,%r14
859	40173f: 0f b6 44 24 2f	movzbl	0x2f(%rsp),%eax
860	401744: 41 88 46 ff	mov	%al,-0x1(%r14)
861	401748: 3c 0a	cmp	\$0xa,%al
862	40174a: 75 18	jne	401764 <rio_readlineb+0xd6>
863	40174c: eb 2f	jmp	40177d <rio_readlineb+0xef>
864	40174e: 83 7c 24 0c 00	cmpl	\$0x0,0xc(%rsp)
865	401753: 75 3a	jne	40178f <rio_readlineb+0x101>
866	401755: 44 89 e8	mov	%r13d,%eax
867	401758: eb 03	jmp	40175d <rio_readlineb+0xcf>
868	40175a: 44 89 e8	mov	%r13d,%eax
869	40175d: 83 f8 01	cmp	\$0x1,%eax
870	401760: 75 1b	jne	40177d <rio_readlineb+0xef>
871	401762: eb 34	jmp	401798 <rio_readlineb+0x10a>
872	401764: 41 83 c5 01	add	\$0x1,%r13d
873	401768: 49 63 c5	movslq	%r13d,%rax
874	40176b: 48 3b 44 24 18	cmp	0x18(%rsp),%rax

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875 401770: 73 0b jae 40177d <rio_readlineb+0xef>
876 401772: e9 76 ff ff ff jmpq 4016ed <rio_readlineb+0x5f>
877 401777: 41 bd 01 00 00 00 mov $0x1,%r13d
878 40177d: 41 c6 06 00 movb $0x0,(%r14)
879 401781: 49 63 c5 movslq %r13d,%rax
880 401784: eb 17 jmp 40179d <rio_readlineb+0x10f>
881 401786: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax
882 40178d: eb 0e jmp 40179d <rio_readlineb+0x10f>
883 40178f: 48 c7 c0 ff ff ff ff mov $0xffffffffffffffff,%rax
884 401796: eb 05 jmp 40179d <rio_readlineb+0x10f>
885 401798: b8 00 00 00 00 mov $0x0,%eax
886 40179d: 48 83 c4 38 add $0x38,%rsp
887 4017a1: 5b pop %rbx
888 4017a2: 5d pop %rbp
889 4017a3: 41 5c pop %r12
890 4017a5: 41 5d pop %r13
891 4017a7: 41 5e pop %r14
892 4017a9: 41 5f pop %r15
893 4017ab: c3 retq
894
895 00000000004017ac <submitr>:
896 4017ac: 41 57 push %r15
897 4017ae: 41 56 push %r14
898 4017b0: 41 55 push %r13
899 4017b2: 41 54 push %r12
900 4017b4: 55 push %rbp
901 4017b5: 53 push %rbx
902 4017b6: 48 81 ec 68 a0 00 00 sub $0xa068,%rsp
903 4017bd: 48 89 fd mov %rdi,%rbp
904 4017c0: 41 89 f5 mov %esi,%r13d
905 4017c3: 48 89 54 24 10 mov %rdx,0x10(%rsp)
906 4017c8: 48 89 4c 24 18 mov %rcx,0x18(%rsp)
907 4017cd: 4d 89 c7 mov %r8,%r15
908 4017d0: 4c 89 cb mov %r9,%rbx
909 4017d3: 4c 8b b4 24 a0 a0 00 mov 0xa0a0(%rsp),%r14
910 4017da: 00
911 4017db: 64 48 8b 04 25 28 00 mov %fs:0x28,%rax
912 4017e2: 00 00
913 4017e4: 48 89 84 24 58 a0 00 mov %rax,0xa058(%rsp)
914 4017eb: 00
915 4017ec: 31 c0 xor %eax,%eax
916 4017ee: c7 44 24 2c 00 00 00 movl $0x0,0x2c(%rsp)
917 4017f5: 00
918 4017f6: ba 00 00 00 00 mov $0x0,%edx
919 4017fb: be 01 00 00 00 mov $0x1,%esi
920 401800: bf 02 00 00 00 mov $0x2,%edi
921 401805: e8 76 f4 ff ff callq 400c80 <socket@plt>
922 40180a: 41 89 c4 mov %eax,%r12d
923 40180d: 85 c0 test %eax,%eax
924 40180f: 79 50 jns 401861 <submitr+0xb5>
925 401811: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
926 401818: 3a 20 43
927 40181b: 49 89 06 mov %rax,(%r14)
928 40181e: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
929 401825: 20 75 6e
930 401828: 49 89 46 08 mov %rax,0x8(%r14)
931 40182c: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
932 401833: 74 6f 20
933 401836: 49 89 46 10 mov %rax,0x10(%r14)
934 40183a: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax
935 401841: 65 20 73
936 401844: 49 89 46 18 mov %rax,0x18(%r14)
937 401848: 41 c7 46 20 6f 63 6b movl $0x656b636f,0x20(%r14)

```

```

938 40184f: 65
939 401850: 66 41 c7 46 24 74 00 movw $0x74,0x24(%r14)
940 401857: b8 ff ff ff ff mov $0xffffffff,%eax
941 40185c: e9 07 06 00 00 jmpq 401e68 <submitr+0x6bc>
942 401861: 48 89 ef mov %rbp,%rdi
943 401864: e8 37 f3 ff ff callq 400ba0 <gethostbyname@plt>
944 401869: 48 85 c0 test %rax,%rax
945 40186c: 75 6b jne 4018d9 <submitr+0x12d>
946 40186e: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax
947 401875: 3a 20 44
948 401878: 49 89 06 mov %rax,(%r14)
949 40187b: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax
950 401882: 20 75 6e
951 401885: 49 89 46 08 mov %rax,0x8(%r14)
952 401889: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
953 401890: 74 6f 20
954 401893: 49 89 46 10 mov %rax,0x10(%r14)
955 401897: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax
956 40189e: 76 65 20
957 4018a1: 49 89 46 18 mov %rax,0x18(%r14)
958 4018a5: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax
959 4018ac: 72 20 61
960 4018af: 49 89 46 20 mov %rax,0x20(%r14)
961 4018b3: 41 c7 46 28 64 64 72 movl $0x65726464,0x28(%r14)
962 4018ba: 65
963 4018bb: 66 41 c7 46 2c 73 73 movw $0x7373,0x2c(%r14)
964 4018c2: 41 c6 46 2e 00 movb $0x0,0x2e(%r14)
965 4018c7: 44 89 e7 mov %r12d,%edi
966 4018ca: e8 81 f2 ff ff callq 400b50 <close@plt>
967 4018cf: b8 ff ff ff ff mov $0xffffffff,%eax
968 4018d4: e9 8f 05 00 00 jmpq 401e68 <submitr+0x6bc>
969 4018d9: 48 c7 44 24 30 00 00 movq $0x0,0x30(%rsp)
970 4018e0: 00 00
971 4018e2: 48 c7 44 24 38 00 00 movq $0x0,0x38(%rsp)
972 4018e9: 00 00
973 4018eb: 66 c7 44 24 30 02 00 movw $0x2,0x30(%rsp)
974 4018f2: 48 63 50 14 movslq 0x14(%rax),%rdx
975 4018f6: 48 8b 40 18 mov 0x18(%rax),%rax
976 4018fa: 48 8d 7c 24 34 lea 0x34(%rsp),%rdi
977 4018ff: b9 0c 00 00 00 mov $0xc,%ecx
978 401904: 48 8b 30 mov (%rax),%rsi
979 401907: e8 a4 f2 ff ff callq 400bb0 <__memmove_chk@plt>
980 40190c: 66 41 c1 cd 08 ror $0x8,%r13w
981 401911: 66 44 89 6c 24 32 mov %r13w,0x32(%rsp)
982 401917: ba 10 00 00 00 mov $0x10,%edx
983 40191c: 48 8d 74 24 30 lea 0x30(%rsp),%rsi
984 401921: 44 89 e7 mov %r12d,%edi
985 401924: e8 07 f3 ff ff callq 400c30 <connect@plt>
986 401929: 85 c0 test %eax,%eax
987 40192b: 79 5d jns 40198a <submitr+0x1de>
988 40192d: 48 b8 45 72 72 6f 72 movabs $0x55203a726f727245,%rax
989 401934: 3a 20 55
990 401937: 49 89 06 mov %rax,(%r14)
991 40193a: 48 b8 6e 61 62 6c 65 movabs $0x6f7420656c62616e,%rax
992 401941: 20 74 6f
993 401944: 49 89 46 08 mov %rax,0x8(%r14)
994 401948: 48 b8 20 63 6f 6e 6e movabs $0x7463656e6f6320,%rax
995 40194f: 65 63 74
996 401952: 49 89 46 10 mov %rax,0x10(%r14)
997 401956: 48 b8 20 74 6f 20 74 movabs $0x20656874206f7420,%rax
998 40195d: 68 65 20
999 401960: 49 89 46 18 mov %rax,0x18(%r14)
1000 401964: 41 c7 46 20 73 65 72 movl $0x76726573,0x20(%r14)

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1001 40196b: 76
1002 40196c: 66 41 c7 46 24 65 72 movw $0x7265,0x24(%r14)
1003 401973: 41 c6 46 26 00 movb $0x0,0x26(%r14)
1004 401978: 44 89 e7 mov %r12d,%edi
1005 40197b: e8 d0 f1 ff ff callq 400b50 <close@plt>
1006 401980: b8 ff ff ff ff mov $0xffffffff,%eax
1007 401985: e9 de 04 00 00 jmpq 401e68 <submitr+0x6bc>
1008 40198a: 48 c7 c2 ff ff ff ff mov $0xffffffffffffffff,%rdx
1009 401991: 48 89 df mov %rbx,%rdi
1010 401994: b8 00 00 00 00 mov $0x0,%eax
1011 401999: 48 89 d1 mov %rdx,%rcx
1012 40199c: f2 ae repnz scas %es:(%rdi),%al
1013 40199e: 48 f7 d1 not %rcx
1014 4019a1: 48 89 ce mov %rcx,%rsi
1015 4019a4: 48 8b 7c 24 10 mov 0x10(%rsp),%rdi
1016 4019a9: 48 89 d1 mov %rdx,%rcx
1017 4019ac: f2 ae repnz scas %es:(%rdi),%al
1018 4019ae: 49 89 c8 mov %rcx,%r8
1019 4019b1: 48 8b 7c 24 18 mov 0x18(%rsp),%rdi
1020 4019b6: 48 89 d1 mov %rdx,%rcx
1021 4019b9: f2 ae repnz scas %es:(%rdi),%al
1022 4019bb: 48 f7 d1 not %rcx
1023 4019be: 49 89 c9 mov %rcx,%r9
1024 4019c1: 4c 89 ff mov %r15,%rdi
1025 4019c4: 48 89 d1 mov %rdx,%rcx
1026 4019c7: f2 ae repnz scas %es:(%rdi),%al
1027 4019c9: 4d 29 c1 sub %r8,%r9
1028 4019cc: 49 29 c9 sub %rcx,%r9
1029 4019cf: 48 8d 44 76 fd lea -0x3(%rsi,%rsi,2),%rax
1030 4019d4: 49 8d 44 01 7b lea 0x7b(%r9,%rax,1),%rax
1031 4019d9: 48 3d 00 20 00 00 cmp $0x2000,%rax
1032 4019df: 76 73 jbe 401a54 <submitr+0x2a8>
1033 4019e1: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax
1034 4019e8: 3a 20 52
1035 4019eb: 49 89 06 mov %rax,(%r14)
1036 4019ee: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax
1037 4019f5: 20 73 74
1038 4019f8: 49 89 46 08 mov %rax,0x8(%r14)
1039 4019fc: 48 b8 72 69 6e 67 20 movabs $0x6f6f7420676e6972,%rax
1040 401a03: 74 6f 6f
1041 401a06: 49 89 46 10 mov %rax,0x10(%r14)
1042 401a0a: 48 b8 20 6c 61 72 67 movabs $0x202e656772616c20,%rax
1043 401a11: 65 2e 20
1044 401a14: 49 89 46 18 mov %rax,0x18(%r14)
1045 401a18: 48 b8 49 6e 63 72 65 movabs $0x6573616572636e49,%rax
1046 401a1f: 61 73 65
1047 401a22: 49 89 46 20 mov %rax,0x20(%r14)
1048 401a26: 48 b8 20 53 55 42 4d movabs $0x5254494d42555320,%rax
1049 401a2d: 49 54 52
1050 401a30: 49 89 46 28 mov %rax,0x28(%r14)
1051 401a34: 48 b8 5f 4d 41 58 42 movabs $0x46554258414d5f,%rax
1052 401a3b: 55 46 00
1053 401a3e: 49 89 46 30 mov %rax,0x30(%r14)
1054 401a42: 44 89 e7 mov %r12d,%edi
1055 401a45: e8 06 f1 ff ff callq 400b50 <close@plt>
1056 401a4a: b8 ff ff ff ff mov $0xffffffff,%eax
1057 401a4f: e9 14 04 00 00 jmpq 401e68 <submitr+0x6bc>
1058 401a54: 48 8d 94 24 40 20 00 lea 0x2040(%rsp),%rdx
1059 401a5b: 00
1060 401a5c: b9 00 04 00 00 mov $0x400,%ecx
1061 401a61: b8 00 00 00 00 mov $0x0,%eax
1062 401a66: 48 89 d7 mov %rdx,%rdi
1063 401a69: f3 48 ab rep stos %rax,%es:(%rdi)

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```

1064 401a6c: 48 89 df      mov    %rbx,%rdi
1065 401a6f: 48 c7 c1 ff ff ff ff mov    $0xffffffffffffffff,%rcx
1066 401a76: f2 ae        repnz scas %es:(%rdi),%al
1067 401a78: 48 f7 d1      not    %rcx
1068 401a7b: 48 83 e9 01    sub    $0x1,%rcx
1069 401a7f: 85 c9        test   %ecx,%ecx
1070 401a81: 0f 84 fd 03 00 00 je     401e84 <submitr+0x6d8>
1071 401a87: 83 e9 01      sub    $0x1,%ecx
1072 401a8a: 4c 8d 6c 0b 01 lea     0x1(%rbx,%rcx,1),%r13
1073 401a8f: 48 89 d5      mov    %rdx,%rbp
1074 401a92: 44 0f b6 03    movzbl (%rbx),%r8d
1075 401a96: 41 80 f8 2a    cmp    $0x2a,%r8b
1076 401a9a: 74 23        je     401abf <submitr+0x313>
1077 401a9c: 41 8d 40 d3    lea     -0x2d(%r8),%eax
1078 401aa0: 3c 01        cmp    $0x1,%al
1079 401aa2: 76 1b        jbe    401abf <submitr+0x313>
1080 401aa4: 41 80 f8 5f    cmp    $0x5f,%r8b
1081 401aa8: 74 15        je     401abf <submitr+0x313>
1082 401aaa: 41 8d 40 d0    lea     -0x30(%r8),%eax
1083 401aae: 3c 09        cmp    $0x9,%al
1084 401ab0: 76 0d        jbe    401abf <submitr+0x313>
1085 401ab2: 44 89 c0      mov    %r8d,%eax
1086 401ab5: 83 e0 df      and    $0xffffffff,%eax
1087 401ab8: 83 e8 41      sub    $0x41,%eax
1088 401abb: 3c 19        cmp    $0x19,%al
1089 401abd: 77 0a        ja     401ac9 <submitr+0x31d>
1090 401abf: 48 8d 45 01    lea     0x1(%rbp),%rax
1091 401ac3: 44 88 45 00    mov    %r8b,0x0(%rbp)
1092 401ac7: eb 6c        jmp     401b35 <submitr+0x389>
1093 401ac9: 41 80 f8 20    cmp    $0x20,%r8b
1094 401acd: 75 0a        jne    401ad9 <submitr+0x32d>
1095 401acf: 48 8d 45 01    lea     0x1(%rbp),%rax
1096 401ad3: c6 45 00 2b    movb    $0x2b,0x0(%rbp)
1097 401ad7: eb 5c        jmp     401b35 <submitr+0x389>
1098 401ad9: 41 8d 40 e0    lea     -0x20(%r8),%eax
1099 401add: 3c 5f        cmp    $0x5f,%al
1100 401adf: 76 0a        jbe    401aeb <submitr+0x33f>
1101 401ae1: 41 80 f8 09    cmp    $0x9,%r8b
1102 401ae5: 0f 85 02 04 00 00 jne    401eed <submitr+0x741>
1103 401aeb: 45 0f b6 c0    movzbl %r8b,%r8d
1104 401aef: b9 48 27 40 00 mov    $0x402748,%ecx
1105 401af4: ba 08 00 00 00 mov    $0x8,%edx
1106 401af9: be 01 00 00 00 mov    $0x1,%esi
1107 401afe: 48 8d bc 24 40 80 00 lea     0x8040(%rsp),%rdi
1108 401b05: 00
1109 401b06: b8 00 00 00 00 mov    $0x0,%eax
1110 401b0b: e8 60 f1 ff ff callq  400c70 <__sprintf_chk@plt>
1111 401b10: 0f b6 84 24 40 80 00 movzbl 0x8040(%rsp),%eax
1112 401b17: 00
1113 401b18: 88 45 00      mov    %al,0x0(%rbp)
1114 401b1b: 0f b6 84 24 41 80 00 movzbl 0x8041(%rsp),%eax
1115 401b22: 00
1116 401b23: 88 45 01      mov    %al,0x1(%rbp)
1117 401b26: 48 8d 45 03    lea     0x3(%rbp),%rax
1118 401b2a: 0f b6 94 24 42 80 00 movzbl 0x8042(%rsp),%edx
1119 401b31: 00
1120 401b32: 88 55 02      mov    %d1,0x2(%rbp)
1121 401b35: 48 83 c3 01    add    $0x1,%rbx
1122 401b39: 4c 39 eb      cmp    %r13,%rbx
1123 401b3c: 0f 84 42 03 00 00 je     401e84 <submitr+0x6d8>
1124 401b42: 48 89 c5      mov    %rax,%rbp
1125 401b45: e9 48 ff ff ff jmpq    401a92 <submitr+0x2e6>
1126 401b4a: 48 89 da      mov    %rbx,%rdx

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1127 401b4d: 48 89 ee      mov    %rbp,%rsi
1128 401b50: 44 89 e7      mov    %r12d,%edi
1129 401b53: e8 c8 ef ff ff callq  400b20 <write@plt>
1130 401b58: 48 85 c0      test   %rax,%rax
1131 401b5b: 7f 0f        jg     401b6c <submitr+0x3c0>
1132 401b5d: e8 8e ef ff ff callq  400af0 <__errno_location@plt>
1133 401b62: 83 38 04      cmpl   $0x4,(%rax)
1134 401b65: 75 12        jne    401b79 <submitr+0x3cd>
1135 401b67: b8 00 00 00 00 mov    $0x0,%eax
1136 401b6c: 48 01 c5      add    %rax,%rbp
1137 401b6f: 48 29 c3      sub    %rax,%rbx
1138 401b72: 75 d6        jne    401b4a <submitr+0x39e>
1139 401b74: 4d 85 ed      test   %r13,%r13
1140 401b77: 79 5f        jns    401bd8 <submitr+0x42c>
1141 401b79: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
1142 401b80: 3a 20 43
1143 401b83: 49 89 06      mov    %rax,(%r14)
1144 401b86: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
1145 401b8d: 20 75 6e
1146 401b90: 49 89 46 08      mov    %rax,0x8(%r14)
1147 401b94: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1148 401b9b: 74 6f 20
1149 401b9e: 49 89 46 10      mov    %rax,0x10(%r14)
1150 401ba2: 48 b8 77 72 69 74 65 movabs $0x6f74206574697277,%rax
1151 401ba9: 20 74 6f
1152 401bac: 49 89 46 18      mov    %rax,0x18(%r14)
1153 401bb0: 48 b8 20 74 68 65 20 movabs $0x7265732065687420,%rax
1154 401bb7: 73 65 72
1155 401bba: 49 89 46 20      mov    %rax,0x20(%r14)
1156 401bbe: 41 c7 46 28 76 65 72 movl    $0x726576,0x28(%r14)
1157 401bc5: 00
1158 401bc6: 44 89 e7      mov    %r12d,%edi
1159 401bc9: e8 82 ef ff ff callq  400b50 <close@plt>
1160 401bce: b8 ff ff ff ff mov    $0xffffffff,%eax
1161 401bd3: e9 90 02 00 00 jmpq    401e68 <submitr+0x6bc>
1162 401bd8: 44 89 a4 24 40 80 00 mov    %r12d,0x8040(%rsp)
1163 401bdf: 00
1164 401be0: c7 84 24 44 80 00 00 movl    $0x0,0x8044(%rsp)
1165 401be7: 00 00 00 00
1166 401beb: 48 8d 84 24 50 80 00 lea     0x8050(%rsp),%rax
1167 401bf2: 00
1168 401bf3: 48 89 84 24 48 80 00 mov    %rax,0x8048(%rsp)
1169 401bfa: 00
1170 401bfb: ba 00 20 00 00      mov    $0x2000,%edx
1171 401c00: 48 8d 74 24 40      lea     0x40(%rsp),%rsi
1172 401c05: 48 8d bc 24 40 80 00 lea     0x8040(%rsp),%rdi
1173 401c0c: 00
1174 401c0d: e8 7c fa ff ff callq  40168e <rio_readlineb>
1175 401c12: 48 85 c0      test   %rax,%rax
1176 401c15: 7f 74        jg     401c8b <submitr+0x4df>
1177 401c17: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
1178 401c1e: 3a 20 43
1179 401c21: 49 89 06      mov    %rax,(%r14)
1180 401c24: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
1181 401c2b: 20 75 6e
1182 401c2e: 49 89 46 08      mov    %rax,0x8(%r14)
1183 401c32: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1184 401c39: 74 6f 20
1185 401c3c: 49 89 46 10      mov    %rax,0x10(%r14)
1186 401c40: 48 b8 72 65 61 64 20 movabs $0x7269662064616572,%rax
1187 401c47: 66 69 72
1188 401c4a: 49 89 46 18      mov    %rax,0x18(%r14)
1189 401c4e: 48 b8 73 74 20 68 65 movabs $0x6564616568207473,%rax

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1190 401c55: 61 64 65
1191 401c58: 49 89 46 20      mov    %rax,0x20(%r14)
1192 401c5c: 48 b8 72 20 66 72 6f  movabs $0x73206d6f72662072,%rax
1193 401c63: 6d 20 73
1194 401c66: 49 89 46 28      mov    %rax,0x28(%r14)
1195 401c6a: 41 c7 46 30 65 72 76  movl   $0x65767265,0x30(%r14)
1196 401c71: 65
1197 401c72: 66 41 c7 46 34 72 00  movw   $0x72,0x34(%r14)
1198 401c79: 44 89 e7         mov    %r12d,%edi
1199 401c7c: e8 cf ee ff ff   callq 400b50 <close@plt>
1200 401c81: b8 ff ff ff ff   mov    $0xffffffff,%eax
1201 401c86: e9 dd 01 00 00   jmpq  401e68 <submitr+0x6bc>
1202 401c8b: 4c 8d 84 24 40 60 00  lea    0x6040(%rsp),%r8
1203 401c92: 00
1204 401c93: 48 8d 4c 24 2c   lea    0x2c(%rsp),%rcx
1205 401c98: 48 8d 94 24 40 40 00  lea    0x4040(%rsp),%rdx
1206 401c9f: 00
1207 401ca0: be 4f 27 40 00   mov    $0x40274f,%esi
1208 401ca5: 48 8d 7c 24 40   lea    0x40(%rsp),%rdi
1209 401caa: b8 00 00 00 00   mov    $0x0,%eax
1210 401caf: e8 3c ef ff ff   callq 400bf0 <__isoc99_sscanf@plt>
1211 401cb4: 44 8b 44 24 2c   mov    0x2c(%rsp),%r8d
1212 401cb9: 41 81 f8 c8 00 00 00  cmp    $0xc8,%r8d
1213 401cc0: 0f 84 be 00 00 00   je     401d84 <submitr+0x5d8>
1214 401cc6: 4c 8d 8c 24 40 60 00  lea    0x6040(%rsp),%r9
1215 401ccd: 00
1216 401cce: b9 a0 26 40 00   mov    $0x4026a0,%ecx
1217 401cd3: 48 c7 c2 ff ff ff ff  mov    $0xffffffffffffffff,%rdx
1218 401cda: be 01 00 00 00   mov    $0x1,%esi
1219 401cdf: 4c 89 f7         mov    %r14,%rdi
1220 401ce2: b8 00 00 00 00   mov    $0x0,%eax
1221 401ce7: e8 84 ef ff ff   callq 400c70 <__sprintf_chk@plt>
1222 401cec: 44 89 e7         mov    %r12d,%edi
1223 401cef: e8 5c ee ff ff   callq 400b50 <close@plt>
1224 401cf4: b8 ff ff ff ff   mov    $0xffffffff,%eax
1225 401cf9: e9 6a 01 00 00   jmpq  401e68 <submitr+0x6bc>
1226 401cfe: ba 00 20 00 00   mov    $0x2000,%edx
1227 401d03: 48 8d 74 24 40   lea    0x40(%rsp),%rsi
1228 401d08: 48 8d bc 24 40 80 00  lea    0x8040(%rsp),%rdi
1229 401d0f: 00
1230 401d10: e8 79 f9 ff ff   callq 40168e <rio_readlineb>
1231 401d15: 48 85 c0         test   %rax,%rax
1232 401d18: 7f 6a         jg     401d84 <submitr+0x5d8>
1233 401d1a: 48 b8 45 72 72 6f 72  movabs $0x43203a726f727245,%rax
1234 401d21: 3a 20 43
1235 401d24: 49 89 06         mov    %rax,(%r14)
1236 401d27: 48 b8 6c 69 65 6e 74  movabs $0x6e7520746e65696c,%rax
1237 401d2e: 20 75 6e
1238 401d31: 49 89 46 08      mov    %rax,0x8(%r14)
1239 401d35: 48 b8 61 62 6c 65 20  movabs $0x206f7420656c6261,%rax
1240 401d3c: 74 6f 20
1241 401d3f: 49 89 46 10      mov    %rax,0x10(%r14)
1242 401d43: 48 b8 72 65 61 64 20  movabs $0x6165682064616572,%rax
1243 401d4a: 68 65 61
1244 401d4d: 49 89 46 18      mov    %rax,0x18(%r14)
1245 401d51: 48 b8 64 65 72 73 20  movabs $0x6f72662073726564,%rax
1246 401d58: 66 72 6f
1247 401d5b: 49 89 46 20      mov    %rax,0x20(%r14)
1248 401d5f: 48 b8 6d 20 73 65 72  movabs $0x726576726573206d,%rax
1249 401d66: 76 65 72
1250 401d69: 49 89 46 28      mov    %rax,0x28(%r14)
1251 401d6d: 41 c6 46 30 00   movb   $0x0,0x30(%r14)
1252 401d72: 44 89 e7         mov    %r12d,%edi

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1253 401d75: e8 d6 ed ff ff      callq 400b50 <close@plt>
1254 401d7a: b8 ff ff ff ff      mov    $0xffffffff,%eax
1255 401d7f: e9 e4 00 00 00      jmpq   401e68 <submitr+0x6bc>
1256 401d84: 80 7c 24 40 0d      cmpb   $0xd,0x40(%rsp)
1257 401d89: 0f 85 6f ff ff ff   jne     401cfe <submitr+0x552>
1258 401d8f: 80 7c 24 41 0a      cmpb   $0xa,0x41(%rsp)
1259 401d94: 0f 85 64 ff ff ff   jne     401cfe <submitr+0x552>
1260 401d9a: 80 7c 24 42 00      cmpb   $0x0,0x42(%rsp)
1261 401d9f: 0f 85 59 ff ff ff   jne     401cfe <submitr+0x552>
1262 401da5: ba 00 20 00 00      mov     $0x2000,%edx
1263 401daa: 48 8d 74 24 40      lea     0x40(%rsp),%rsi
1264 401daf: 48 8d bc 24 40 80 00 lea     0x8040(%rsp),%rdi
1265 401db6: 00
1266 401db7: e8 d2 f8 ff ff      callq 40168e <rio_readlineb>
1267 401dbc: 48 85 c0            test    %rax,%rax
1268 401dbf: 7f 70             jg      401e31 <submitr+0x685>
1269 401dc1: 48 b8 45 72 72 6f 72 movabs  $0x43203a726f727245,%rax
1270 401dc8: 3a 20 43
1271 401dcb: 49 89 06            mov     %rax,(%r14)
1272 401dce: 48 b8 6c 69 65 6e 74 movabs  $0x6e7520746e65696c,%rax
1273 401dd5: 20 75 6e
1274 401dd8: 49 89 46 08            mov     %rax,0x8(%r14)
1275 401ddc: 48 b8 61 62 6c 65 20 movabs  $0x206f7420656c6261,%rax
1276 401de3: 74 6f 20
1277 401de6: 49 89 46 10            mov     %rax,0x10(%r14)
1278 401dea: 48 b8 72 65 61 64 20 movabs  $0x6174732064616572,%rax
1279 401df1: 73 74 61
1280 401df4: 49 89 46 18            mov     %rax,0x18(%r14)
1281 401df8: 48 b8 74 75 73 20 6d movabs  $0x7373656d20737574,%rax
1282 401dff: 65 73 73
1283 401e02: 49 89 46 20            mov     %rax,0x20(%r14)
1284 401e06: 48 b8 61 67 65 20 66 movabs  $0x6d6f726620656761,%rax
1285 401e0d: 72 6f 6d
1286 401e10: 49 89 46 28            mov     %rax,0x28(%r14)
1287 401e14: 48 b8 20 73 65 72 76 movabs  $0x72657672657320,%rax
1288 401e1b: 65 72 00
1289 401e1e: 49 89 46 30            mov     %rax,0x30(%r14)
1290 401e22: 44 89 e7            mov     %r12d,%edi
1291 401e25: e8 26 ed ff ff      callq 400b50 <close@plt>
1292 401e2a: b8 ff ff ff ff      mov     $0xffffffff,%eax
1293 401e2f: eb 37             jmp     401e68 <submitr+0x6bc>
1294 401e31: 48 8d 74 24 40      lea     0x40(%rsp),%rsi
1295 401e36: 4c 89 f7            mov     %r14,%rdi
1296 401e39: e8 c2 ec ff ff      callq 400b00 <strcpy@plt>
1297 401e3e: 44 89 e7            mov     %r12d,%edi
1298 401e41: e8 0a ed ff ff      callq 400b50 <close@plt>
1299 401e46: 41 0f b6 06          movzbl  (%r14),%eax
1300 401e4a: 83 e8 4f            sub     $0x4f,%eax
1301 401e4d: 75 0f             jne     401e5e <submitr+0x6b2>
1302 401e4f: 41 0f b6 46 01      movzbl  0x1(%r14),%eax
1303 401e54: 83 e8 4b            sub     $0x4b,%eax
1304 401e57: 75 05             jne     401e5e <submitr+0x6b2>
1305 401e59: 41 0f b6 46 02      movzbl  0x2(%r14),%eax
1306 401e5e: 85 c0            test    %eax,%eax
1307 401e60: 0f 95 c0            setne   %al
1308 401e63: 0f b6 c0          movzbl  %al,%eax
1309 401e66: f7 d8            neg     %eax
1310 401e68: 48 8b 94 24 58 a0 00 mov     0xa058(%rsp),%rdx
1311 401e6f: 00
1312 401e70: 64 48 33 14 25 28 00 xor     %fs:0x28,%rdx
1313 401e77: 00 00
1314 401e79: 0f 84 00 01 00 00   je      401f7f <submitr+0x7d3>
1315 401e7f: e9 f6 00 00 00      jmpq   401f7a <submitr+0x7ce>

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1316 401e84: 48 8d 84 24 40 20 00 lea    0x2040(%rsp),%rax
1317 401e8b: 00
1318 401e8c: 48 89 44 24 08      mov    %rax,0x8(%rsp)
1319 401e91: 4c 89 3c 24          mov    %r15, (%rsp)
1320 401e95: 4c 8b 4c 24 18      mov    0x18(%rsp),%r9
1321 401e9a: 4c 8b 44 24 10      mov    0x10(%rsp),%r8
1322 401e9f: b9 d0 26 40 00      mov    $0x4026d0,%ecx
1323 401ea4: ba 00 20 00 00      mov    $0x2000,%edx
1324 401ea9: be 01 00 00 00      mov    $0x1,%esi
1325 401eae: 48 8d 7c 24 40      lea    0x40(%rsp),%rdi
1326 401eb3: b8 00 00 00 00      mov    $0x0,%eax
1327 401eb8: e8 b3 ed ff ff      callq 400c70 <__sprintf_chk@plt>
1328 401ebd: 48 8d 7c 24 40      lea    0x40(%rsp),%rdi
1329 401ec2: b8 00 00 00 00      mov    $0x0,%eax
1330 401ec7: 48 c7 c1 ff ff ff ff mov    $0xffffffffffffffff,%rcx
1331 401ece: f2 ae              repnz scas %es:(%rdi),%al
1332 401ed0: 48 f7 d1            not    %rcx
1333 401ed3: 48 83 e9 01          sub    $0x1,%rcx
1334 401ed7: 49 89 cd            mov    %rcx,%r13
1335 401eda: 0f 84 f8 fc ff ff   je     401bd8 <submitr+0x42c>
1336 401ee0: 48 89 cb            mov    %rcx,%rbx
1337 401ee3: 48 8d 6c 24 40      lea    0x40(%rsp),%rbp
1338 401ee8: e9 5d fc ff ff      jmpq   401b4a <submitr+0x39e>
1339 401eed: 48 b8 45 72 72 6f 72 movabs $0x52203a726f727245,%rax
1340 401ef4: 3a 20 52
1341 401ef7: 49 89 06            mov    %rax, (%r14)
1342 401efa: 48 b8 65 73 75 6c 74 movabs $0x747320746c757365,%rax
1343 401f01: 20 73 74
1344 401f04: 49 89 46 08          mov    %rax,0x8(%r14)
1345 401f08: 48 b8 72 69 6e 67 20 movabs $0x6e6f6320676e6972,%rax
1346 401f0f: 63 6f 6e
1347 401f12: 49 89 46 10          mov    %rax,0x10(%r14)
1348 401f16: 48 b8 74 61 69 6e 73 movabs $0x6e6120736e696174,%rax
1349 401f1d: 20 61 6e
1350 401f20: 49 89 46 18          mov    %rax,0x18(%r14)
1351 401f24: 48 b8 20 69 6c 6c 65 movabs $0x6c6167656c6c6920,%rax
1352 401f2b: 67 61 6c
1353 401f2e: 49 89 46 20          mov    %rax,0x20(%r14)
1354 401f32: 48 b8 20 6f 72 20 75 movabs $0x72706e7520726f20,%rax
1355 401f39: 6e 70 72
1356 401f3c: 49 89 46 28          mov    %rax,0x28(%r14)
1357 401f40: 48 b8 69 6e 74 61 62 movabs $0x20656c6261746e69,%rax
1358 401f47: 6c 65 20
1359 401f4a: 49 89 46 30          mov    %rax,0x30(%r14)
1360 401f4e: 48 b8 63 68 61 72 61 movabs $0x6574636172616863,%rax
1361 401f55: 63 74 65
1362 401f58: 49 89 46 38          mov    %rax,0x38(%r14)
1363 401f5c: 66 41 c7 46 40 72 2e movw   $0x2e72,0x40(%r14)
1364 401f63: 41 c6 46 42 00      movb   $0x0,0x42(%r14)
1365 401f68: 44 89 e7            mov    %r12d,%edi
1366 401f6b: e8 e0 eb ff ff      callq 400b50 <close@plt>
1367 401f70: b8 ff ff ff ff      mov    $0xffffffff,%eax
1368 401f75: e9 ee fe ff ff      jmpq   401e68 <submitr+0x6bc>
1369 401f7a: e8 b1 eb ff ff      callq 400b30 <__stack_chk_fail@plt>
1370 401f7f: 48 81 c4 68 a0 00 00 add     $0xa068,%rsp
1371 401f86: 5b                  pop    %rbx
1372 401f87: 5d                  pop    %rbp
1373 401f88: 41 5c              pop    %r12
1374 401f8a: 41 5d              pop    %r13
1375 401f8c: 41 5e              pop    %r14
1376 401f8e: 41 5f              pop    %r15
1377 401f90: c3                  retq
1378

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1379 000000000401f91 <init_timeout>:
1380 401f91: 53                push  %rbx
1381 401f92: 89 fb            mov   %edi,%ebx
1382 401f94: 85 ff            test  %edi,%edi
1383 401f96: 74 1e            je     401fb6 <init_timeout+0x25>
1384 401f98: be 60 16 40 00   mov   $0x401660,%esi
1385 401f9d: bf 0e 00 00 00   mov   $0xe,%edi
1386 401fa2: e8 e9 eb ff ff   callq 400b90 <signal@plt>
1387 401fa7: 85 db            test  %ebx,%ebx
1388 401fa9: bf 00 00 00 00   mov   $0x0,%edi
1389 401fae: 0f 49 fb         cmovns %ebx,%edi
1390 401fb1: e8 8a eb ff ff   callq 400b40 <alarm@plt>
1391 401fb6: 5b                pop   %rbx
1392 401fb7: c3                retq
1393
1394 000000000401fb8 <init_driver>:
1395 401fb8: 55                push  %rbp
1396 401fb9: 53                push  %rbx
1397 401fba: 48 83 ec 28       sub   $0x28,%rsp
1398 401fbe: 48 89 fd          mov   %rdi,%rbp
1399 401fc1: 64 48 8b 04 25 28 00 mov   %fs:0x28,%rax
1400 401fc8: 00 00
1401 401fca: 48 89 44 24 18     mov   %rax,0x18(%rsp)
1402 401fcf: 31 c0             xor   %eax,%eax
1403 401fd1: be 01 00 00 00     mov   $0x1,%esi
1404 401fd6: bf 0d 00 00 00     mov   $0xd,%edi
1405 401fdb: e8 b0 eb ff ff     callq 400b90 <signal@plt>
1406 401fe0: be 01 00 00 00     mov   $0x1,%esi
1407 401fe5: bf 1d 00 00 00     mov   $0x1d,%edi
1408 401fea: e8 a1 eb ff ff     callq 400b90 <signal@plt>
1409 401fef: be 01 00 00 00     mov   $0x1,%esi
1410 401ff4: bf 1d 00 00 00     mov   $0x1d,%edi
1411 401ff9: e8 92 eb ff ff     callq 400b90 <signal@plt>
1412 401ffe: ba 00 00 00 00     mov   $0x0,%edx
1413 402003: be 01 00 00 00     mov   $0x1,%esi
1414 402008: bf 02 00 00 00     mov   $0x2,%edi
1415 40200d: e8 6e ec ff ff     callq 400c80 <socket@plt>
1416 402012: 89 c3             mov   %eax,%ebx
1417 402014: 85 c0             test  %eax,%eax
1418 402016: 79 4f             jns   402067 <init_driver+0xaf>
1419 402018: 48 b8 45 72 72 6f 72 movabs $0x43203a726f727245,%rax
1420 40201f: 3a 20 43
1421 402022: 48 89 45 00        mov   %rax,0x0(%rbp)
1422 402026: 48 b8 6c 69 65 6e 74 movabs $0x6e7520746e65696c,%rax
1423 40202d: 20 75 6e
1424 402030: 48 89 45 08        mov   %rax,0x8(%rbp)
1425 402034: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1426 40203b: 74 6f 20
1427 40203e: 48 89 45 10        mov   %rax,0x10(%rbp)
1428 402042: 48 b8 63 72 65 61 74 movabs $0x7320657461657263,%rax
1429 402049: 65 20 73
1430 40204c: 48 89 45 18        mov   %rax,0x18(%rbp)
1431 402050: c7 45 20 6f 63 6b 65 movl   $0x656b636f,0x20(%rbp)
1432 402057: 66 c7 45 24 74 00   movw   $0x74,0x24(%rbp)
1433 40205d: b8 ff ff ff ff     mov   $0xffffffff,%eax
1434 402062: e9 0a 01 00 00     jmpq   402171 <init_driver+0x1b9>
1435 402067: bf 60 27 40 00     mov   $0x402760,%edi
1436 40206c: e8 2f eb ff ff     callq 400ba0 <gethostbyname@plt>
1437 402071: 48 85 c0            test  %rax,%rax
1438 402074: 75 68             jne    4020de <init_driver+0x126>
1439 402076: 48 b8 45 72 72 6f 72 movabs $0x44203a726f727245,%rax
1440 40207d: 3a 20 44
1441 402080: 48 89 45 00        mov   %rax,0x0(%rbp)

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1442 402084: 48 b8 4e 53 20 69 73 movabs $0x6e7520736920534e,%rax
1443 40208b: 20 75 6e
1444 40208e: 48 89 45 08          mov    %rax,0x8(%rbp)
1445 402092: 48 b8 61 62 6c 65 20 movabs $0x206f7420656c6261,%rax
1446 402099: 74 6f 20
1447 40209c: 48 89 45 10          mov    %rax,0x10(%rbp)
1448 4020a0: 48 b8 72 65 73 6f 6c movabs $0x2065766c6f736572,%rax
1449 4020a7: 76 65 20
1450 4020aa: 48 89 45 18          mov    %rax,0x18(%rbp)
1451 4020ae: 48 b8 73 65 72 76 65 movabs $0x6120726576726573,%rax
1452 4020b5: 72 20 61
1453 4020b8: 48 89 45 20          mov    %rax,0x20(%rbp)
1454 4020bc: c7 45 28 64 64 72 65 movl   $0x65726464,0x28(%rbp)
1455 4020c3: 66 c7 45 2c 73 73    movw   $0x7373,0x2c(%rbp)
1456 4020c9: c6 45 2e 00          movb   $0x0,0x2e(%rbp)
1457 4020cd: 89 df                mov    %ebx,%edi
1458 4020cf: e8 7c ea ff ff       callq  400b50 <close@plt>
1459 4020d4: b8 ff ff ff ff       mov    $0xffffffff,%eax
1460 4020d9: e9 93 00 00 00       jmpq   402171 <init_driver+0x1b9>
1461 4020de: 48 c7 04 24 00 00 00 movq   $0x0,(%rsp)
1462 4020e5: 00
1463 4020e6: 48 c7 44 24 08 00 00 movq   $0x0,0x8(%rsp)
1464 4020ed: 00 00
1465 4020ef: 66 c7 04 24 02 00    movw   $0x2,(%rsp)
1466 4020f5: 48 63 50 14          movslq 0x14(%rax),%rdx
1467 4020f9: 48 8b 40 18          mov    0x18(%rax),%rax
1468 4020fd: 48 8d 7c 24 04       lea    0x4(%rsp),%rdi
1469 402102: b9 0c 00 00 00       mov    $0xc,%ecx
1470 402107: 48 8b 30             mov    (%rax),%rsi
1471 40210a: e8 a1 ea ff ff       callq  400bb0 <__memmove_chk@plt>
1472 40210f: 66 c7 44 24 02 3b 6e movw   $0x6e3b,0x2(%rsp)
1473 402116: ba 10 00 00 00       mov    $0x10,%edx
1474 40211b: 48 89 e6             mov    %rsp,%rsi
1475 40211e: 89 df                mov    %ebx,%edi
1476 402120: e8 0b eb ff ff       callq  400c30 <connect@plt>
1477 402125: 85 c0                test   %eax,%eax
1478 402127: 79 32                jns    40215b <init_driver+0x1a3>
1479 402129: 41 b8 60 27 40 00    mov    $0x402760,%r8d
1480 40212f: b9 20 27 40 00       mov    $0x402720,%ecx
1481 402134: 48 c7 c2 ff ff ff ff mov    $0xffffffffffffffff,%rdx
1482 40213b: be 01 00 00 00       mov    $0x1,%esi
1483 402140: 48 89 ef             mov    %rbp,%rdi
1484 402143: b8 00 00 00 00       mov    $0x0,%eax
1485 402148: e8 23 eb ff ff       callq  400c70 <__sprintf_chk@plt>
1486 40214d: 89 df                mov    %ebx,%edi
1487 40214f: e8 fc e9 ff ff       callq  400b50 <close@plt>
1488 402154: b8 ff ff ff ff       mov    $0xffffffff,%eax
1489 402159: eb 16                jmp     402171 <init_driver+0x1b9>
1490 40215b: 89 df                mov    %ebx,%edi
1491 40215d: e8 ee e9 ff ff       callq  400b50 <close@plt>
1492 402162: 66 c7 45 00 4f 4b    movw   $0x4b4f,0x0(%rbp)
1493 402168: c6 45 02 00          movb   $0x0,0x2(%rbp)
1494 40216c: b8 00 00 00 00       mov    $0x0,%eax
1495 402171: 48 8b 4c 24 18       mov    0x18(%rsp),%rcx
1496 402176: 64 48 33 0c 25 28 00 xor     %fs:0x28,%rcx
1497 40217d: 00 00
1498 40217f: 74 05                je     402186 <init_driver+0x1ce>
1499 402181: e8 aa e9 ff ff       callq  400b30 <__stack_chk_fail@plt>
1500 402186: 48 83 c4 28          add     $0x28,%rsp
1501 40218a: 5b                    pop     %rbx
1502 40218b: 5d                    pop     %rbp
1503 40218c: c3                    retq
1504

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1505 00000000040218d <driver_post>:
1506 40218d: 53          push  %rbx
1507 40218e: 48 83 ec 10 sub  $0x10,%rsp
1508 402192: 48 89 cb     mov  %rcx,%rbx
1509 402195: 85 d2       test %edx,%edx
1510 402197: 74 27       je   4021c0 <driver_post+0x33>
1511 402199: 48 89 f2     mov  %rsi,%rdx
1512 40219c: be 78 27 40 00 mov $0x402778,%esi
1513 4021a1: bf 01 00 00 00 mov $0x1,%edi
1514 4021a6: b8 00 00 00 00 mov $0x0,%eax
1515 4021ab: e8 50 ea ff ff callq 400c00 <__printf_chk@plt>
1516 4021b0: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)
1517 4021b5: c6 43 02 00 movb $0x0,0x2(%rbx)
1518 4021b9: b8 00 00 00 00 mov $0x0,%eax
1519 4021be: eb 3e       jmp  4021fe <driver_post+0x71>
1520 4021c0: 48 85 ff     test %rdi,%rdi
1521 4021c3: 74 2b       je   4021f0 <driver_post+0x63>
1522 4021c5: 80 3f 00     cmpb $0x0,(%rdi)
1523 4021c8: 74 26       je   4021f0 <driver_post+0x63>
1524 4021ca: 48 89 0c 24 mov  %rcx,(%rsp)
1525 4021ce: 49 89 f1     mov  %rsi,%r9
1526 4021d1: 41 b8 ec 22 40 00 mov $0x4022ec,%r8d
1527 4021d7: 48 89 f9     mov  %rdi,%rcx
1528 4021da: ba 8f 27 40 00 mov $0x40278f,%edx
1529 4021df: be 6e 3b 00 00 mov $0x3b6e,%esi
1530 4021e4: bf 60 27 40 00 mov $0x402760,%edi
1531 4021e9: e8 be f5 ff ff callq 4017ac <submitr>
1532 4021ee: eb 0e       jmp  4021fe <driver_post+0x71>
1533 4021f0: 66 c7 03 4f 4b movw $0x4b4f,(%rbx)
1534 4021f5: c6 43 02 00 movb $0x0,0x2(%rbx)
1535 4021f9: b8 00 00 00 00 mov $0x0,%eax
1536 4021fe: 48 83 c4 10 add  $0x10,%rsp
1537 402202: 5b          pop   %rbx
1538 402203: c3          retq
1539 402204: 90          nop
1540 402205: 90          nop
1541 402206: 90          nop
1542 402207: 90          nop
1543 402208: 90          nop
1544 402209: 90          nop
1545 40220a: 90          nop
1546 40220b: 90          nop
1547 40220c: 90          nop
1548 40220d: 90          nop
1549 40220e: 90          nop
1550 40220f: 90          nop
1551
1552 000000000402210 <__libc_csu_init>:
1553 402210: 48 89 6c 24 d8 mov  %rbp,-0x28(%rsp)
1554 402215: 4c 89 64 24 e0 mov  %r12,-0x20(%rsp)
1555 40221a: 48 8d 2d df 0b 20 00 lea  0x200bdf(%rip),%rbp # 602e00 <__init_array_end>
1556 402221: 4c 8d 25 d0 0b 20 00 lea  0x200bd0(%rip),%r12 # 602df8
1557 <__frame_dummy_init_array_entry>
1557 402228: 4c 89 6c 24 e8 mov  %r13,-0x18(%rsp)
1558 40222d: 4c 89 74 24 f0 mov  %r14,-0x10(%rsp)
1559 402232: 4c 89 7c 24 f8 mov  %r15,-0x8(%rsp)
1560 402237: 48 89 5c 24 d0 mov  %rbx,-0x30(%rsp)
1561 40223c: 48 83 ec 38 sub  $0x38,%rsp
1562 402240: 4c 29 e5     sub  %r12,%rbp
1563 402243: 41 89 fd     mov  %edi,%r13d
1564 402246: 49 89 f6     mov  %rsi,%r14
1565 402249: 48 c1 fd 03 sar  $0x3,%rbp
1566 40224d: 49 89 d7     mov  %rdx,%r15

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1567 402250: e8 6b e8 ff ff      callq 400ac0 <_init>
1568 402255: 48 85 ed                test  %rbp,%rbp
1569 402258: 74 1c                  je    402276 <__libc_csu_init+0x66>
1570 40225a: 31 db                xor   %ebx,%ebx
1571 40225c: 0f 1f 40 00          nopl  0x0(%rax)
1572 402260: 4c 89 fa                mov   %r15,%rdx
1573 402263: 4c 89 f6                mov   %r14,%rsi
1574 402266: 44 89 ef                mov   %r13d,%edi
1575 402269: 41 ff 14 dc          callq *(%r12,%rbx,8)
1576 40226d: 48 83 c3 01          add   $0x1,%rbx
1577 402271: 48 39 eb                cmp   %rbp,%rbx
1578 402274: 75 ea                jne   402260 <__libc_csu_init+0x50>
1579 402276: 48 8b 5c 24 08        mov   0x8(%rsp),%rbx
1580 40227b: 48 8b 6c 24 10        mov   0x10(%rsp),%rbp
1581 402280: 4c 8b 64 24 18        mov   0x18(%rsp),%r12
1582 402285: 4c 8b 6c 24 20        mov   0x20(%rsp),%r13
1583 40228a: 4c 8b 74 24 28        mov   0x28(%rsp),%r14
1584 40228f: 4c 8b 7c 24 30        mov   0x30(%rsp),%r15
1585 402294: 48 83 c4 38          add   $0x38,%rsp
1586 402298: c3                    retq
1587 402299: 0f 1f 80 00 00 00 00 nopl  0x0(%rax)
1588
1589 0000000004022a0 <__libc_csu_fini>:
1590 4022a0: f3 c3                repz retq
1591 4022a2: 90                    nop
1592 4022a3: 90                    nop
1593
1594 Disassembly of section .fini:
1595
1596 0000000004022a4 <_fini>:
1597 4022a4: 48 83 ec 08          sub   $0x8,%rsp
1598 4022a8: 48 83 c4 08          add   $0x8,%rsp
1599 4022ac: c3                    retq
1600

```