

#### SYCL - Introduction and Hands-on

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# **Book Keeping**

Get the example and the presentation:

```
git clone https://github.com/alcf-perfengr/sycltrain cd sycltrain/presentation/2020_07_30_ATPESC
```

People who are here to help:

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Introduction

# What programming model to use to target GPU?

- · OpenMP (pragma based)
- Cuda (proprietary)
- · Hip (low level)
- · OpenCL (low level)
- Kokkos, RAJA, OCCA (high level, abstraction layer, academic projects)



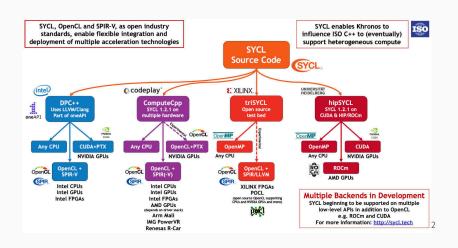
# What is SYCL™?<sup>1</sup>

- 1. Target C++ programmers (template, lambda)
  - 1.1 No language extension
  - 1.2 No pragmas
  - 1.3 No attribute
- 2. Borrow lot of concept from battle tested OpenCL (platform, device, work-group, range)
- 3. Single Source (two compilations passes)
- 4. High level data-transfer
- SYCL is a Specification developed by the Khronos Group (OpenCL, SPIR, Vulkan, OpenGL)

<sup>&</sup>lt;sup>1</sup>SYCL Doesn't mean "Someone You Couldn't Love". Sadly.



# **SYCL Implementations**



<sup>&</sup>lt;sup>2</sup>Credit: Khronos groups (https://www.khronos.org/sycl/)



# Goal of this presentation

- 1. Give you a feel of SYCL
- 2. Go through code examples (and make you do some homework)
- 3. Teach you enough so that can search for the rest if you interested
- 4. Ouestion are welcomed! <sup>3</sup>

<sup>&</sup>lt;sup>3</sup>Please just talk, or use slack



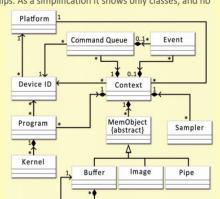
# Theory

# A picture is worth a thousand words<sup>4</sup>

#### **OpenCL Class Diagram**

The figure below describes the OpenCL specification as a class diagram using the Unified Modeling Language<sup>1</sup> (UML) notation. The diagram shows both nodes and edges which are classes and their relationships. As a simplification it shows only classes, and no attributes or operations.

Annotations Relationships abstract classes {abstract} aggregations inheritance Δ relationship ^ navigability Cardinality many one and only one 1 0..1 optionally one 1.\* one or more



Unified Modeling Language (http://www.uml.org/) is a trademark of Object Management Group (OMG).

<sup>&</sup>lt;sup>4</sup>and this is a UML diagram so maybe more!



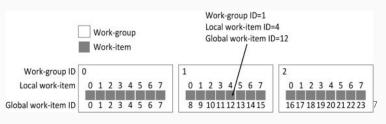
# Memory management: SYCL innovation

- 1. Buffers encapsulate your data
- 2. Accessors describe how you access those data
- 3. Buffer destruction will cause synchronization



# **Implicit Loop**

- A Kernel is invoked once for each work item 5
- · local work size Work items are grouped into a work group <sup>6</sup>
- The total number of all work items is specified by the global work size



<sup>&</sup>lt;sup>5</sup>similar to *MPI\_rank* 

<sup>&</sup>lt;sup>7</sup>Credit The OpenCL Programming Book by Fixstars



<sup>&</sup>lt;sup>6</sup>similar to *pragma omp simdlen/safelen* 

# Implicit Loop: Example!

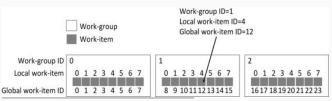
```
global_work_size = 24; local_work_size = 8
```

#### SYCL / Opencl / CUDA

parallel\_for<global\_work\_size,local\_work\_size>(mykernel);

#### OpenMP<sup>8</sup>

```
# wG = work_group; wC = work_item
for (wG_id=0; wG_id++; wG_id < (global_work_size / local_work_size)
for (local_wI_id=0; local_wI_id++; local_wI_id < local_work_size)
global_wI_id = local_wI_id + wG_id*local_wG_size</pre>
```



<sup>&</sup>lt;sup>8</sup>Using chunking / tilling / vectorization technique



Hands-on

### Where do I run (cluster)?

Two Cluster are available for you 9:

- 1. cooley.alcf.anl.gov. Nvidia GPU (Tesla K80)
- 2. devcloud.intel.com. Intel iGPU (Intel Iris "Gen9")
  - https://devcloud.intel.com/datacenter/learn/ connect-with-ssh-linux-macos/<sup>11</sup>

- https://software.intel.com/content/www/us/en/develop/ articles/oneapi-repo-instructions.html
- or https://github.com/alcf-perfengr/sycltrain/blob/master/ .travis.yml

<sup>&</sup>lt;sup>11</sup>And if you are using windows you shouldn't!



<sup>&</sup>lt;sup>9</sup>If you feel adventurous<sup>10</sup>, install a SYCL compiler in your machine:

#### What do I run?

- After logged to you the cluster, get the example and the presentation:
- git clone https://github.com/alcf-perfengr/sycltrain
- cd sycltrain/presentation/2020\_07\_30\_ATPESC
- 2. Get a node
- ./fetch a node.devcloud.sh
- 2 # ./fetch\_a\_node.cooley.sh
- 3. Source the correct environment
- cd sycltrain/presentation/2020 07 30 ATPESC
- source env.devcloud.rc
- 3 #source env.cooley.rc
- 4. Compile and run examples
- cd 9\_sycl\_of\_hell
- 2 make run\_0\_tiny\_sycl\_info



# **Example and Exercise**

- 1. Examples are available in the 9\_sycl\_of\_hell folder
- 2. Exercises are available in the exercise folder
  - 2.1 We will do the One Atom together 12
  - 2.2 Harder problem will be covered at the end if we have time<sup>13</sup>

<sup>&</sup>lt;sup>13</sup>But you are encouraged to do them by yourself... And send me the solution!



<sup>12!=&</sup>quot;watching me"

## Lets go!

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  - 2.2 Harder problem will be covered at the end if we have time<sup>15</sup>

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# Conclusion

#### Conclusion

- 1. SYCL is C++
- 2. Many vendors (Intel, Nvidia, AMD) and hardware (CPU, GPU, FPGA) supported
- 3. Implicit data-movement by default (Buffer / Accessors concepts)



# Lot of goods resources online

### Spec

- 1. https://www.khronos.org/registry/SYCL/specs/
  sycl-1.2.1.pdf
- 2. https://www.khronos.org/files/sycl/ sycl-121-reference-card.pdf

#### Examples

- 1. https://github.com/codeplaysoftware/
   computecpp-sdk/tree/master/samples
- 2. https://github.com/alcf-perfengr/sycltrain

#### **Documentations**

- 1. https://sycl.tech/
- 2. Mastering DPC++ for Programming of Heterogeneous Systems using C++ and SYCL (ISBN 978-1-4842-5574-2)



#### Q&A

Thanks you! Do you have any questions?

