## 1.构建RISCV版本的benchmark binary

SPEC版本:1.1.9

配置:

架构:x86

OS: CentOS Stream release 8

官方完整的构建SPEC的步骤在这里:https://www.spec.org/cpu2017/Docs/quick-start.html

Example Install: This example presumes that your name is Walter, your compiler is GCC, your operating system is Linux, and your hardware is x86.

SPEC CPU®2017 Quick Start: Copyright © 2017-2019 Standard Performance Evaluation Corporation (SPEC®)

这里假设你已经完成了前面挂载,安装SPEC2017的过程,接下来给出编译benchmark的过程。

这里是在x86架构上使用**riscv的工具链(需要自己准备)**作为编译工具进行交叉编译,编译出SPEC的benchmark二进制程序:

1. 首先需要修改官方给出的config文件为适配RISCV,这里直接给出一份改过后能用的:

```
# SPEC CPU2017 config file for: gcc / g++ / gfortran on Linux Risc-v(xwt)
# Usage: (1) Copy this to a new name
              cd $SPEC/config
              cp Example-x.cfg myname.cfg
         (2) Change items that are marked 'EDIT' (search for it)
# SPEC tested this config file with:
    Compiler version(s): 4.4.7, 4.9.2, 5.2.0, 6.3.0 Operating system(s): Oracle Linux Server 6.5 and
                             Oracle Linux Server 6.5 and 7.2 /
                            Red Hat Enterprise Linux Server 6.5 and 7.2
\ensuremath{\text{\#}} If your system differs, this config file might not work.
# You might find a better config file at http://www.spec.org/cpu2017/results
# Known Limitations with GCC 4
    (1) Possible problem: compile time messages
                              error: unrecognized command line option '...'
       Recommendation: Use a newer version of the compiler.
                          If that is not possible, remove the unrecognized
                          option from this config file.
   (2) Possible problem: run time errors messages
           527.cam4_r or 627.cam4_s *** Miscompare of cam4_validate.txt
       Recommendation: Use a newer version of the compiler.
                       If that is not possible, try reducing the optimization.
# Compiler issues: Contact your compiler vendor, not SPEC.
# For SPEC help: http://www.spec.org/cpu2017/Docs/techsupport.html
```

```
#-----
#----- Label -----
# Arbitrary string to tag binaries
              Two Suggestions: # (1) EDIT this label as you try new ideas.
                         # (2) Use a label meaningful to *you*.
%define label mytest
#----- Preprocessor -----
                   # EDIT to control 32 or 64 bit compilation. Or,
64 # you can set it on the command line using:
%ifndef %{bits}
% define bits
%endif
                          #
                                 'runcpu --define bits=nn'
%ifndef %{build_ncpus}  # EDIT to adjust number of simultaneous compiles.
% define build_ncpus 8  # Or, you can set it on the command line:
%endif
                                 'runcpu --define build_ncpus=nn'
# Don't change this part.
%define os
                   LINUX
%if %{bits} == 64
% define model
%elif %{bits} == 32
% define model
                    -m32
%else
% error Please define number of bits - see instructions in config file
#----- Global Settings -----
# For info, see:
     https://www.spec.org/cpu2017/Docs/config.html#fieldname
# Example: https://www.spec.org/cpu2017/Docs/config.html#tune
command_add_redirect = 1
iterations
                = 1
= %{label}-m%{bits}
line_width
                = 1020
log_line_width = 1020
makeflags = --jobs=%{build_ncpus}
            = 1
mean_anyway
output_format
                 = txt,html,cfg,pdf,csv
                = 1
preenv
reportable
                 = 0
                  = base
tune
#----- How Many CPUs? -----
\# Both SPECrate and SPECspeed can test multiple chips / cores / hw threads
   - For SPECrate, you set the number of copies.
   - For SPECspeed, you set the number of threads.
{\tt \# See: https://www.spec.org/cpu2017/Docs/system-requirements.html \# Multiple CPUs}
   q. How many should I set?
#
   a. Unknown, you will have to try it and see!
# To get you started, some suggestions:
     copies - This config file defaults to testing only 1 copy. You might
#
             try changing it to match the number of cores on your system,
             or perhaps the number of virtual CPUs as reported by:
                  grep -c processor /proc/cpuinfo
             Be sure you have enough memory. See:
#
             https://www.spec.org/cpu2017/Docs/system-requirements.html#memory
     threads - This config file sets a starting point. You could try raising
             it. A higher thread count is much more likely to be useful for
             fpspeed than for intspeed.
intrate, fprate:
                 = 1 # EDIT to change number of copies (see above)
  copies
intspeed, fpspeed:
  threads
                 = 4 # EDIT to change number of OpenMP threads (see above)
```

```
#----- Compilers
default:
                   EDIT: the directory where your compiler is installed
%ifndef %{gcc_dir}
% define gcc_dir /opt/riscv-toolchain-bin-rv64gc-linux
%endif
# EDIT if needed: the preENV line adds library directories to the runtime
      path. You can adjust it, or add lines for other environment variables.
      See: https://www.spec.org/cpu2017/Docs/config.html#preenv
      and: https://gcc.gnu.org/onlinedocs/gcc/Environment-Variables.html
  \verb|preENV_LD_LIBRARY_PATH| = %{gcc_dir}/lib64/:%{gcc_dir}/lib/:/lib64|
 #preENV_LD_LIBRARY_PATH = %{gcc_dir}/lib64/:%{gcc_dir}/lib64:%{ENV_LD_LIBRARY_PATH}
                        = %{gcc_dir}/bin/
  SPECLANG
  CC
                        = $(SPECLANG)riscv64-unknown-linux-gnu-gcc
                                                                    -std=c99
  CXX
                         = $(SPECLANG)riscv64-unknown-linux-gnu-g++
                        = $(SPECLANG)riscv64-unknown-linux-gnu-gfortran
  # How to say "Show me your version, please"
  CC_VERSION_OPTION = -v
  CXX_VERSION_OPTION
                        = -v
  FC_VERSION_OPTION
  EXTRA\_CFLAGS = -march=rv64imafd - w
  EXTRA\_FFLAGS = -fallow-argument-mismatch
default:
%if %{bits} == 64
  sw_base_ptrsize = 64-bit
  sw_peak_ptrsize = 64-bit
  sw_base_ptrsize = 32-bit
  sw_peak_ptrsize = 32-bit
%endif
#----- Portability -----
default:
                    # data model applies to all benchmarks
%if %{bits} == 32
   # Strongly recommended because at run-time, operations using modern file
   # systems may fail spectacularly and frequently (or, worse, quietly and
   # randomly) if a program does not accommodate 64-bit metadata.
   EXTRA_PORTABILITY = -D_FILE_OFFSET_BITS=64
%else
   EXTRA_PORTABILITY = -DSPEC_LP64
%endif
# Benchmark-specific portability (ordered by last 2 digits of bmark number)
500.perlbench_r,600.perlbench_s: #lang='C'
%if %{bits} == 32
% define suffix IA32
%else
% define suffix X64
%endif
  PORTABILITY = -DSPEC_%{os}_%{suffix}
521.wrf_r,621.wrf_s: #lang='F,C'
  CPORTABILITY = -DSPEC_CASE_FLAG
  FPORTABILITY = -fconvert=big-endian
523.xalancbmk_r,623.xalancbmk_s: #lang='CXX'
  PORTABILITY = -DSPEC_%{os}
526.blender_r: #lang='CXX,C'
  PORTABILITY = -funsigned-char -DSPEC_LINUX
527.cam4_r,627.cam4_s: #lang='F,C'
  PORTABILITY = -DSPEC_CASE_FLAG
628.pop2_s: #lang='F,C'
  CPORTABILITY = -DSPEC_CASE_FLAG

FPORTABILITY = -fconvert=big-endian
```

```
#----- Tuning Flags common to Base and Peak -----
# Speed (OpenMP and Autopar allowed)
%if %{bits} == 32
  intspeed, fpspeed:
  # Many of the speed benchmarks (6nn.benchmark_s) do not fit in 32 bits
  # If you wish to run SPECint2017_speed or SPECfp2017_speed, please use
       runcpu --define bits=64
  fail_build = 1
%else
  intspeed, fpspeed:
     EXTRA_OPTIMIZE = -fopenmp -DSPEC_OPENMP
  fpspeed:
     \# 627.cam4 needs a big stack; the preENV will apply it to all
     # benchmarks in the set, as required by the rules.
     preENV OMP STACKSIZE = 120M
%endif
#----- Baseline Tuning Flags -----
\ensuremath{\text{\#}}\xspace EDIT if needed -- Older GCC might not support some of the optimization
                 switches here. See also 'About the -fno switches' below.
default=base:
                  # flags for all base
               = -g -03 -fno-unsafe-math-optimizations
  OPTIMIZE
intrate,intspeed=base: # flags for integer base
%if %{bits} == 32
   EXTRA_COPTIMIZE = -fgnu89-inline -fno-strict-aliasing
  EXTRA_COPTIMIZE = -fno-strict-aliasing
   LDCFLAGS
              = -z muldefs
%endif
# Notes about the above
# - 500.perlbench_r/600.perlbench_s needs -fno-strict-aliasing.
# - 502.gcc_r/602.gcc_s needs -fgnu89-inline or -z muldefs
# - For 'base', all benchmarks in a set must use the same options.
# - Therefore, all base benchmarks get the above. See:
       www.spec.org/cpu2017/Docs/runrules.html#BaseFlags
       www.spec.org/cpu2017/Docs/benchmarks/500.perlbench_r.html
       www.spec.org/cpu2017/Docs/benchmarks/502.gcc_r.html
#----- Peak Tuning Flags -----
default=peak:
  basepeak = yes # if you develop some peak tuning, remove this line.
  # About the -fno switches
  # -----
  # For 'base', this config file (conservatively) disables some optimizations.
  \# You might want to try turning some of them back on, by creating a 'peak'
  # section here, with individualized benchmark options:
          500.perlbench_r=peak:
            OPTIMIZE = this
          502.gcc_r=peak:
            OPTIMIZE = that
         503.bwaves_r=peak:
            OPTIMIZE = other .....(and so forth)
  # If you try it:
  # - You must remove the 'basepeak' option, above.
     - You will need time and patience, to diagnose and avoid any errors.
    - perlbench is unlikely to work with strict aliasing
```

```
# - Some floating point benchmarks may get wrong answers, depending on:
            the particular chip
            the version of GCC
            other optimizations enabled
   #
            -m32 vs. -m64
     - See: http://www.spec.org/cpu2017/Docs/config.html
      - and: http://www.spec.org/cpu2017/Docs/runrules.html
# Tester and System Descriptions - EDIT all sections below this point
# For info about any field, see
            https://www.spec.org/cpu2017/Docs/config.html#fieldname
# Example: https://www.spec.org/cpu2017/Docs/config.html#hw_memory
#----- EDIT to match your version -----
  sw\_compiler001 = C/C++/Fortran: Version 6.2.0 of GCC, the sw\_compiler002 = GNU Compiler Collection
#----- EDIT info about you -----
# To understand the difference between hw_vendor/sponsor/tester, see:
     https://www.spec.org/cpu2017/Docs/config.html#test_sponsor
intrate,intspeed,fprate,fpspeed: # Important: keep this line
   hw_vendor = My Corporation
   tester
                     = My Corporation
  test_sponsor = My Corporation
license_num = nnn (Your SPEC license number)
prepared_by = # Ima Pseudonym
# prepared_by
                                                              # Whatever you like: is never output
#----- EDIT system availability dates -----
intrate, intspeed, fprate, fpspeed: # Important: keep this line
                      # Example
                                                             # Brief info about field
   hw_avail
                     = # Nov-2099
                                                             # Date of LAST hardware component to ship
   sw_avail
                    = # Nov-2099
                                                             # Date of LAST software component to ship
#----- EDIT system information -----
intrate,intspeed,fprate,fpspeed: # Important: keep this line
# Example # Brief info
# hw_cpu_name = # Intel Xeon E9-9999 v9 # chip name
# Nominal ch
                                                             # Brief info about field
   hw_cpu_nominal_mhz = # 9999
                                                             # Nominal chip frequency, in MHz
hw_cpu_max_mhz = # 9999

hw_cpu_max_mhz = # 9999

# hw_disk = # 9 x 9 TB SATA III 9999 RPM

hw_model = # TurboBlaster 3000

# hw_nchips = # 99

hw_ncores = # 9999

hw_ncpuorder = # 1-9 chips
                                                             # Max chip frequency, in MHz
                                                            # Size, type, other perf-relevant info
                                                             # system model name
                                                             # number chips enabled
                                                             # number cores enabled
                                                             # Ordering options
   hw_nthreadspercore = # 9
                                                              # number threads enabled per core
                     = # TurboNUMA Router 10 Gb
                                                             # Other perf-relevant hw, or "None"
# hw_memory001 = # 999 GB (99 x 9 GB 2Rx4 PC4-2133P-R, # The 'PCn-etc' is from the JEDEC
# hw_memory002
                     = # running at 1600 MHz)
                                                              # label on the DIMM.
                     = # 99 KB I + 99 KB D on chip per core # Primary cache size, type, location
   hw pcache
                  = # 99 KB I+D on chip per 9 cores # Second cache or "None"
= # 9 MB I+D on chip per chip # Third cache or "None"
   hw scache
   hw_tcache
                     = # 9 GB I+D off chip per system board # Other cache or "None"
   hw ocache
  fw bios
                     = # American Megatrends 39030100 02/29/2016 # Firmware information
# sw_file
                     = # ext99
                                                             # File system
                     = # Linux Sailboat
                                                             # Operating system
# sw os001
# sw_os002
sw_other
                     = # Distribution 7.2 SP1
                                                             # and version
                     = # TurboHeap Library V8.1
                                                             # Other perf-relevant sw, or "None"
# sw_state
                    = # Run level 99
                                                             # Software state.
# Note: Some commented-out fields above are automatically set to preliminary
# values by sysinfo
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo
# Uncomment lines for which you already know a better answer than sysinfo
 HASH
620.omnetpp_s=base=mytest-m64:
```

```
# Last updated 2023-07-08 22:30:41
opthash=cd53771dd44803118c0a9c5bb08244ebe95f9dc282fce7663dca22b8a5475a89
baggage=
compiler version=\
@eNqVU8tu2zAQv0sreEsKh1IaGD4I0MEQHK0AURm22vomUBRFEeGrfNhuvr6UVDtqCgPNQSB2Zrg7\
u0uB/HDIq++r3f5L8bUqtmU40uibZZKC2jPuIJPAaoJtHOXFZrPKy2qd51mitEsMs/gInVIcd4hJ\
WIFPHBdziiFnOp+TAIyixRx6+SLVSY4MpNJDOptdc27KovqxW263q90HcsdxwllNzgQnFOPbpZLP\
j/FT/Jhwp+DJIK2JiUpkKHEpuHkpypVsGfWGNODEXJeCibHe/9XcBQsW80U0gNANFbKbBYJEG9Ky\
87TjgQ1MXxHaX9Yo5f7hkz/ER0eIgK9hGAEiEtWcQNuh3vo1dty+BRxJ6hElNsMPeDZ7aJVxBskg\
aJgdFawWvmk50n+D1r4DfnrUCOS6dzIkmWOvxExgOTi4RHVowIaifTprcBaWGb9NciIUnjs2tjZ0\
i2qWcb2YN1fA4C7rHwcTqG3wBXZekswo/EIcuMufN8v1vnoudlW53K1XZQaLJwAAFFiohvBMkIar \\ \\ \\ value fill for the control of the control
0110Hq7/qY3KzhDUqAFMqVaWna091zoMM0w+vGmAl0q7tpYpCRCnyqRjwqaqX1XUt3kkZiDHFwru\
w8/1CUS/AafTPzM=
compile_options=\
@eNq1UltrwjAUfu+v00RVoo6JD0UFe5l2q03RytxeSq2pZrZJ6cVdfv1iuzKFDbbhAiEn0Zfv5Duf \\ \label{eq:control}
IzhOqi2NWExBpAUTPFeVvMhYWPhZvTcs8w80Y9HrEF0hRZq5DJF2u9vtIkXRvcxVAXVkaidieXiA\
hRBxuAsYx2u5s00/tw1xzHj50pEPdVC/h0u+5+KZ1x685SXetlqAQ8Ci6aMtABsL19Tl4RimtpwA\
tn KWlHFQiKyTymND09M3aSdiQ20ZcG95U98xPW1p2YY5B8Bbw0QacMSFBM+DiMqPFzt8BEvYW1BB \\ \\
Qr1wJFLKk/QD3yeu6cxcaK622+8BDHJRZiEdIUVXQV+thpdioSlItFviekN0RgpSJN+6u7yxx50F\
9J0TVHk109GnfhPwV8aQQlSQ+NbMejRlnZ/QV+WYK28+9k8yvySz6rQ0dcncG2uWbXkPzYcqipFi\
W87dBdVVz/YXOvhWCAOxfqJhkY/qZH00CaCaoW1cUBP/PI2qXbI8vu1Ea++YFFAH
exehash=5e675225566d2f4862f28b1bde730d8b1463cc152f41fc30bf2a8696edfd9972
623.xalancbmk s=base=mvtest-m64:
# Last updated 2023-07-08 22:31:53
opthash=6707bd6120d695cd24b4c04344fe4dab05d2100c0242deaaf2068cfc0eb4177d
baggage=
compiler version=\
@eNqVU8tu2zAQvOsreEsKh1IaGD4I0MEQHKOAURm22vomUBRFEeGrfNhuvr6UVDtqCqPNQSB2Zrq7\
\verb"u0uB/HDIq++r3f5L8bUqtmU40uibZZKC2jPuIJPAaoJtH0XFZrPKy2qd51mitEsMs/gInVIcd4hJ" \\ |
WIfPHBdziiFn0p+TAIyixRx6+SLVSY4MpNJD0ptdc27KovqxW263q90HcsdxwllNzgQnF0PbpZLP\
j/FT/Jhwp+DJIK2JiUpkKHEpuHkpypVsGfWGNODEXJeCibHe/9XcBQsW80U0gNANFbKbBYJEG9Ky\
87Tiq01MXxHaX9Y05f7hkz/ER0eIqK9hGAEiEtWcONuh3vo1dtv+BRxJ6hElNsMPeDZ7aJVxBskq\
a \tt JgdFawWvmk50n+D1r4DfnrUCOS6dzIkmWOvxExgOTi4RHVowIaifTprcBawGb9NciIUnjs2tjZ0 \verb|\\|
i2qWcb2YN1fA4C7rHwcTqG3wBXZekswo/EicuMufN8v1vnoudlW53K1XZQaLJwAAFFiohvBMkIar\
w8/1CUS/AafTPzM=
compile_options=\
@eNrtVFtvmzAUfudXWLxWDtkW5SFqKjngpXSALS4T24tFHZJ6BTvikqb79XNgNMnUh2iKtJdZsjiX\
z5/sc75DoCQss+d8LYocqG0jlKxnRt1UgjesauVKVGyXV2L90jc/mIY2aw3R9mg8HpuGYR0fzoBp\
6aNWJWq+g41SBX/KhISPele76WTDYSFku7d0oAdNJ7CVz1K9yD4DN7KFm5sbADmAarjHSAHoRBTb\
+ hM4e \\ JEsgbYQpSwgLL4PMXIiHUiRhwLmBj72mR8tmUeQg0MA3ZHe+7ziec2PlrVS5blniXJbnITq\\ \\ Value (2.15) \\ Value (2.1
bH/itY0oLL/eeCpb6cdbrvTzUlWvf0LiKpM1Vz2GK7k7ALIik9wSkhftKtdXpSF5YKnvURRGhyu+\
BZLY9Qa3T0aDG6F0Yp84HwfbIf5gfkWe66CYdKc0HUsiPFQERxFa4qEoGwDJJwDXUuk01Nk6191v\
nuCh4gX4mXV1BwD8Lizz3CBJD+5abXNZboc4oTiw6RFGpxMAbmvV6orcmYY9A3aazg8li4G0LB4I\
/G3oUy+wjhGncYjYCe+7kuuIeui7dFqIpqFJv1zxp9Stiyfn0tG5VY8/ct7Ud+BkHWUNQKd6z7ni\
FP3TvnWPIclhfE9m9xfMTAxV
exehash=9cc718ed4047caeaad1119613d8a8841cc258323402290b33402034914b81384
631.deepsjeng s=base=mytest-m64:
# Last updated 2023-07-08 22:32:16
opthash=e12862ca16c376fcddb53131bf19f606eaf1cecc925d86e4cbd6cfe1d0549b27
baggage=
compiler_version=\
uOuB/HDIq++r3f5L8bUqtmU4OuibZZKC2jPuIJPAaoJtHOXFZrPKy2qd51mitEsMs/gInVIcd4hJ\
WIfPHBdziiFn0p+TAIyixRx6+SLVSY4MpNJD0ptdc27KovqxW263q90HcsdxwllNzgQnF0PbpZLP\
{\tt j/FT/Jhwp+DJIK2JiUpkKHEpuHkpypVsGfWGNODEXJeCibHe/9XcBQsW80U0gNANFbKbBYJEG9Ky\backslash PROMOTE CONTROL of the contr
87TjgQ1MXxHaX9Yo5f7hkz/EROeIgK9hGAEiEtWcQNuh3vo1dty+BRxJ6hElNsMPeDZ7aJVxBskg\
aJgdFawWvmk50n+D1r4DfnrUCOS6dzIkmWOvxExgOTi4RHVowIaifTprcBaWGb9NciIUnjs2tjZ0\
i2gWcb2YN1fA4C7rHwcTgG3wBXZekswo/EicuMufN8v1vnoudlW53K1XZOaLJwAAFFiohvBMkIar
w8/1CUS/AafTPzM=
compile_options=\
@eNq1Ut9rwjAQfs9fceRVoo6JD0UFWzvXrW2CVtC9lNq1NVMT6Q8399cvtutU2GADFwi5506+3H3f\
uVKQbbCOYr6JQ05yLkWmoSxPeZj7aSGeeervo5THhz6+wUiZmQpRdrPdbm0ED0owDXBLpbZSnoV7 \\ \\ \\ vCMPRdrPdbm0ED0owDXBLpbZSnoV7 \\ \\ vCMPRdrPdbm0ED0owDXBLpb
MnRr7DumQycLAJIAobdAYiEVXhbEkeolX5Fj/pa/ByUKVIvEcheJ7e4T0qfMdB0G9dVm3Q5AL5NF\
{\tt GkYDjAwNjPm8f63GakCqP1Dm9fFFnxgpCg02u70H46nyXfZcenXTNe79r4ATCRhRDRSk5VhPpnL9} \\ \\ {\tt CkYDjAwNjPm8f63GakCqP1Dm9fFFnxgpCg02u70H46nyXfZcenXTNe79r4ATCRhRDRSk5VhPpnL9} \\ \\ {\tt CkYDjAwNjPm8f63GakCqP1Dm9fFFnxgpCg02u70H46nyXfZcenXTNe79r4ATCRhRDRSk5VhPpnL9} \\ \\ {\tt CkYDjAwNjPm8f63GakCqP1Dm9fFFnxgpCg02u70H46nyXfZcenXTNe79r4ATCRhRDRSk5VhPpnL9} \\ \\ {\tt CkYDjAwNjPm8f63GakCqP1Dm9fFFnxgpCg02u70H46nyXfZcenXTNe79r4ATCRhRDRSk5VhPpnL9} \\ {\tt CkYDjAwNjPm8f63GakCqP1Dm9f64GakCquarter } \\ {\tt CkYDjAwNjPm8f63GakCquarter 
\verb|hpEyx5x7k6F/lvktP+XnVSijE2+oW7blLeoaS9Ywsi338YozUMn1B2l/1LYnly9RmGcD0Fsn3gFK|| \\
WezRFWX+ZzXKcuns0EFn4/MBvk0vsQ==
exehash=e623b0e9ac4981429a50052f5875923a2f64ac5cd7af17be7a3535416200ac73
641.leela s=base=mvtest-m64:
```

```
# Last updated 2023-07-08 22:32:24
opthash=b46944a246a92b74fb0ca92bf4076745d9c11ef6a145bcc9e26ef6be0757c934
baggage=
compiler version=\
@e \texttt{NqVU8tu2zAQv0sreEsKh1IaGD4I0MEQHK0AURm22vomUBRFEeGrfNhuvr6UVDtqCgPNQSB2Zrg7} \\ \\
WIFPHBdziiFnOp+TAIyixRx6+SLVSY4MpNJDOptdc27KovqxW263q90HcsdxwllNzgQnFOPbpZLP\
j/FT/Jhwp+DJIK2JiUpkKHEpuHkpypVsGfWGNODEXJeCibHe/9XcBQsW8OUQqNANFbKbBYJEG9Ky\
87 \\ \text{TjgQ1MXxHaX9Yo5f7hkz/ER0eIgK9hGAEiEtWcQNuh3vo1dty+BRxJ6hElNsMPeDZ7aJVxBskg} \\ \backslash \\
aJgdFawWvmk50n+D1r4DfnrUCOS6dzIkmWOvxExgOTi4RHVowIaifTprcBaWGb9NciIUnjs2tjZ0\
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Xd8K2+vy91bbDZybeHQ4Mh+qguZAwPSW3ndXHPcn5PQx7rdoZcX0CPMtSx0vSf7fMjdN0PV9S2df \\ \\ \\ Xd8K2+vy91bbDZybeHQ4Mh+qguZAwPSW3ndXHPcn5PQx7rdoZcX0CPMtSx0vSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDZybeHQ4Mh+qquZAwPSW3ndXHPcn5PQx7rdoZcX0CPMtSx0vSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDZybeHQ4Mh+qquZAwPSW3ndQx0VSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDQx0VSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDQx0VSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDQx0VSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDQx0VSf7fMjdN0PV9S2df \\ Xd8K2+vy91bbDQx0VSf7fMjdN0PV9S2df \\ \\ Xd8K2+vy91bbDQx0VSf7fMjdN0PV9S2df \\ Xd8K2+vy91bbDQx0VSf7f
\verb"oMEZRKVIsj3fi+7G0vYcq4rvBV+PNT8APo0qFZzkfY++q5nf07aX7P5Hhht+1cWV1a9x0HQSAH2b \verb"\"oMEZRKVIsj3fi+7G0vYcq4rvBV+PNT8APo0qFZzkfY++q5nf07aX7P5Hhht+1cWV1a9x0HQSAH2b \verb|\"oMEZRKVIsj3fi+7G0vYcq4rvBV+PNT8APo0qFZzkfY++q5nf07aX7P5Hhht+1cWV1a9x0HQSAH2b \verb|\"oMEZRKVIsj3fi+7G0vYcq4rvBV+PNT8APo0qFZzkfY++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hhht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07aX7P5Hht+1cWV1a9x0Hz++q5nf07
 +Ytj9TDqkh8IPlz7LzvmX/aCuP7Sim5i37M7iGX1UhG07sZuMnNSUqWkSkmVkiolVUqqlFQpqVJS\
paT+f5L6E1W2FXw=
exehash=b4e3e5e6896cc97081016ca6089e1e33bf3170da252e5803c5829339501721c7
648.exchange2 s=base=mvtest-m64:
# Last updated 2023-07-18 01:39:10
opthash = 7c1887ab5d4ad6808ab5178847c9ae83591305d2ffdc80448597038f9f969cc0
baggage=
compiler version=\
@eNqVU1tr2zAYffev0Fs7WtldCXkw+CGYJAzCHBKv65uRZVkW1W26JFl//WS7Sb20wPpqxHf0kb7z\
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2VHBauGbliP9N2jtB+CXR41ArvsgQ5I59krMBJaDg3NUhwJsSNo/Zw3OwjDj9050hMJzx8bShmpR\
\verb|ztku57PmAhjcZf1yMIHaBp9h5yXJjMIvxIGbfLVZrPfVqthV5WK3XpYZLB4BAFBgoRrCM0Earo43|| \\
Qfn8/J/aqOwMQQ9YwBRoZdkp2nutQzND58NOA6xEmLG1TEmAOFUmGBM2Bf2oor7MAzEDOW4ouA1/\label{eq:control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_control_contro
2BcQ/QFNeEEs
compile options=\
ieA49rc0YBFFIkmZ4MrUVCoZSZcy42smlzsqWfDR1u90DVIFEsiNer2ua9rQ80ykq4SSJEF4MPOs\
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giPGs/cafDiKmg2c8S0Xe37cwSHPcBgImUqfI0xyS+UgDYFwiLB7j3DABRxTfkBh30kG54qYHfxC\
\verb"B9t+FIk99mWYxZSn0GYKZGQD0yKhPE70U4K2TTTst2/a9rGo23uEccKQqh6uIFFhcI3bCr3hqepP\\ \\
/ \texttt{i+TPs3k78gde/J0a+RFXMe5jMsGEGqJ1RslqeqgSnyhQKjg5AxuDf9fDH/nUvTsLvKbVblWn7AV} \\
 exehash=26d4d074ac3579f189eb8dcfc435cceac2e3cae18f7097938f69077db5b8dc08
657.xz s=base=mvtest-m64:
# Last updated 2023-07-18 01:39:16
opthash=832e6a83d62cbde49f8d2f71b7af20ff79739e272caa927f941a88038c243223
baggage=
compiler version=\
@eNqVU02PmzAQvfMrfNutdg3bVZQDEocIZaNKUYkIbfeGjDFgrbFdfyTp/vo00GTpVpG6B2TNe88z\
b \verb|2YMStPy+zrff8m+| tmug \verb|CMOv|| ku \verb|W1R5LhzmElnNqA2DNNtu12| lRbt \verb|10iZR2keGWHrBTStCOcIkr|| tmug \verb|CMOv|| tmug \verb|CMO
 +MxhuWqpFlz6UwTAJFousJcvUh3lxOBWetxSesm5LbLvR77a7db5B3KHYSR4xU6MRpDseqno80P4\
GD5Ewil8NERrZoKCmJa5GF29FKRKNrz1htXoyF0Xo5mxwf/F3BkDC/R8B2HsxgrJ1QIg0YY1/DTv\
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 T4ReLuoLYGiXDI+D96Sp6Rl2XrLEKPrCHLpJn7arzb58yvKyWOWbdZHg7BEhhHvaq5qJpGe1UMcb\
\verb"UD4//6c2KDrDSI1GMEZaWX4K9l5rGCZMHt40oqqHHVvLlUREtMqAsd7GaFhVMLR5YGYkpxeKbuHn" \verb"ND4//6c2KDrDSI1GMEZaWX4K9l5rGCZMHt40oqqHHVvLlUREtMqAsd7GaFhVMLR5YGYkpxeKbuHn" \verb"ND4//6c2KDTGTM" \verb"ND4//6c2KDTM" "ND4//6c2KDTM" "
+oSC34wMPvM=
compile_options=\
@eNrlvN9vmzAQfuevsHh3frRZtkalUkJowkYwSsnU9gU5xhAvYCMMaZu/fgaWlCh7WLXlaZaM/XHf \land and an algorithms and algorithms and algorithms and algorithms are algorithms are algorithms. The algorithms are algorithms are algorithms are algorithms are algorithms are algorithms and algorithms are algorithms are algorithms. The algorithms are algorithms are algorithms are algorithms are algorithms are algorithms are algorithms. The algorithms are algorithms are algorithms are algorithms are algorithms are algorithms are algorithms. The algorithms are algorithms. The algorithms are algorithms. The algorithms are a
nbi773AFhyne0oglFIisYILLkSaLnJEiyEsesjzY0ZxFb4be1zV1lYqi7p1er6drmokW3gjoXeXa\
zZkk01qIkZANZhvu1c53w0FMYMJ4+dpVLxrScABLvuXihTcWGPMSxoSAakFZhAa5u0G0ACq0H9UR
AE4fPMtUhzu1JqvZLxyMVz4KJk++hZZTa2n0XvtX14NPw89fFGE+/m4FJnLv7VkwN/oHl4W1CGx0\
{\tt QI/PANodtwVGSZDSNGCiQhsMr9RJRJoKri4JWyf7FHdxxlqoerTgGZtsKNm2sGRppmr9/iKkSYFP\backslash Approximately and the control of the contro
ArZAjnlMiQhprgoTA4iuAYy4U0WT0KKqdcUGVhVK2R7XdQLHpYw52RhVB1iKoxDAF+UrMsrT7JA6\
8ix34TUhm6ZDnDAsGY+rCA3J8YYDAG6lKHNC73TNHAHTNC7S8vY4mnxFnm/oJwL0NSU001vd0+PZ\
g7 K diq G2 Tiz Xn Acnh P9 FHbq GR kAVz V7 Yz 5 ZK/k+k Uvt Yj/5 yH JiHop 1 rps Vqx f+t jto B29 xz cd Xt \\ \\ \\
```

```
TggKfuzuMee/lMu/FEKdPlpV496a9Z+Ftxo0
exehash=59a18538d41528025f2e41abe74e64712fd5aa895917d9144b9a68c22a5bf044
998.specrand_is=base=mytest-m64:
# Last updated 2023-07-18 01:39:18
opthash=ccf9befe890849146fcf9e59afcff8dcf06e62327c9c865cf0bcd3deb9cb9963
baggage=
compiler_version=\
@e NqVU02PmzAQvfMrfNutdg3bVZQDEocIZaNKUYkIbfeGjDFgrbFdfyTp/vo00GTpVpG6B2TNe88z \\ \\
+MxhuWqpFlz6UwTAJFousJcvUh3lxOBWetxSesm5LbLyR77a7db5B3KHYSR4xU6MRpDseqno80P4\
Tgpe9b5uBNF/g9a+A356UvfEde9kRHLHX5mZwXJ0cI4qaMBC0SGdNTSBZYZvk5wJey8cn1obuyUV\
T4ReLuoLYGiXDI+D96Sp6Rl2XrLEKPrCHLpJn7arzb58vvKvWOWbdZHg7BEhhHvag5gJpGe1UMcb\
+oSC34wMPvM=
compile options=\
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fd99jpTEKn3guSg5qF0tlDQzx9RasDrRjcyETvZci/ynS86JY5fGQuz6bDweE8fx6CqeARnZ0pEW\
hu2xVqpk21RIvLef3k8nBcNSy0bHyG70o0kEG/kg1aPsM1jIBgvGoA00deayy0tABqg0lzpTgMu7\
{\tt 2PfsL1r6i80NYGB2n0lUZshUVSlpawtAegGYS2VPMGn0bXf1FluSSjylHRUcwyY127rtJUWV5hng} \\ \\ {\tt 1000} \\ {
o61V0y6r3fNxCY39aBX3lL0umJYiNUIWLUMPCuPpB0DKqEYzfk0cbwae555Elp6cLj7Re02SFxoR\
x87CizcfwvnNnc291KvLLvzI+5gcAH8KSBw6A8sbrIJvvs2/Rc2uxv+yvp0n3oH3tawD1ID/r1IP\
CYfY1/p3HfXImN6u54sgDNZfD413QyF0GESfT+VQwCeomjLjeWeqd7nvPT67Uvff0avNNQzi99wB\
OlOEy1M5jrbkx+kee/5Hu/xPI3Tt0037IgbP4Rf61IrB
exehash=93f5135b6b03cb284ff2d2b28bef20e6b7d44022e932f83640ff5b51f288c920
```

• 注意:上面的文件基本复制就能用,但是要修改一下你的工具链路径,找到下述内容,并把/opt/riscv-toolchain-bin-rv64gc-linux路径改成你的工具链路径。

- EXTRA\_CFLAGS = -march=rv64imafd 加入了这个编译选项,因为我们的程序不支持如compress等指令。
- 2. 在已经安装好的SPEC根目录下(path/to/SPEC),进入config文件夹,新建一个user.cfg文件,然后将第1步中的cfg文件内容 复制进去。

```
cd yourpath/to/SPEC
cd config
touch user.cfg
```

3. 进入bin目录,并运行runcpu命令:

```
cd ../bin
./runcpu --config=user.cfg SPECspeed2017_int_base
#如果想要编译所有的基准测试集,运行:
./runcpu --config=user.cfg all
```

4. 由于runcpu命令会在编译build出benchmark二进制程序接着运行它,而我由于交叉编译出的RISC-v的benchmark二进制程序没有办法在x86上运行,故最后肯定会报错,但是不用管,我们只要二进制程序,编译好的二进制程序就放在yourpath/to/SPEC/benchspec/CPU/XXX/exe中,这个XXX代表的是某一个benchmark,由于刚才跑的是SPECspeed2017\_int\_base,所以可以查看这个yourpath/to/SPEC/benchspec/CPU/600.perlbench\_s/exe中有没有出现一个二进制程序,有的话就成功了。

## 2.适配ARM的CFG,一样需要修改gcc工具链路径,其余构建binary步骤与之前步骤相同:

```
# SPEC CPU2017 config file for: qcc / q++ / qfortran on Linux ARM systems
# Usage: (1) Copy this to a new name
          cd $SPEC/config
#
             cp Example-x.cfg myname.cfg
        (2) Change items that are marked 'EDIT' (search for it)
#
# SPEC tested this config file with:
# Compiler version(s): 5.3, 6.2
# Operating system(s): Ubuntu 16.04
# Hardware: Cavium Thunde
                           Cavium ThunderX, HPE Moonshot
# If your system differs, this config file might not work.
{\tt \#\ You\ might\ find\ a\ better\ config\ file\ at\ http://www.spec.org/cpu2017/results}
# Known Limitations
    It is possible that you might encounter compile time or run time errors
#
     with older versions of GCC (for example, 4.x)
      Recommendation: Use a newer version of the compiler.
                    If that is not possible, try reducing the optimization.
# Compiler issues: Contact your compiler vendor, not SPEC.
# For SPEC help: http://www.spec.org/cpu2017/Docs/techsupport.html
# Arbitrary string to tag binaries
            Two Suggestions: # (1) EDIT this label as you try new ideas.
%define label mytest
                                # (2) Use a label meaningful to *you*.
#----- Preprocessor -----
%ifndef %{build_ncpus}  # EDIT to adjust number of simultaneous compiles.
% define build_ncpus 8  # Or, you can set it on the command line:
%endif  # 'runcpu --define build_ncpus=nn'
# Don't change this part.
%define os
                 LINUX
%if %{bits} == 64
% define model
                       -mabi=ln64
%elif %{bits} == 32
% define model
                      -mabi=ilp32
% error Please define number of bits - see instructions in config file
%endif
#----- Global Settings -----
# For info, see:
            https://www.spec.org/cpu2017/Docs/config.html#fieldname
# Example: https://www.spec.org/cpu2017/Docs/config.html#tune
command\_add\_redirect = 1
flagsurl = $[top]/config/flags/gcc.xml
ignore_errors
                   = 1
ignore_errors = 1
iterations = 1
label = %{label}-%{bits}
line_width = 1020
log_line_width = 1020
makeflags = --jobs=%{build_ncpus}
mean_anyway = 1
```

```
output_format = txt,html,cfg,pdf,csv
preenv
                   = 1
reportable
                   = 0
tune
                    = base
#----- How Many CPUs? -----
# Both SPECrate and SPECspeed can test multiple chips / cores / hw threads
   For SPECrate, you set the number of copies.For SPECspeed, you set the number of threads.
# See: https://www.spec.org/cpu2017/Docs/system-requirements.html#MultipleCPUs
    q. How many should I set?
    a. Unknown, you will have to try it and see!
# To get you started, some suggestions:
     copies - This config file defaults to testing only 1 copy. You might
              try changing it to match the number of cores on your system,
#
              or perhaps the number of virtual CPUs as reported by:
                     grep -c processor /proc/cpuinfo
#
              Be sure you have enough memory. See:
              https://www.spec.org/cpu2017/Docs/system-requirements.html#memory
     threads - This config file sets a starting point. You could try raising
               it. A higher thread count is much more likely to be useful for
               fpspeed than for intspeed.
#
intrate, fprate:
  copies
                  = 1 # EDIT to change number of copies (see above)
intspeed, fpspeed:
                   = 8 # EDIT to change number of OpenMP threads (see above)
  threads
#----- Compilers ------
default:
                   EDIT: the directory where your compiler is installed
%ifndef %{gcc_dir}
                     /work/stu/wtxiao/WorkSpace/all-toolchain/toolchain-arm/gcc-arm-10.2-2020.11-x86_64-aarch64-none-linux-gnu
% define qcc dir
%endif
\ensuremath{\text{\#}}\xspace EDIT if needed: the preENV line adds library directories to the runtime
      path. You can adjust it, or add lines for other environment variables.
       See: https://www.spec.org/cpu2017/Docs/config.html#preenv
      and: https://gcc.gnu.org/onlinedocs/gcc/Environment-Variables.html
  preENV_LD_LIBRARY_PATH = %{gcc_dir}/lib64/:%{gcc_dir}/lib/:/lib64
  #preENV_LD_LIBRARY_PATH = %{gcc_dir}/lib64/:%{gcc_dir}/lib/:/lib64:%{ENV_LD_LIBRARY_PATH}
                        = %{gcc_dir}/bin/
  SPECLANG
                         = $(SPECLANG)aarch64-none-linux-gnu-gcc -std=c99 %{model}
= $(SPECLANG)aarch64-none-linux-gnu-g++ %{model}
  CC
  CXX
                         = $(SPECLANG)aarch64-none-linux-gnu-gfortran
                                                                                %{model}
   # How to say "Show me your version, please"
  CC_VERSION_OPTION = -v
   CXX_VERSION_OPTION
                         = -v
   FC_VERSION_OPTION
                         = -v
  EXTRA_FFLAGS = -fallow-argument-mismatch
default:
%if %{bits} == 64
  sw_base_ptrsize = 64-bit
   sw_peak_ptrsize = 64-bit
   sw_base_ptrsize = 32-bit
  sw_peak_ptrsize = 32-bit
%endif
#----- Portability ------
default: # data model applies to all benchmarks
%if %{bits} == 32
   # Strongly recommended because at run-time, operations using modern file
   # systems may fail spectacularly and frequently (or, worse, quietly and
    # randomly) if a program does not accommodate 64-bit metadata.
    EXTRA_PORTABILITY = -D_FILE_OFFSET_BITS=64
```

```
%else
   EXTRA_PORTABILITY = -DSPEC_LP64
%endif
# Benchmark-specific portability (ordered by last 2 digits of bmark number)
500.perlbench_r,600.perlbench_s: #lang='C'
%if %{bits} == 32
% define suffix AARCH32
%else
% define suffix AARCH64
%endif
  PORTABILITY = -DSPEC_%{os}_%{suffix}
521.wrf_r,621.wrf_s: #lang='F,C'
  CPORTABILITY = -DSPEC_CASE_FLAG
  FPORTABILITY = -fconvert=big-endian
523.xalancbmk_r,623.xalancbmk_s: #lang='CXX'
  PORTABILITY = -DSPEC_%{os}
526.blender_r: #lang='CXX,C'
  PORTABILITY = -funsigned-char -DSPEC_LINUX
527.cam4 r,627.cam4 s: #lang='F,C'
  PORTABILITY = -DSPEC_CASE_FLAG
628.pop2_s: #lang='F,C'
  PORTABILITY = -DSPEC_CASE_FLAG -fconvert=big-endian
#----- Tuning Flags common to Base and Peak -----
# Speed (OpenMP and Autopar allowed)
%if %{bits} == 32
  intspeed, fpspeed:
  # Many of the speed benchmarks (6nn.benchmark_s) do not fit in 32 bits
  # If you wish to run SPECint2017_speed or SPECfp2017_speed, please use
        runcpu --define bits=64
  fail_build = 1
%else
  intspeed, fpspeed:
    EXTRA_OPTIMIZE = -fopenmp -DSPEC_OPENMP
  fpspeed:
     \# 627.cam4 needs a big stack; the preENV will apply it to all
     # benchmarks in the set, as required by the rules.
     preENV_OMP_STACKSIZE = 120M
%endif
#----- Baseline Tuning Flags ------
default=base:
                   # flags for all base
  OPTIMIZE
                 = -03
intrate,intspeed=base: # flags for integer base
%if %{bits} == 32
   EXTRA_COPTIMIZE = -fgnu89-inline -fno-strict-aliasing
%else
   EXTRA_COPTIMIZE = -fno-strict-aliasing
                 = -z muldefs
%endif
# Notes about the above
\# - 500.perlbench_r/600.perlbench_s needs -fno-strict-aliasing.
# - 502.gcc_r/602.gcc_s
                                needs -fgnu89-inline or -z muldefs
# - For 'base', all benchmarks in a set must use the same options.
# - Therefore, all base benchmarks get the above. See:
       www.spec.org/cpu2017/Docs/runrules.html#BaseFlags
```

```
www.spec.org/cpu2017/Docs/benchmarks/500.perlbench_r.html
       www.spec.org/cpu2017/Docs/benchmarks/502.gcc_r.html
#----- Peak Tuning Flags -----
default=peak:
  basepeak = yes # if you develop some peak tuning, remove this line.
  # -----
  # About the -fno switches
  # For 'base', this config file (conservatively) disables some optimizations.
  \mbox{\#} You might want to try turning some of them back on, by creating a 'peak'
  # section here, with individualized benchmark options:
          500.perlbench_r=peak:
  #
            OPTIMIZE = this
          502.gcc_r=peak:
  #
            OPTIMIZE = that
  #
          503.bwaves_r=peak:
            OPTIMIZE = other .....(and so forth)
  # If you try it:
     - You must remove the 'basepeak' option, above.
  # - You will need time and patience, to diagnose and avoid any errors.
     - perlbench is unlikely to work with strict aliasing
  #
     - Some floating point benchmarks may get wrong answers, depending on:
          the particular chip
           the version of \ensuremath{\mathsf{GCC}}
  #
          other optimizations enabled
          -m32 vs. -m64
  # - See: http://www.spec.org/cpu2017/Docs/config.html
  # - and: http://www.spec.org/cpu2017/Docs/runrules.html
#-----
# Tester and System Descriptions - EDIT all sections below this point
# For info about any field, see
           https://www.spec.org/cpu2017/Docs/config.html#fieldname
{\tt \# Example: https://www.spec.org/cpu2017/Docs/config.html\#hw\_memory}
#---- EDIT to match your version -----
default:
  sw_compiler001 = C/C++/Fortran: Version 6.2.0 of GCC, the
  sw_compiler002 = GNU Compiler Collection
#----- EDIT info about you
# To understand the difference between hw_vendor/sponsor/tester, see:
    https://www.spec.org/cpu2017/Docs/config.html#test_sponsor
intrate,intspeed,fprate,fpspeed: # Important: keep this line
  hw_vendor = My Corporation
  tester
                  = My Corporation
  test_sponsor = My Corporation
  license_num = nnn (Your SPEC license number)
prepared_by = # Ima Pseudonym
# prepared_by
                                                       # Whatever you like: is never output
#----- EDIT system availability dates -----
intrate,intspeed,fprate,fpspeed: # Important: keep this line
                   # Example
                                                       # Brief info about field
  hw_avail
                   = # Nov-2099
                                                        # Date of LAST hardware component to ship
  sw_avail
                   = # Nov-2099
                                                       # Date of LAST software component to ship
#----- EDIT system information ------
intrate,intspeed,fprate,fpspeed: # Important: keep this line
                   # Example
                                                       # Brief info about field
# hw cpu name
                  = # Intel Xeon E9-9999 v9
                                                       # chip name
  hw_cpu_nominal_mhz = # 9999
                                                       # Nominal chip frequency, in MHz
  hw_cpu_max_mhz = # 9999
                                                       # Max chip frequency, in MHz
# hw_disk = # 9 x 9 TB SATA III 9999 RPM
hw_model = # TurboBlaster 3000
# hw_nchips = # 99
                  = # 9 x 9 TB SATA III 9999 RPM
                                                       # Size, type, other perf-relevant info
                                                       # system model name
                                                        # number chips enabled
```

```
hw ncores
                                        = # 9999
                                                                                                                       # number cores enabled
      hw_ncpuorder
                                        = # 1-9 chips
                                                                                                                       # Ordering options
      hw_nthreadspercore = # 9
                                                                                                                       # number threads enabled per core
     hw other
                                                                                                                       # Other perf-relevant hw, or "None"
                                         = # TurboNUMA Router 10 Gb
\# hw_memory001 = \# 999 GB (99 x 9 GB 2Rx4 PC4-2133P-R, \# The 'PCn-etc' is from the JEDEC
# hw_memory002
                                        = # running at 1600 MHz)
                                                                                                                       # label on the DIMM.
      hw pcache
                                         = # 99 KB I + 99 KB D on chip per core # Primary cache size, type, location
                                        = # 99 KB I+D on chip per 9 cores
= # 9 MB I+D on chip per chip
      hw scache
                                                                                                                       # Second cache or "None"
     hw_tcache
                                                                                                                       # Third cache or "None"
                                        = # 9 GB I+D off chip per system board # Other cache or "None"
     hw ocache
    fw bios
                                         = # American Megatrends 39030100 02/29/2016 # Firmware information
  # sw_file
                                          = # ext99
                                                                                                                       # File system
  # sw_os001
                                         = # Linux Sailboat
                                                                                                                      # Operating system
  # sw_os002
                                         = # Distribution 7.2 SP1
                                                                                                                       # and version
                                          = # TurboHeap Library V8.1
                                                                                                                      # Other perf-relevant sw, or "None"
      sw_other
  # sw state
                                         = # Run level 99
                                                                                                                       # Software state.
# Note: Some commented-out fields above are automatically set to preliminary
# values by sysinfo
             https://www.spec.org/cpu2017/Docs/config.html#sysinfo
# Uncomment lines for which you already know a better answer than sysinfo
508.namd r=base=mytest-64:
# Last updated 2023-07-19 14:29:45
opthash=c1941f97f0b998589fab8378ccb395d2df3caca12fd8f966d475ecca03ffc129
baggage=
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\verb|L3PXnFVZ0/ash6gu+C+JUME9IPGcifNqy3+4XTWzZ8F0GBE8DYam4zrBS41ZEQwV1/GebkNup/hC||
```

```
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# Last updated 2023-07-19 14:31:46
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## 3.项目中构建出的基准测试集的位置

在SPEC项目目录中的SPEC\_BENCHMARK目录下,分别有x86\_64,arm64,riscv64的基准测试集