SLR Parser Generator

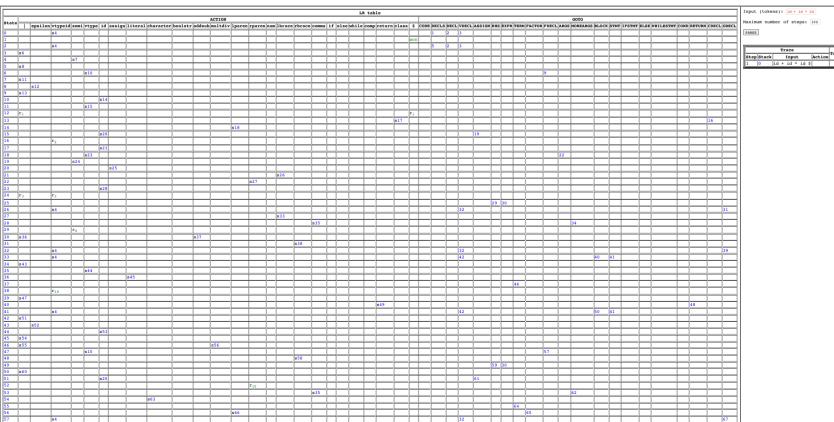
(0) (1) (2) (2) (3) (4) (5) (6) (7) (8) (9) (10) (11) (12) (13) (14) (15) (16) (17) (18) (19)	MEMORY (" '15 f); BECLE > SELL DELS spallon BECLE > SELL DELS spallon BECLE > SELL DELS spallon BECLE > SELL DELS Sellon ASSOCI > VINEL DESC. CENT. ASSOCI > VINEL SELL DELS Sellon seni ASSOCI > 14 saisje NBB *** *** *** *** *** *** ***

	FIRST	/ FOLLOW table
Nonterminal	FIRST	FOLLOW
CODE	{vtypeid}	(\$)
DECLS	{vtypeid}	(\$,)
DECL	{vtypeid}	{vtypeid}
VDECL	{vtypeid}	{ ,vtypeid}
ASSIGN	{id}	(semi)
RHS	()	(semi)
EXPR	()	{ ,addsub,rparen,comp}
TERM	{}	{ ,addsub,multdiv,rparen,comp
FACTOR	{lparen}	{ ,addsub,multdiv,rparen,comp
FDECL	{vtype}	{ ,vtypeid}
ARGS	{vtype}	{rparen}
MOREARGS	{comma}	()
BLOCK	{vtypeid}	{return, ,rbrace}
STMT	(vtypeid)	(vtypeid)
IFSTMT	(if)	(1)
ELSE	{else}	()
WHILESTMT	{while}	(vtypeid)
COND	{}	{rparen}
RETURN	{return}	{rbrace}
CDECL	(class)	(vtypeid)
ODECL	{vtypeid}	{rbrace, }

Goto	Kernel	State	Closure
	(CODE -> .DECLS)		(CODE -> .DECLS; DECLS -> .DECL DECLS epsilon; DECL -> .VDECL FDECL CDECL; VDECL -> .vtypeid semi vtype ASSIGN semi)
	{CODE -> DECLS.}	1	(CODE -> DECLS.)
oto(0, DECL)	{DECLS -> DECL.DECLS epsilon}	2	(DECLS -> DECL.DECLS epsilon; DECLS -> .DECL DECLS epsilon; DECLS -> .DECL DECLS epsilon; DECL -> .VDECL FDECL CDECL; VDECL -> .vtypeid semi vtype ASSIGN semi)
to(0, VDECL)	[DECL -> VDECL. FDECL CDECL)	3	{DECL -> VDECL. FDECL CDECL}
to(0, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	{VDECL -> vtypeid.semi vtype ASSIGN semi}
to(2, DECLS)	{DECLS -> DECL DECLS. epsilon}	5	(DECLS -> DECL DECLS. epsilon)
	{DECLS -> DECL.DECLS epsilon}	2	
	[DECL -> VDECL. FDECL CDECL)	3	
oto(2, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi} {DECL -> VDECL .FDECL CDECL}	4	(DECL -> VDECL FDECL CDECL; FDECL -> .vtype id lparen ARGS rparen lbrace BLOCK RETURN rbrace)
oto(4, semi) oto(5,)	[VDECL -> vtypeid semi. vtype ASSIGN semi] [DECLS -> DECL DECLS .epsilon]	0	(VDECL > vtypeid semi. vtype ASSIGN semi) (DECLS > DECL DECLS Deption)
	(DECL -> VDECL FDECL. CDECL)	-	(SECL -> OBCE FEECL CRECL)
	[FDECL -> vtype.id lparen ARGS rparen lbrace BLOCK RETURN rbrace]		(FURCI -> vtype.id lparen ARGS rparen lbrace BLOCK RETURN rbrace)
oto(7,)	{VDECL -> vtypeid semi .vtype ASSIGN semi}		(VDECL -> vtypeid semi .vtype ASSIGN semi)
oto(8, epsilon)	{DECLS -> DECL DECLS epsilon.}	12	{DECLS >> DECL DECLS epsilon.}
oto(9,)	{DECL -> VDECL FDECL .CDECL}	13	{DECL -> VDECL FDECL .CDECL; CDECL -> .class id lbrace ODECL rbrace}
oto(10, id)	(FDECL -> vtype id.lparen ARGS rparen lbrace BLOCK RETURN rbrace)	14	(FDECL -> vtype id.lparen ARGS rparen lbrace BLOCK RETURN rbrace)
oto(11, vtype)	[VDECL -> vtypeid semi vtype.ASSIGN semi]	15	(VDECL -> vtypeid semi vtype.ASSIGN semi; ASSIGN -> .id assign RHS)
oto(13, CDECL)	{DECL -> VDECL FDECL CDECL.}	16	{DECL -> VDECL FDECL CDECL.}
oto(13, class)	{CDECL -> class.id lbrace ODECL rbrace}	17	(CDECL -> class.id lbrace ODECL rbrace)
oto(14, lparen)	{FDECL -> vtype id lparen.ARGS rparen lbrace BLOCK RETURN rbrace}	18	(FDECL -> vtype id lparen.ARGS rparen lbrace BLOCK RETURN rbrace; ARGS -> .vtype id MOREARGS epsilon)
oto(15, ASSIGN)	{VDECL -> vtypeid semi vtype ASSIGN.semi}	19	{VDECL -> vtypeid semi vtype ASSIGN.semi}
oto(15, id)	{ASSIGN -> id.assign RHS} {CDECL -> class id.lbrace ODECL rbrace}	20	(ASSIGN > id.assign RBS) (CDECL > classign RBS)
oto(18, ARGS)	{FDECL -> vtype id lparen ARGS.rparen lbrace BLOCK RETURN rbrace}	22	{FDECL -> vtype id Daren ARGS.paren brace BLOCK RETURN rbrace}
oto(18, vtype)	{ARGS -> vtype.id MOREARGS epsilon} {VDECL -> vtypeid semi vtype ASSIGN semi.}	24	(ARGS > vtyps.id MOREARGS opsilon (VDCCL > vtyps.id semi vtyps.hSIGN semi.)
	(ASSIGN -> id assign.RHS)		(VEGLU- ~ VLYPETA SEMIL VLYPE ASSAM SEMIL) (ASSIGN ~ SEMIL VLYPE ASSAM SEMIL) (ASSIGN ~ SEMIL VLYPE ASSAM SEMIL)
oto(21, lbrace)	{CDECL -> class id lbrace.ODECL rbrace}	26	[Residum > 10 assignimens name > .arm 110:sa Unstatute DOUISLI; Arm > .arm adusum lenn 1.mm/ [CDECL > class id ibrace ODECL - brace ODECL > .PubECL ODECL Specio, 190EL 05. desino, 190EL > .vtypeid semi vtype ASSIGN semi)
oto(22, rparen)	[CDECL -> class id lbrace.ODECL rbrace] [FDECL -> vtype id lparen ARGS rparen.lbrace BLOCK RETURN rbrace]	27	(FDECL -> type id lparen ARGS rparen.lbrace BLOCK RETURN thrace)
to(23, id)	(ARGS -> vtype id.MOREARGS epsilon)	28	[ARGS -> vtype id.MOREARGS epsilon; MOREARGS -> .comma vtype id MOREARGS epsilon}
oto(25, RHS)	{ASSIGN -> id assign RHS.}	29	(ASSIGN -> id assign RHS.)
oto(25, EXPR)	[ASSIGN -> id assign RHS.] [RHS -> EXPR. literal character boolstr; EXPR -> EXPR.addsub TERM TERM	} 30	{RHS -> EXPR. literal character boolstr; EXPR -> EXPR.addsub TERM TERM}
oto(26, ODECL)	{CDECL -> class id ibrace ODECL.rbrace}	31	(CDECL -> class id lbrace ODECL.rbrace)
oto(26, VDECL)	ODECL -> VDECL.ODECL FDECL ODECL epsilon (VDECL -> vtypeid.semi vtype ASSIGN semi)	32	(ODECL -> VDECL.ODECL FDECL ODECL epsilon; ODECL -> .VDECL ODECL FDECL ODECL epsilon; VDECL -> .vtypeid semi vtype ASSIGN semi)
oto(26, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
oto(27, lbrace)	[FDECL -> vtype id lparen ARGS rparen lbrace.BLOCK RETURN rbrace]		[FDECL -> vtype id lparen RRGS rparen lbrace.BLOCK RETURN rbrace; BLOCK -> .STMT BLOCK epsilon; STMT -> .VDECL ASSIGN semi IFSTMT WHILESTMT; VDECL -> .vtypeid semi vtype ASS
oto(28, MOREARGS)	{ARGS -> vtype id MOREARGS. epsilon}		(ARGS -> vtype id MOREARGS. epsilon)
oto(28, comma)	[MOREARGS -> comma.vtype id MOREARGS epsilon] [RHS -> EXPR .literal character boolstr]	35	[MOREANGS -> comma.vtype id MOREANGS epsilon) [MUSS -> EXPR literal character boolstr)
oto(30, p)	{EXPR -> EXPR addsub.TERM TERM}		[RMR - EARR districted Consister Doubter Doubter RMR multidiv FACTOR FACTOR
oto(31, rbrace)	(CDECL => class id brace ODECL rbrace.)		CRECL -> class id brance ORECL rbrace-)
oto(32, ODECL)	[CDECL -> class id lbrace ODECL rbrace.] [ODECL -> VDECL ODECL. FDECL ODECL epsilon]		(ODECL - VVECL ODECL FDECL ODECL epsilon)
oto(32, VDECL)	{ODECL -> VDECL.ODECL FDECL ODECL epsilon}	32	Control of the contro
	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
oto(33, BLOCK)	{FDECL -> vtype id lparen ARGS rparen lbrace BLOCK.RETURN rbrace}	40	(FDECL -> vtype id lparen ARGS rparen lbrace BLOCK.RETURN rbrace; RETURN -> .return RBS semi)
oto(33, STMT)	{BLOCK -> STMT.BLOCK epsilon}	41	(BLOCK -> STMT.BLOCK epsilon; BLOCK -> .STMT BLOCK epsilon; STMT -> .VDECL ASSIGN semi IFSTMT WHILESTMT; VDECL -> .vtypeid semi vtype ASSIGN semi)
oto(33, VDECL)	{STMT -> VDECL. ASSIGN semi IFSTMT WHILESTMT}	42	{STMT -> VDECL. ASSIGN semi IFSTMT WHILESTMT}
	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
oto(34,)	[ARGS -> vtype id MOREARGS .epsilon]	43	{ARGS -> vtype id MOREARGS .epsilon}
oto(35, vtype)	(MOREARGS -> comma vtype.id MOREARGS epsilon)	44	[MOREARGS -> comma vtype.id MOREARGS epsilon)
	[RHS -> EXPR literal. character boolstr)		[RHS → EXPR literal. character boolstr)
	[EXPR -> EXPR addsub TERM.] TERM; TERM -> TERM.multdiv FACTOR FACTOR)	46	(EXPR -> EXFR addsub TERM. TERM -> TERM.multdiv FACTOR FACTOR)
oto(39,)	{ODECL -> VDECL ODECL .FDECL ODECL epsilon}	47	(ODECL .> VDECL ODECL FDECL ODECL epsilon; FDECL -> .ttype id lparen ARGS rparen lbrace BLOCK RETURN rbrace)
oto(40, RETURN)	(FDECL -> vtype id lparen ARGS rparen lbrace BLOCK RETURN.rbrace) [RETURN -> return.RHS semi)	48	(FDECL -> vtype id lparen ARGS rparen lbrace BLOCK RETURN.rbrace)
oto(40, return) oto(41, BLOCK)	BLOCK -> STMT BLOCK. epsilon)		(RETURN > return.NRS semi; RHS > .EXPR literal character boolstr; EXPR > .EXPR addsub TERN TERN)
oto(41, STMT)	BLOCK -> STHT BLOCK epsilon)	41	Induce -> out mode, epsion
oto(41, VDECL)	STMT -> VDECL. ASSIGN semi IFSTMT WHILESTMT}	42	
oto(41, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
oto(42,)	{STMT -> VDECL .ASSIGN semi IFSTMT WHILESTMT}	51	(STMT -> VDECL ASSIGN semi IFSTMT WHILESTMT; ASSIGN -> .id assign RHS)
	{ARGS -> vtype id MOREARGS epsilon.}		{ARGS -> vtype id MOREARGS epsilon.}
oto(44, id)	[MOREARGS -> comma vtype id.MOREARGS epsilon}		(MOREARGS -> comma vtype id.MOREARGS epsilon; MOREARGS -> .comma vtype id MOREARGS epsilon)
oto(45,)	[RHS -> EXPR literal .character boolstr)	54	(RHS -> EXPR literal .character boolstr}
oto(46,)	{EXPR -> EXPR addsub TERM .TERM}	55	(EXPR -> EXPR addsub TERM .TERM; TERM -> .TERM multdiv FACTOR FACTOR}
oto(46, multdiv)	{TERM -> TERM multdiv.FACTOR FACTOR} {ODECL -> VDECL ODECL FDECL.ODECL epsilon}	56	(TERM -> TERM multdiv.FACTOR FACTOR; FACTOR -> .lparen EXPR rparen id num)
		57	(ODECL -> VDECL ODECL FDECL.ODECL epsilon; ODECL -> .VDECL ODECL FDECL ODECL Epsilon; VDECL -> .vtypeid semi vtype ASSIGN semi)
oto(47, vtype)	(FDECL -> vtype.id lparen ARGS rparen lbrace BLOCK RETURN rbrace)	10	
oto(48, rbrace)	[FDECL -> vtype id lparen ARGS rparen lbrace BLOCK RETURN rbrace.] [RETURN -> return RHS.semi]	58	[FDECL -> vtype id lparen ARGS rparen lbrace BLOCK RETURN rbrace.]
	{RETURN -> return RHS.semi}	59	(RETURN -> return RHs.semi)
	[RHS -> EXPR. literal character boolstr; EXPR -> EXPR.addsub TERM TERM		
oto(50,)	[BLOCK -> STMT BLOCK .epsilon]	61	(BLOCK > SYMT BLOCK epsilon) (SYMT > VUBCL ASSIGN.sems IFSYMT WHILESYMT)
	(STWT -> VDECL ASSIGN.semi IFSTWT WHILESTWT)		COINT == YULGL ADOLUM-DUBL IFSINT WHILESINT)
	[ASSIGN -> id.assign RHS] [MOREARGS -> comma vtype id MOREARGS. epsilon]	62	IMOREARGS => comma vtvne id MOREARGS, ensilon)
oto(53, comma)	(MOREARGS -> comma vtype id MOREARGS. epsilon) [MOREARGS -> comma.vtype id MOREARGS epsilon)	35	(MOREARGS -> comma vtype id MOREARGS. epsilon)
oto(54, character)	(RHS -> EXPR literal character, boolstr)		[RBS -> EXFR literal character. boolstr}
oto(55, TERM)	[RHS -> EXPR literal character. boolstr} [EXPR -> EXPR addsub TERM TERM.; TERM -> TERM.multdiv FACTOR FACTOR}	64	[KMR > EARR district Unitalization DOUBLE
oto(56, FACTOR)	{TERM -> TERM multdiv FACTOR. FACTOR}	65	Team - Team multiday Farra, Farray and - Individual Farray (Team - Team multiday Farray) (Team - Team multiday Farray)
	[FACTOR -> lparen.EXPR rparen id num)		(FACTOR -> lparen.EXPR rparen id num; EXPR -> .EXPR addsub TERM TERM)
oto(57, ODECL)	{ODECL -> VDECL ODECL FDECL ODECL. epsilon}	67	(ODECL -> VDECL ODECL FDECL ODECL. epsilon)
oto(57, VDECL)	{ODECL -> VDECL.ODECL FDECL ODECL epsilon}	32	
oto(57, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
oto(59, semi)	(RETURN -> return RHS semi.)		(RETURN -> return RHS semi.)
	{BLOCK -> STMT BLOCK epsilon.}		(BLOCK -> STMT BLOCK epsilon.)
	(STMT -> VDECL ASSIGN semi. IFSTMT WHILESTMT)		(STMT -> VDECL ASSIGN semi. IFSTMT WHILESTMT)
	[MOREARGS -> comma vtype id MOREARGS .epsilon}		(MOREARGS -> comma vtype id MOREARGS .epsilon)
	[RHS -> EXPR literal character .boolstr)		{RBS -> EXFR literal character .boolstr}
oto(64, multdiv)	{TERM -> TERM multdiv.FACTOR FACTOR}	56	
oto(65,)	[TERM -> TERM multdiv FACTOR .FACTOR]		(TERM -> TERM multdiv FACTOR FACTOR; FACTOR -> .lparen EXFR rparen id num}
	[FACTOR -> lparen EXPR.rparen id num; EXPR -> EXPR.addsub TERM TERM]		[FRCTOR -> lparen EXPR.rparen id num; EXPR -> EXPR.addsub TERM TERM)
oto(67,)	{ODECL -> VDECL ODECL FDECL ODECL .epsilon}		(ODECL >> VDECL ODECL FDECL ODECL epsilon)
oto(70,)	(STMT -> VDECL ASSIGN semi .IFSTMT WHILESTMT)		(STMT -> VDECL ASSIGN semi IFSTMT WHILESTMT; IFSTMT -> .if lparen COND rparen lbrace BLOCK rbrace ELSE)
oto(71, epsilon)	{MOREARGS -> comma vtype id MOREARGS epsilon.}		MADERARGS -> Comma vtype id MOREARGS epsilon.}
oto(72, boolstr)	[RHS -> EXPR literal character boolstr.]		(RHS -> EXFR literal character boolstr.)
oto(73, FACTOR)	[TERM -> TERM multdiv FACTOR FACTOR.]		{TERM -> TERM multdiv FACTOR FACTOR.}
oto(73, lparen)	{FACTOR -> lparen.EXPR rparen id num}	66	
oto(74. rparen)	(FACTOR -> lparen EXPR rparen. id num)	80	(FACTOR -> lparen EXFR rparen. id num)
1 174 17	{EXPR -> EXPR addsub.TERM TERM}	137	(ODECL -> VDECL ODECL FDECL ODECL epsilon.)
oto(74, addsub)			
oto(74, addsub) oto(75, epsilon)	{ODECL -> VDECL ODECL FDECL ODECL epsilon.}		
oto(74, addsub) oto(75, epsilon) oto(76, IFSTMT)	{STMT -> VDECL ASSIGN semi IFSTMT. WHILESTMT}	82	(STMT -> VDECL ASSIGN semi IFSTMT. WHILESTMT)
oto(74, addsub) oto(75, epsilon) oto(76, IFSTMT) oto(76, if)		82	

https://jsmachines.sourceforge.net/machines/slr.html

			SLR closure table
Goto	Kernel	State	
	(IFSTNT -> if lparen.COND rparen lbrace BLOCK rbrace ELSE)	-	(IFSTNT -> if lparen.COND rparen lbrace BLOCK rbrace ELSE; COND -> .EXFR comp EXFR boolstr; EXFR -> .EXFR addsub TERM TERM}
goto(84, id)	(FACTOR -> lparen EXPR rparen id. num)		FRACTOR -> lparen EXPR rparen id. num}
	(STMT -> VDECL ASSIGN semi IFSTMT WHILESTMT.)		(STHT - V DECE ASSIGN semi ISSTH WHILESTHI.)
goto(85, while)	(WHILESTMT -> while.lparen COND rparen lbrace BLOCK rbrace)		(WHILESTWT -> while.lparen COND rparen lbrace BLOCK rbrace)
goto(86, COND)	(IFSTMT -> if lparen COND.rparen lbrace BLOCK rbrace ELSE)		[IFSTNT -> if lparen COND.rparen lbrace BLOCK rbrace ELSE]
goto(86, EXPR)	(COND -> EXPR.comp EXPR boolstr; EXPR -> EXPR.addsub TERM TERM)		[COND -> EXPR.comp EXPR boolstr; EXPR -> EXPR.addsub TERN TERN)
goto(87,)	(FACTOR -> lparen EXPR rparen id .num)		{FACTOR -> lparen EXPR rparen id .num}
	(WHILESTMT -> while lparen.COND rparen lbrace BLOCK rbrace)		[WHILESTHT -> while lparen.COND rparen lbrace BLOCK rbrace; COND -> .EXPR comp EXPR boolstr; EXPR -> .EXPR addsub TERM TERM)
goto(90, rparen)	(IFSTMT -> if lparen COND rparen.lbrace BLOCK rbrace ELSE)		[IFSTMT → if lparen COND rparen.lbrace BLOCK rbrace ELSE}
goto(91, comp)	(COND -> EXPR comp.EXPR boolstr)		[COND -> EXPR comp.EXPR boolstr; EXPR -> .EXPR addsub TERM TERM}
	(EXPR -> EXPR addsub.TERM TERM)	37	
goto(92, num)	(FACTOR -> 1paren EXPR rparen id num.)	96	{FACTOR -> lparen EXPR rparen id num.}
goto(93, COND)	(WHILESTMT -> while lparen COND.rparen lbrace BLOCK rbrace)		[WHILESTHY -> while lparen COND.rparen lbrace BLOCK rbrace}
qoto(93, EXPR)	(COND -> EXPR.comp EXPR boolstr; EXPR -> EXPR.addsub TERM TERM)	91	
goto(94, 1brace)	(IFSTMT -> if lparen COND rparen lbrace.BLOCK rbrace ELSE)	98	(IFSTMT -> if lparen COND rparen lbrace.BLOCK rbrace ELSE; BLOCK -> .STMT BLOCK epsilon; STMT -> .VDECL ASSIGN semi IFSTMT WHILESTMT; VDECL -> .vtypeid semi vtype ASSIGN semi)
goto(95, EXPR)	(COND -> EXPR comp EXPR. boolstr; EXPR -> EXPR.addsub TERM TERM)		[COND → EXPR comp EXPR. boolstr; EXPR → EXPR.addsub TERM TERM}
goto(97, rparen)	(WHILESTMT -> while lparen COND rparen.lbrace BLOCK rbrace)	100	{WHILESTWT -> while lparen COND rparen.lbrace BLOCK rbrace}
goto(98, BLOCK)	(IFSTMT -> if lparen COND rparen lbrace BLOCK.rbrace ELSE)	101	{IFSTMT -> if lparen COND rparen lbrace BLOCK.rbrace ELSE}
goto(98, STMT)	{BLOCK -> STMT.BLOCK epsilon}	41	
goto(98, VDECL)	{STMT -> VDECL. ASSIGN semi IFSTMT WHILESTMT}	42	
goto(98, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
goto(99,)	{COND -> EXPR comp EXPR .boolstr}	102	{COND -> EXPR comp EXPR .boolstr}
goto(99, addsub)	{EXPR -> EXPR addsub.TERM TERM}	37	
goto(100, lbrace)	{WHILESTMT -> while lparen COND rparen lbrace.BLOCK rbrace}	103	(WHILESTMT -> while lparen COND rparen lbrace.BLOCK rbrace; BLOCK -> .STMT BLOCK epsilon; STMT -> .VDECL ASSIGN semi IFSTMT WHILESTMT; VDECL -> .vtypeid semi vtype ASSIGN semi)
goto(101, rbrace)	{IFSTMT -> if lparen COND rparen lbrace BLOCK rbrace.ELSE}	104	{IFSTNT -> if lparen COND rparen lbrace BLOCK rbrace.ELSE; ELSE -> .else lbrace BLOCK rbrace epsilon}
goto(102, boolstr)	{COND -> EXPR comp EXPR boolstr.}	105	{COND -> EXPR comp EXPR boolstr.}
goto(103, BLOCK)	{WHILESTMT -> while lparen COND rparen lbrace BLOCK.rbrace}	106	{WHILESTMT -> while lparen COND rparen lbrace BLOCK.rbrace}
goto(103, STMT)	{BLOCK -> STMT.BLOCK epsilon}	41	
goto(103, VDECL)	{STMT -> VDECL. ASSIGN semi IFSTMT WHILESTMT}	42	
goto(103, vtypeid)	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
goto(104, ELSE)	(IFSTMT -> if lparen COND rparen lbrace BLOCK rbrace ELSE.)	107	{IFSTMT -> if lparen COND rparen lbrace BLOCK rbrace ELSE.}
goto(104, else)	{ELSE -> else.lbrace BLOCK rbrace epsilon}	108	(ELSE -> else.lbrace BLOCK rbrace epsilon)
goto(106, rbrace)	(WHILESTMT -> while lparen COND rparen lbrace BLOCK rbrace.)	109	{WHILESTWT -> while lparen COND rparen lbrace BLOCK rbrace.}
goto(108, lbrace)	(ELSE -> else lbrace.BLOCK rbrace epsilon)	110	(ELSE -> else lbrace.BLOCK rbrace epsilon; BLOCK -> .STMT BLOCK epsilon; STMT -> .VDECL ASSIGN semi IFSTMT WHILESTMT; VDECL -> .vtypeid semi vtype ASSIGN semi)
goto(110, BLOCK)	(ELSE -> else lbrace BLOCK.rbrace epsilon)	111	{ELSE -> else lbrace BLOCK.rbrace epsilon}
goto(110, STMT)	{BLOCK -> STMT.BLOCK epsilon}	41	
goto(110, VDECL)	{STMT -> VDECL. ASSIGN semi IFSTMT WHILESTMT}	42	
	{VDECL -> vtypeid.semi vtype ASSIGN semi}	4	
goto(111, rbrace)	{ELSE -> else lbrace BLOCK rbrace. epsilon}		{ELSE -> else lbrace BLOCK rbrace. epsilon}
goto(112,)	(ELSE -> else lbrace BLOCK rbrace .epsilon)		{ELSE -> else lbrace BLOCK rbrace .epsilon}
goto(113, epsilon)	{ELSE -> else lbrace BLOCK rbrace epsilon.}	114	(ELSE -> else lbrace BLOCK rbrace epsilon.)



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