A/D conversion

CPE 381 Foundations of Signals & Systems for Computer Engineers

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A/D conversion Background

- sampling analog signals
- fs > 2 B
- signal to noise ratio
- dynamic range

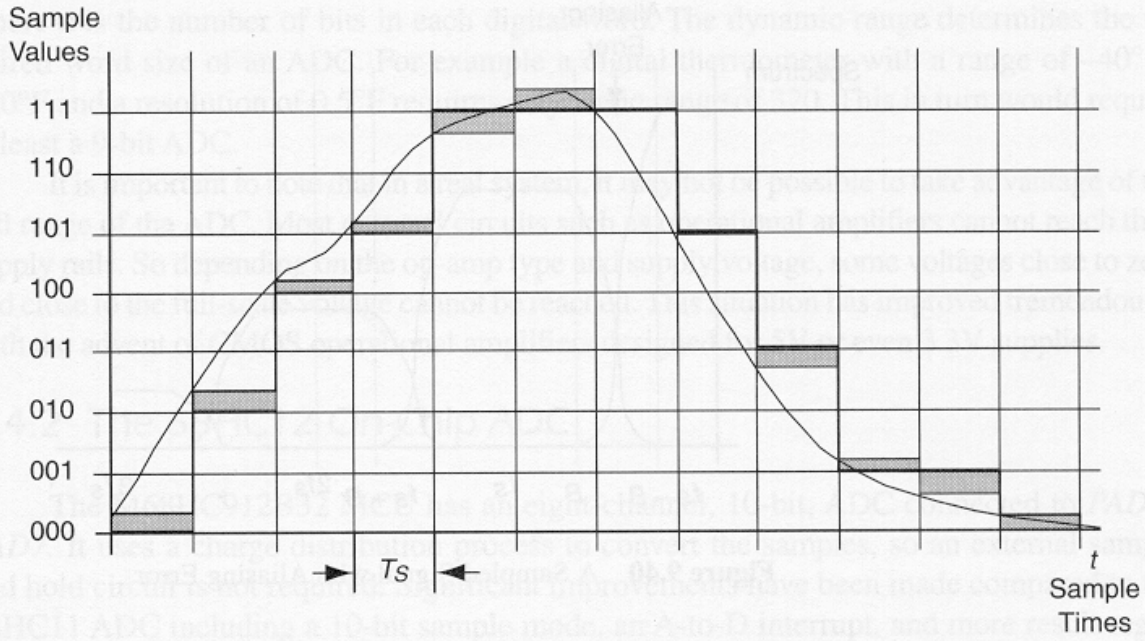


Figure 9.38 A-to-D Conversion Samples in the Time Domain

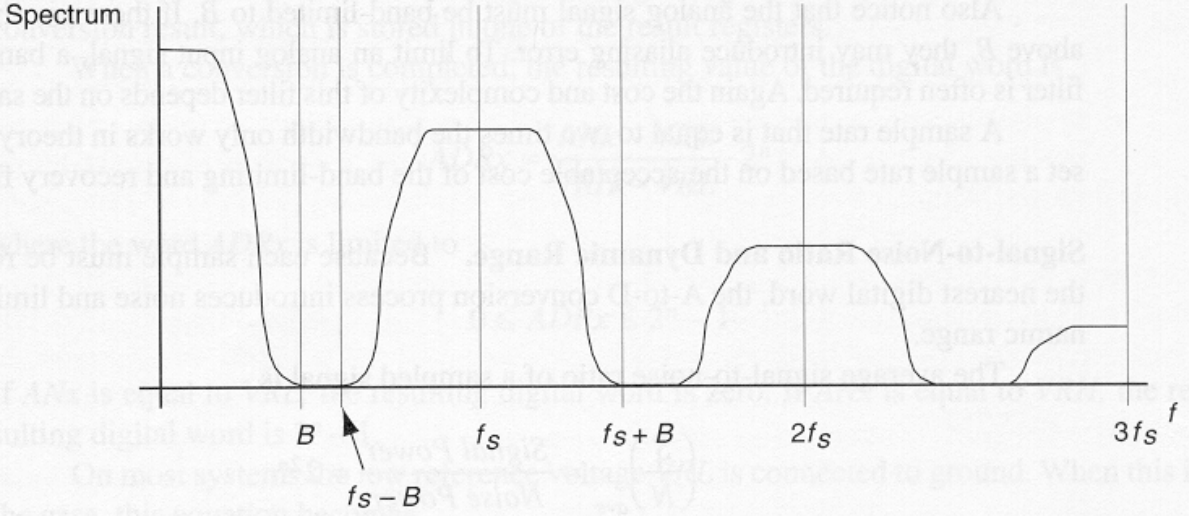
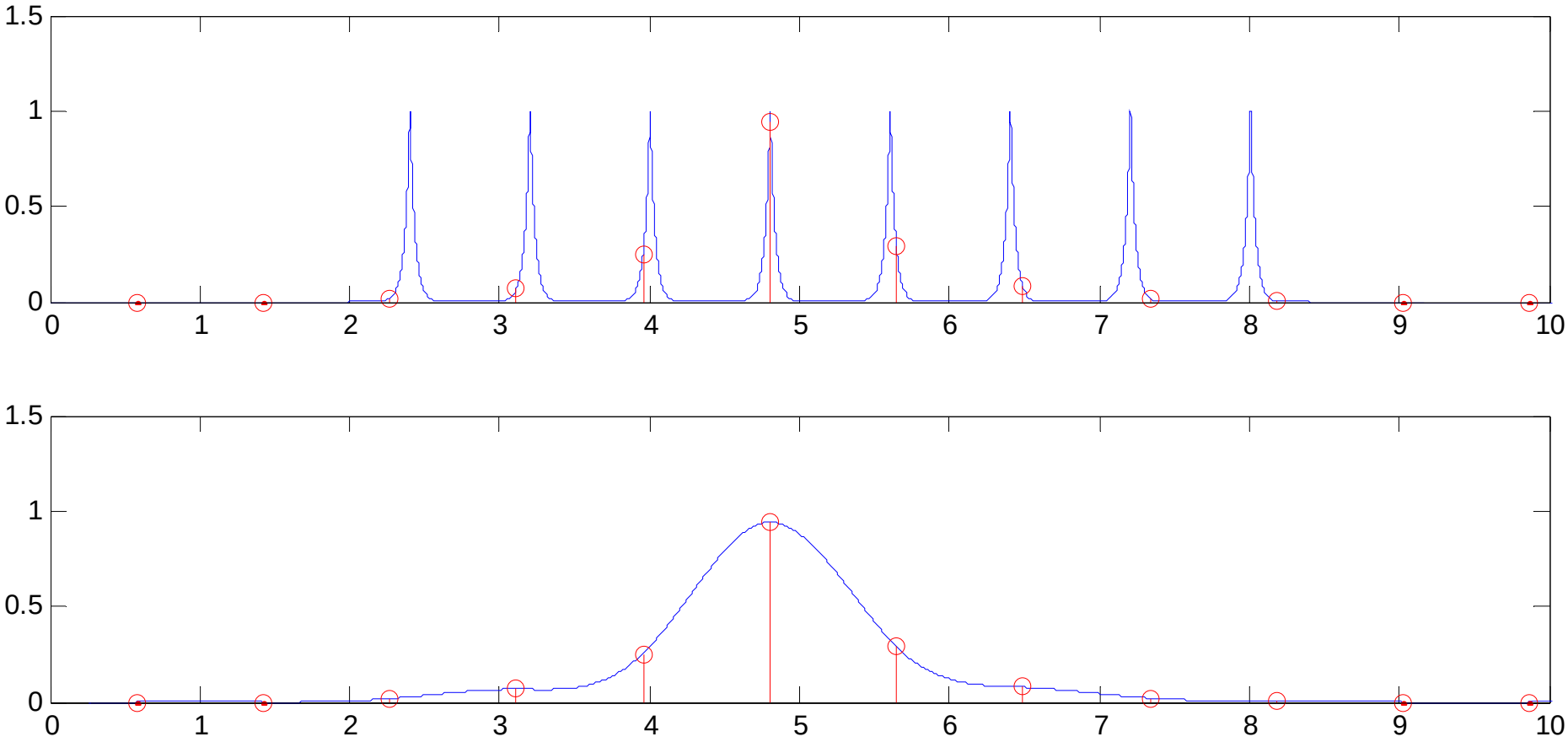


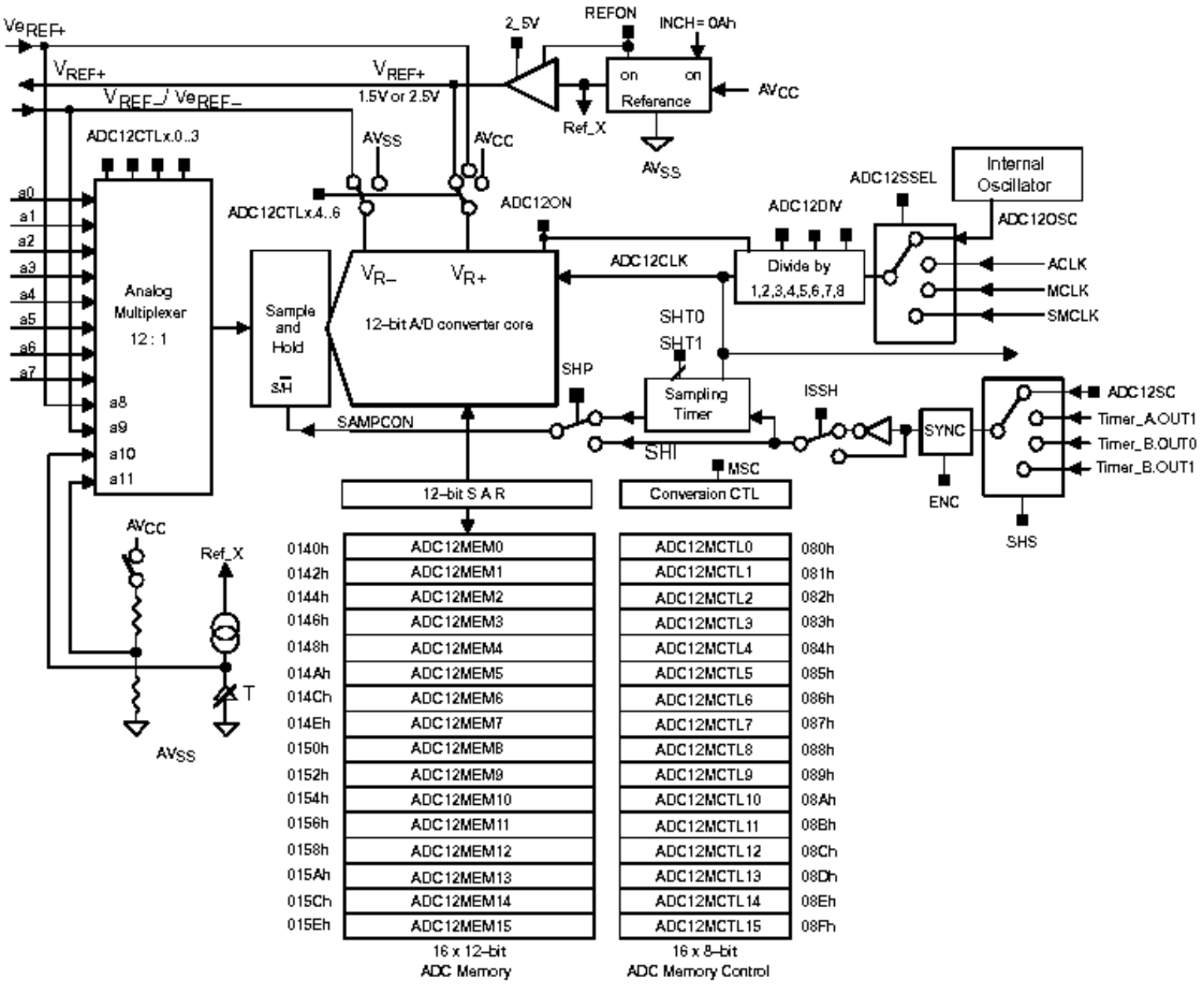
Figure 9.39 Frequency Spectrum of a Sampled Signal

A/D conversion example



F149 AD converter block diagram

Figure 15–1. ADC12 Schematic



F149 A/D converter

- 8 external inputs
- 4 internal inputs
  - Vref+
  - Vref-
  - Vcc
  - Temperature
- Conversion (CONSEQ)
  - Single channel
    - Single conversion
    - Multiple conversions
  - Sequence of channels
    - Single conversion
    - Multiple conversions
  - Sequence starts from CStartAdd in ADC12CTL1
- 200 ksps, on chip RC oscillator
- Sixteen storage registers for conversion results
- Separate power down

F149 A/D converter #2

- Nadc = 4095*(Vin-Vr-)/(Vr+-Vr-)
- 3 LSBs resolved resistively
  - 200 µA from the reference
  - possible problems with external reference
  - Vcc
  - Temperature
- Possible errors
  - Coupling (PCB techniques)
  - Leakage current
    - ± 50 nA (page 43 F149 datasheet)
  - Err=4.096*(leakage_curr[µA]*source_resistance[kΩ])/(Vr+-Vr-)
    - 10 KΩ source resistance with 1.5V reference gives 1.4LSB error
  - Input switching currents