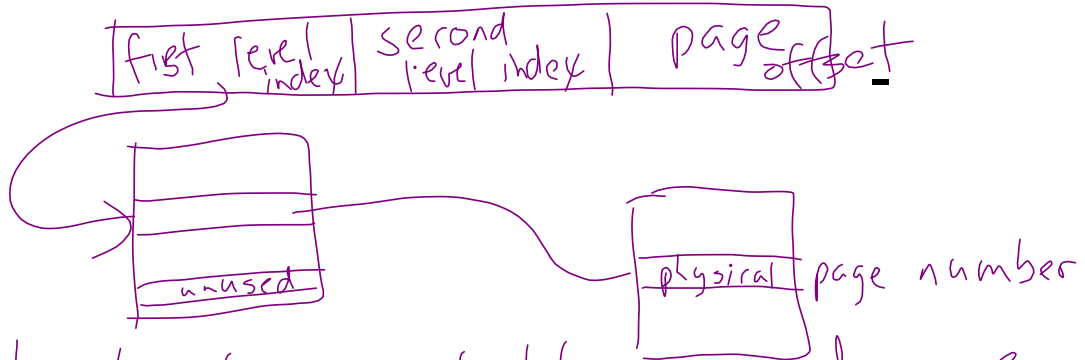
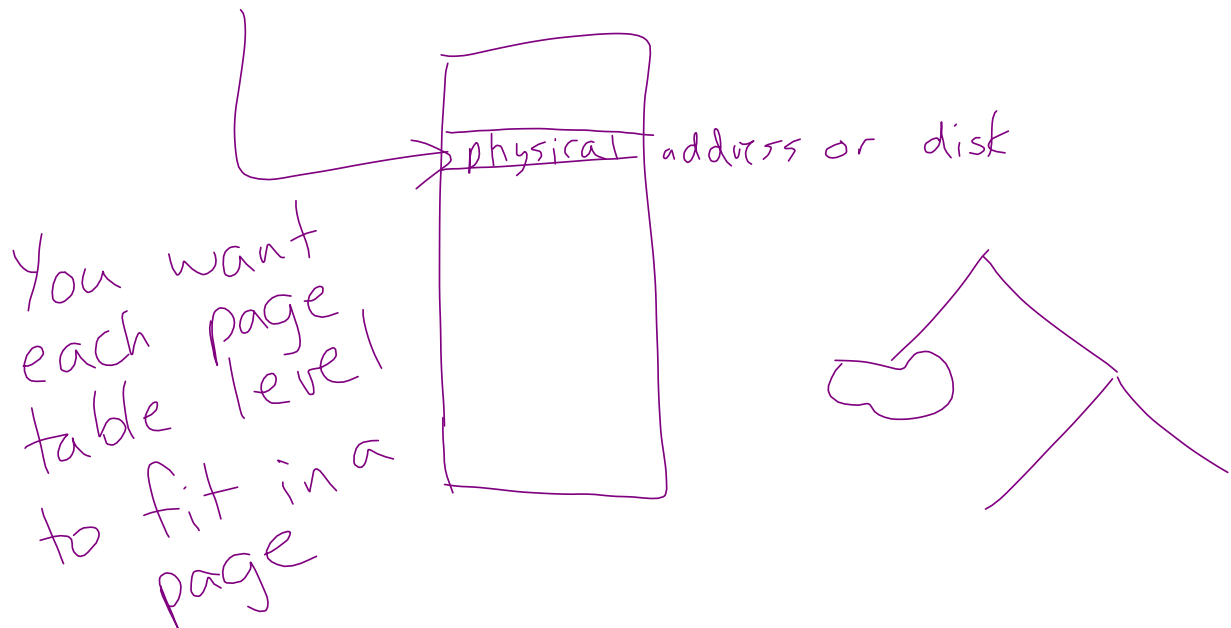


Multiple Level Page Tables



Single level page table - need one entry for every virtual page number

virtual page#	page offset
---------------	-------------



You want each page table level to fit in a page

Page size 1KB

PTE 4 B

Physical address 32 bits

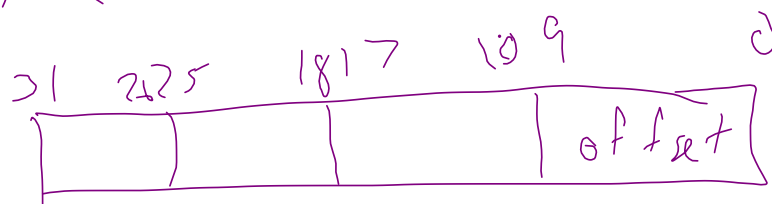
Virtual address 32 bits

How many
level page
table is
required?

$$\frac{1 \text{ KB}}{\text{page}} \times \frac{\text{PTE}}{4 \text{ B}} = \frac{256 \text{ PTE}}{2^8 \text{ page}}$$

1KB page \Rightarrow 10 bits offset

$$2^{10} = 1 \text{ K}$$



First Level
64 entries
next level
third level

1234	5678	9101112
0011	0111	0101

$$C_1 = \text{XOR}(1, 3, 5, 7, 9, 11) = 0$$

$$C_2 = \text{XOR}(2, 3, 6, 7, 10, 11) = 0$$

$$C_4 = \text{XOR}(4, 5, 6, 7, 12) = 0$$

$$C_8 = \text{XOR}(8, 9, 10, 11, 12) = 1 \quad \leftarrow \text{MSB}$$

error in bit 8

$$C_1 = 1$$

$$C_2 = 0$$

$$C_4 = 1$$

$$C_8 = 0$$

Bit 5 is
wrong

$$C_1 = 0$$

$$C_2 = 0$$

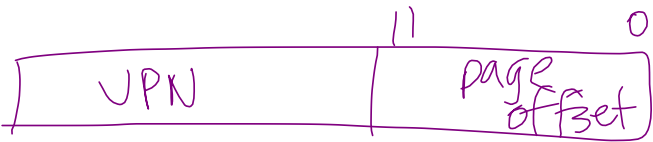
$$C_4 = 0$$

$$C_8 = 0$$

all
correct

	V	Tag	PPN
0	1	11	12
1	1	7	4
2	1	3	6
3	0	4	9

4096 Page #



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