

Guest Check

TABLE	GUESTS	SERVER	
			87964
	Why would the	the	
	the Ccche to		
	be disabled?		
	ensure operation		
	are done instantly		
	also for limited		
	operation that		
	the data will		
	not need to be		
	stored		
	Chris Stacker		
	9/22/15	Tax	
		Total	

Guest Check

[illegible]

@adams

Guest Check

TABLE	GUESTS	SERVER
		87957
	Eric Littenen	(3)
Q:	What is a good example of Barrier use?	
A:	Serial set up of multiple data collection channels, with a synchronized start of all channels so that all channels are collecting the same data.	
		Tax
		Total

2 adams

est Check

GUESTS	SERVER	87963
Why would memory		
be marked as		
executable or		
data?		
To help prevent		
code injection		
Chris Stocker		
9/22/15		
Tax		
Total		

Guest Check

[illegible]

Guest Check

TABLE	GUESTS	SERVER	
			87954
	Eric Littleken		
	Q: Why is it bad for		
	a process to call thread_yield		
	while waiting for a MUTEX?		
	A: There is no guarantee		
	that the MUTEX will be free		
	when the operating system		
	re-schedules that process,		
	Therefore, the chances that		
	that process gets the MUTEX		
	diminish.		
		Tax	
		Total	

TABLE	GUESTS	SERVER
		87953
		Chris Stacker
		problem with Sleep wake example Slide 35 + 36
		What is the problem with the code?
		Tax
		Total

Guest Check

TABLE	GUESTS	SERVER
		87967
Q: There are a lot of pre-fetchers that tries to guess what is the next block of memory that will be needed and fetch it in advance. What happens if the pre-fetcher pre-fetcher fetches the wrong block?		
A: It will waste computer time, since the block will not be used.		
		Tax
		Total

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TABLE	GUESTS	SERVER
		87950
Eric Littleken		
What limitations are there for what commands can be connected with a pipe?		
What happens when your first command provides bad input to the second command?		
Tax		
Total		

2. adams

Guest Check

TABLE	GUESTS	SERVER
		67960
	Michael Walsh	
Slide	LWS Clock Algorithm(?)	3
Q:	Does A clock algorithm check pages in the order that they were loaded.	
A:	No.	
	Tax	
	Total	

Guest Check

TABLE	GUESTS	SERVER	
			87958
Aaron Werth			3
Question			
I had a question about an equation: What do the variables mean?			
$\sum_{i=1}^N \frac{C_i}{P_i} \leq 1$			
Answer			
P = time allocated			
C = time used			
i = the process.			
		Tax	
		Total	

SA108A

~~Guest~~ Check

TABLE	GUESTS	SERVER
		87962
Nick Werline		3
Question: Who has a flash drive?		
Answer: I do.		
9/22/15		
Tax		
Total		

GUESTS	SERVES	
		67959
Michael Walsh		
Second Chance Algorithms		
Is there a way to identify how many times a page has been referenced?		
Counting how many times the page has been referenced: the page to be permanently stored is either 50, or		
	Tax	
	Total	

2. Adams

TABLE	GUESTS	SERVER	
			87961
Aaron Werth			
I had a question about virtual pages. I asked what the "X" meant in the diagram.			
This is a virtual page not used by the program. It would cause a page fault if a user tried to access it. In this case, the ^{Tax} page would have ^{Total}			

SA108A to be loaded into memory or allocated

2 Adams

Guest		Server	
TABLE	GUESTS	SERVER	
			67961
	Nick Werline		:
Q	Does the operating system or the program decide how many pages are needed?		
A	The operating system will decide an initial number but then dynamically allocate based upon the number of page faults occurring.		
		Tax	
		Total	

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2: adams

Test Check

GUESTS	SERVER
67963	
Michael Walsh	Ticket #3
Shared Libraries	
Why are libraries stored in the OS as opposed to stored in the programs.	
Shared libraries can be called up instead of duplicated in memory to conserve memory space.	
The OS can update libraries without accessing individual programs.	
Tax	
Total	

adams

Guest Check

TABLE	GUESTS	SERVER
87951		
Aaron Werth		
Question: Is a critical region needed when several processes need to use a printer?		
Answer: In this case a queue is more likely to be used.		
A console or keyboard would require a critical region with several processes.		
Tax		
Total		

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TABLE	GUESTS	SERVER
67964		
Nick Werline		
Q: When a program references a page that is not in memory, the instruction causing the fault is stopped partway through, what is a problem with this?		
A: The operating system would have to determine where the instruction begins. The value of the PC could be pointing elsewhere.		
10/1		
Tax		
Total		

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[illegible]

Ronald Burke

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TABLE	GUESTS	SERVER
87966		
Q/ZZ @ 3:08		V
TLB tables		
have 12-4,096		
Entries		
& have a		
hit time of		
0.5-1 clock		
Cycles		
Mike Baldwin		
Tax		
Total		

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TABLE	GUESTS	SERVER	
Bryant Johnson	(2)	CPE 434	
How are the locations for processes and files searched for in memory management?			
		9/17/2015	
		Tax	
		Total	

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TABLE	GUESTS	SERVER
		87965
How is the TLB Cache		
Fast 2 Translation		
Accessed - Buffers		
It is because it accesses		
the pages through parallel		
Scanning rather than		
Loading the page one		
at a time		
Size:		
17-4096 entries		
Tax		
Total		

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TABLE	GUESTS	SERVER
		67958
9/29 @ 2:50		
Q- Why don't we zero		
out memory when it is no longer		
needed?		
A- The step would be		
unnecessary & the data		
may be used again before		
it is over written.		
Mike Baldwin		
Tax		
Total		

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TABLE	GUESTS	SERVER
		87959
Bryant Johnson		CPE 434
When will worst fit be		
a good scenario for		
memory management?		
		9/17/2015
Tax		
Total		

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