

NEERAJ PREMKUMAR

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OBJECTIVE

Motivated and trained fresher in **VLSI Design and Verification**, seeking entry-level opportunity to contribute to digital design and functional verification projects. Well-versed in **Verilog, System Verilog, UVM**, and standard **AMBA protocol**, with hands-on simulation practice using **EDA Tools**. Interested in growing as a skilled **verification engineer** in a challenging environment.

EDUCATION

International PG Diploma in VLSI Design and Verification

Jun 2024- Mar 2025

IBIS Academy, Thrissur (Accredited by IACET - USA)

Relevant Coursework: FPGA, CMOS, AMBA, RTL & SoC design verification.

B.Tech, Electronics and Communication Engineering

Sep 2020- Jun 2024

APJ Abdul Kalam Technological University

Vidya Academy of Science And Technology, Thrissur

Relevant Coursework: Digital Electronics, VLSI design

TECHNICAL SKILLS

Verilog	C	System Verilog	Python	VHDL
SVA	STA	UVM	DFT	Linux
AHB	APB	AXI	UART	

PROJECTS

AHB to APB Bridge Verification

Jan 2025 - Feb 2025

- Implemented the AMBA Advanced Peripheral Bus (APB) protocol in Verilog for peripheral communication.
- Developed master and slave modules supporting APB read/write transactions with proper handshaking and timing control.

1x3 Router Design and Verification

Oct 2024 – Nov 2024

- Executed the design and implementation of a high-performance 1x3 router project in Verilog. Also, optimized data routing and distribution in intricate systems, resulting in uninterrupted data flow and a 20% reduction in latency compared to previous designs, leading to a substantial enhancement in overall system efficiency.
- Software used:** Modelsim 18.1, EDA Playground, AMD Vivado.

Real-Time Vehicle Tracking and Fuel Monitoring System

Sep 2023 - May 2024

- Real-Time GPS-based Vehicle Tracking and Fuel Consumption Monitoring System for Transportation Efficiency.
- Used GPS and fuel sensors for data acquisition; transmitted data to Firebase cloud.
- Created a Kodular app that retrieves and displays live vehicle location and fuel levels via Firebase.
- Software used:** Firebase Console, Kodular, Arduino IDE.

INTERNSHIP & CERTIFICATES

Internship - Keltron (May 2023)

Completed a hands-on internship at a government-run electronics and networking center, contributing to system-level hardware and network operations.

Certifications

- Introduction to Design Verification using UVM - IEEE CASS SBC TKMCE (March 2025)
- Completed training in Python Programming by Spoken Tutorial Project, IIT Bombay (Aug 2023)
- Attended workshop on Arduino Programming conducted by NIT Calicut (Nov 2022)