









The image shows a detailed PCB layout for an ADXL345 module. The central component is the ADXL345 digital accelerometer chip, which is connected to a microcontroller via I2C or SPI. The layout includes several power and ground planes, with decoupling capacitors placed near the power pins of the chip and the microcontroller. A VREF circuit is shown at the bottom, consisting of a resistor network and a capacitor, used to provide a stable reference voltage for the accelerometer. The PCB is populated with various passive components, including resistors and capacitors, and has several test points and connection pads. The layout is organized to minimize signal interference and ensure reliable operation of the module.

VREF circuit should
be placed close between
proc and dram





