

Video Board Test Results-VB4

1. Attach video board, do smoke test, measure basic voltages (same as driver). Measure on boards and at connectors.

Table 1: Power Supply Voltages

	+5V	+15V	-15V	+24V	2.5 V	3.3V
Before LSE load	0.497 A	0.038 A	0.034 A	0.000 A	2.429 V	3.272 V
Load LSE	0.471 A	0.065 A	0.061 A	0.001 A	2.398 V	3.278 V
Run LSE	0.398 A	0.065A	0.061 A	0.001 A	2.301 V	3.281 V
	0.442 A	0.109 A	0.103 A	0.001 A	2.443 V	3.281 V
Stop LSE	0.471 A	0.065 A	0.061 A	0.001 A	2.395 V	1.278

2. Set the DAC's using vdm60f.fpg (most likely already done as part of the driver board testing). Record the set values from vdm60f and calculate the expected values in Table

Table 2: DAC Set values and Expected Output Voltages

DAC SET	Set (Hex)	Set (Dec)	/4096	Expected Value
OG	0940	2368	.578125	1.908
IG1	06FB	1787	0.436279296875	1.440
IG2	0730	1840	0.44921875	1.482421875
SCP	0CD9	3289	0.802978515625	2.6498291015625
RD	0CC4	3268	0.7978515625	2.63291015625
BS	0000	0	0	0
SUB	0ED8	3800	0.927734375	3.0615234375
DR-A	0CDE	3294	0.80419921875	2.653857421875
DR-B	0CDE	3294	0.80419921875	2.653857421875
DR-C	0CDE	3294	0.80419921875	2.653857421875
DR-D	0CDE	3294	0.80419921875	2.653857421875

3. Check DACs: Measure voltage of each output of the DACs. Value should be within 1% TBR of expected values.

DAC	CCD1	CCD2	CCD3	CCD4
REF	3.307	3.309	3.305	3.309
OG	1.903	1.920	1.911	1.911
IG1	1.451	1.430	1.434	1.453
IG2	1.491	1.492	1.473	1.482
SCP	2.661	2.645	2.663	2.682
RD	2.629	2.646	2.647	2.633
BS	0.010	0.002	0.006	0.025
SUB	3.064	3.061	3.051	3.063
DR-A	2.655	2.636	2.667	2.664
DR-B	2.649	2.660	2.656	2.657
DR-C	2.662	2.671	2.654	2.645
DR-D	2.639	2.659	2.675	2.655

4. Measure to actual voltages corresponding to each of the parameters above and record in Table

DAC	CCD1	CCD2	CCD3	CCD4
OG	-1.104	-1.047	-1.061	-1.084
IG1	-2.766	-2.826	-2.789	-2.735
IG2	-2.574	-2.601	-2.643	-2.636
SCP	12.17	12.06	12.19	12.24
RD	12.02	12.09	12.07	12.02
BS				
SUB	-46.10	-46.09	-45.74	-45.94
DR-A	20.01	19.94	20.06	20.04
DR-B	19.97	20.06	20.13	20.13
DR-C	19.98	20.08	20.09	19.97
DR-D	19.95	20.13	20.17	19.94

5. Using the LSE, query each of the housekeeping values for each of the video board voltages and record below.

DAC	CCD1	CCD2	CCD3	CCD4
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OG	-1.07	-1.08	-1.08	-1.03
IG1	-2.72	-2.76	-2.71	-2.69
IG2	-2.61	-2.53	-2.61	-2.63
SCP	12.14	11.97	12.08	12.20
RD	11.96	12.01	12.03	11.97
BS	0.02	-0.05	0.06	-0.02
SUB	-45.76	-45.48	-45.72	-45.36
DR-A	20.10	19.92	20.05	19.97
DR-B	19.90	20.09	19.97	20.19
DR-C	20.09	20.18	20.08	20.21
DR-D	20.06	20.06	20.04	19.86

6. Install dummy RTDs and measure all temperature HK. Intent is to check precision of temperature sensors? Plug all resistance temperature detector (device) (RTDs) in and place next to thermometer. Measure temperatures as environmental temperature changes. Expected accuracy is 5%. TBR. Alternatively, use programmable electronic load to measure precision. (for reference only) (2 days)
7. Take no-load full frame image (FFIs). Measure noise via histogram of pixel values. Compare to TBD requirement on video chain noise. (1 day - assuming software is complete)
8. Install pulser into one video chain at a time. Take FFIs at various pulser amplitudes and periods. Measure from FFIs all of the below. **Note that any anomalies will be followed by debugging to understand and mitigate the first time.**
 - a. Cross-talk: pulse one video channel, look for signals (at TBD level; look at Kepler requirement/performance) on other channels. More a measurement in RRU, input to combined differential photometric performance (CDPP) spreadsheet (1 day)
 - b. Linearity and gain: Vary pulse amplitude, measure signal, plot (compare to what?)(1 day)
 - c. Over/undershoot: Burst 100-200 pixels. Should have sharp edges. Over/undershoot is over/undershoot over square wave. (Need a picture here). Quantity to measure is amplitude of over/undershoot: what do we compare that to? Kepler actual performance (as requirement), while goal is much better.(1 day)
 - d. Noise: histogram of pixels at various pulser levels, get video chain noise (compare to video chain noise allocation)(1 day)
 - e. Bias flatness: requires capacitive load. Look for sag/brightening at edges of image. Doesn't really need pulser. Criterion TBR. Need picture to describe effect.(1 day)
 - f. Saturation: where the linearity test falls off the edge, measures saturation in volts. Should at least read full-scale of ADC, $\pm 3V$.(1 day)
9. Hook up to cold CCD to measure sensitivity to photons (for reference only) (1 day)

- a. Fe55, optical light
- b. Confirms performance of parallel clocks under load
- c. Can one do this with a smaller CCD? yes