

RESUME

THANGA ANANTH.J

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Tuticorin(DT) -628203.

PERSONAL DATA:

D.O.B : 15-01-1992
Sex : Male
Blood Group : 'B' -ve
Nationality : Indian
Marital Status : Single

Father's Name :
Mr.T.JEYARAJ

Occupation: Farming

Mother's Name:
Mrs.J.THEIVASUNDARI

Languages Known:

English, Tamil.

OBJECTIVE

To utilize my skills and capabilities in the Electronics and Embedded Sector with own interest to pursue organization and to expand my knowledge horizon.

TECHNICAL SKILLS

- Programming Language : Embedded C
- Microcontrollers : 8051, PIC, AVR, ARM
- Simulation software : LabVIEW, MULTISIM, TCAD
MATLAB
- Serial Protocols : I²C, SPI

ACADEMIC RECORD

Course	Name of the Institution	Board/ University	Year of study	MARKS %/ CGPA
PGDESD (Embedded System Design)	CDAC Hyderabad	DeitY, MC&IT, Govt of India.	Feb 2016- Aug 16	67.71% (A-Grade)
M.E- (Embedded System Technologies)	Regional Centre Anna University Tirunelveli.	Anna University, Chennai	2013- 15	7.7
B.E – (Electronics And Communication)	University College of Engineering. Thoothukudi.	Anna University, Chennai	2009- 13	8.36
XII	Sri R.K.C Hr. Sec. School Udangudi.	State Board	2008- 09	90%
X	Sri R.K.C Hr. Sec. School Udangudi	State Board	2006- 07	85.8%

FIELDS OF INTEREST

- Embedded Systems
- Electronic Devices and Circuits
- Analog and Digital Electronics

ACADEMIC PROJECTS

COURSE: PG-DESD

IMPLEMENTATION OF ROBUST SERVER- CLIENT SYSTEM USING WIFI SENSOR NODES (TI CC3200, ESP 8266-12E) AND RASPBERRY PI

Aim of this project to create client server model for continuously monitor the temperature and send it to the servers hosted on Raspberry Pi

Roles Played:

- 1) Implementation of Wi-Fi based sensor node
- 2) Development of multi-client TCP/IP Socket

COURSE: M.E.

SIMULATION AND ANALYSIS OF SINGLE ELECTRON TRANSISTOR USING TCAD

Aim of this project is to design a Single Electron Transistor using TCAD and implement it for basic logic gates like NAND, NOR, AND, OR and NOT

Roles Played:

- 1) Designing a Single Electron Transistor
- 2) Creating Net list
- 3) Implementation of Logical gates

COURSE: B.E.

ELECTRONICALLY GUIDED WALKING STICK FOR VISUALLY IMPAIRED PERSONS

Aim of this project is to design an electronically guided walking stick which will help blind peoples to walk inside any closed premises by using FM Receivers.

Roles played:

- 1) Architecture
- 2) Hardware Design and Implementation of RF Transceiver
- 3) Integrating Voice playback section with RF Transceiver
- 4) Power requirement analysis and designing rectifier

IDENTIFICATION OF OBJECTS UNDER SHADOW AND VEHICLE DETECTION FOR ROAD MONITORING

Aim of this project is to identify the objects under shadow and to detect vehicle for road monitoring which will help to measure the traffic densities.

Roles Played:

- 1) Code development for shadow detection and removal
- 2) Implementation for vehicle detection

EXPERIENCE (Aug 2016 – till now)

Position: Device Driver Developer Trainee

Organization: Arasan Chips and Systems Tuticorin TamilNadu

UFS2.1 IP WITH ASSOCIATED DRIVER STACK

This project aims to provide IP solutions for UFS2.1 Mobile Storage and developing associated driver stacks.

Roles Played:

- 1) Testing ufs2.1 IP with ufs2.1 driver stack
- 2) Debugging the driver code.

PRESENTATIONS

- **2nd prize** in inter-national level project presentation **ASTHIRAM** for the **Mini-Project**.
- Attended national level Embedded Conference on **LabVIEW** in AUTT Tirunelveli.

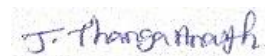
JOURNALS PUBLISHED

‘TCAD Simulation of Single-Electron Transistor with Recessed Channel’, International Journal of Applied Engineering Research, Vol.9, No.24: 23781-23788 (ISSN 0973-4562 - Impact Factor 1.833 - Annexure II).

‘Simulation of Single-Electron Transistor for Low Power VLSI circuits ’, International Journal of Applied Engineering Research, Vol.10, No.7: 16833-16842 (ISSN 0973-4562 - Impact Factor 1.833 - Annexure II).

I hereby declare that all the details given above are true to the best of my knowledge and belief.

PLACE: Tuticorin



(THANGA ANANTH.J)