ENHANCED VOLTAGE DOUBLER CIRCUIT USING IC555 TIMER: DESIGN AND APPLICATION

ECB1204 - ANALOG INTEGRATED CIRCUIT

A PROJECT REPORT

Submitted by

SHAKTHIVISESH N

THANISHKUMAR K

VISHAL A

in partial fulfillment for the award of the degree

of

BACHELOR OF ENGINEERING

in

ELECTRONICS AND COMMUNICATION ENGINEERING

K. RAMAKRISHNAN COLLEGE OF TECHNOLOGY

(An Autonomous Institution, Affiliated to Anna University Chennai and Approved by AICTE, New Delhi)

SAMAYAPURAM, TIRUCHIRAPPALLI – 621 112

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BONAFIDE CERTIFICATE

Certified that this project report titled "**ENHANCED VOLTAGE DOUBLER CIRCUIT USING IC555 TIMER: DESIGN AND APPLICATION"** is the bonafide work of **SHAKTHIVISESH N** (**2303811710621100**), **THANISHKUMAR K** (**2303811710621112), VISHAL A (2303811710621124)** who carried out the project under my supervision. Certified further, that to the best of my knowledge the work reported herein does not from part of any other project report or dissertation on the basis of which a degree or award was conferred on an earlier occasion on this or any other candidate**.**

SIGNATURE

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Submitted for the viva-voce examination held on ……

INTERNAL EXAMINER **EXTERNAL EXAMINER**

DECLARATION

We jointly declare that the project report on "**ENHANCED VOLTAGE DOUBLER CIRCUIT USING IC555 TIMER: DESIGN AND APPLICATION"** is the result of original work done by us and best of our knowledge, similar work has not been submitted to "ANNA UNIVERSITY CHENNAI" for the requirement of Degree of BACHELOR OF ENGINEERING. This project report is submitted on the partial fulfillment of the requirement of the award of Degree of BACHELOR OF ENGINEERING.

Signature

SHAKTHIVISESH N

THANISHKUMAR K

VISHAL A

Place : Samayapuram

Date :

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