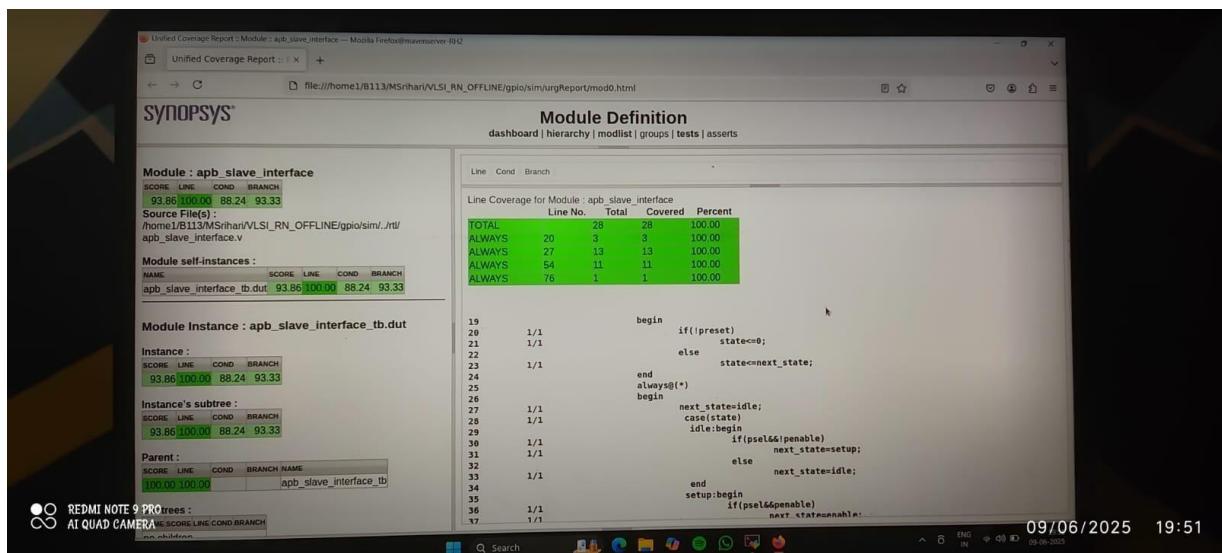
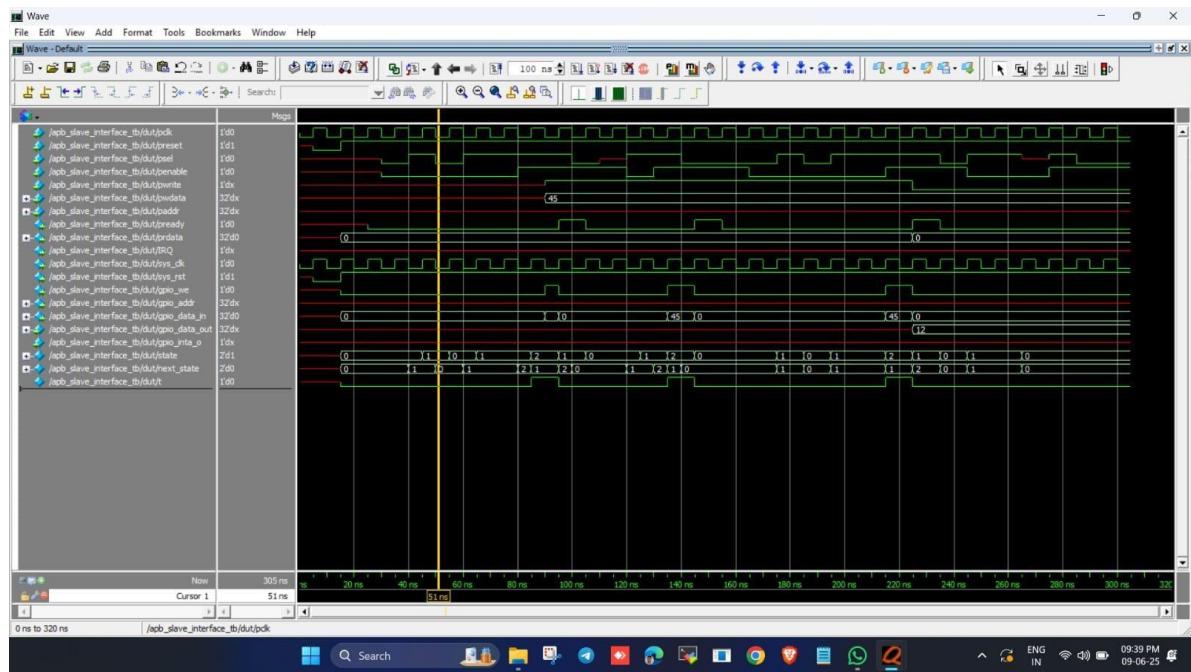


APB-Slave Interface

Code Coverage Report



Simulation



Lint Report

```
report_lint_apb_slave_interface.txt (~/VLSI_RN_OFFLINE/gpio/linting) - GVIM@mavenserver-RH2
File Edit Tools Syntax Buffers Window Help
[Icons]

Report Summary
-----
Product Info
Name : VC Static Master Shell
Version : T-2022.06 -- May 30, 2022

Report Info
Created : Jun 09, 2025 19:45:17

TopLevelModule: apb_slave_interface

Management Summary
-----


| Stage            | Family | Fatals | Errors | Warnings | Infos |
|------------------|--------|--------|--------|----------|-------|
| LANGUAGE_CHECK   | CODING | 0      | 0      | 0        | 1     |
| STRUCTURAL_CHECK | CODING | 0      | 0      | 0        | 15    |
| Total            |        | 0      | 0      | 0        | 16    |



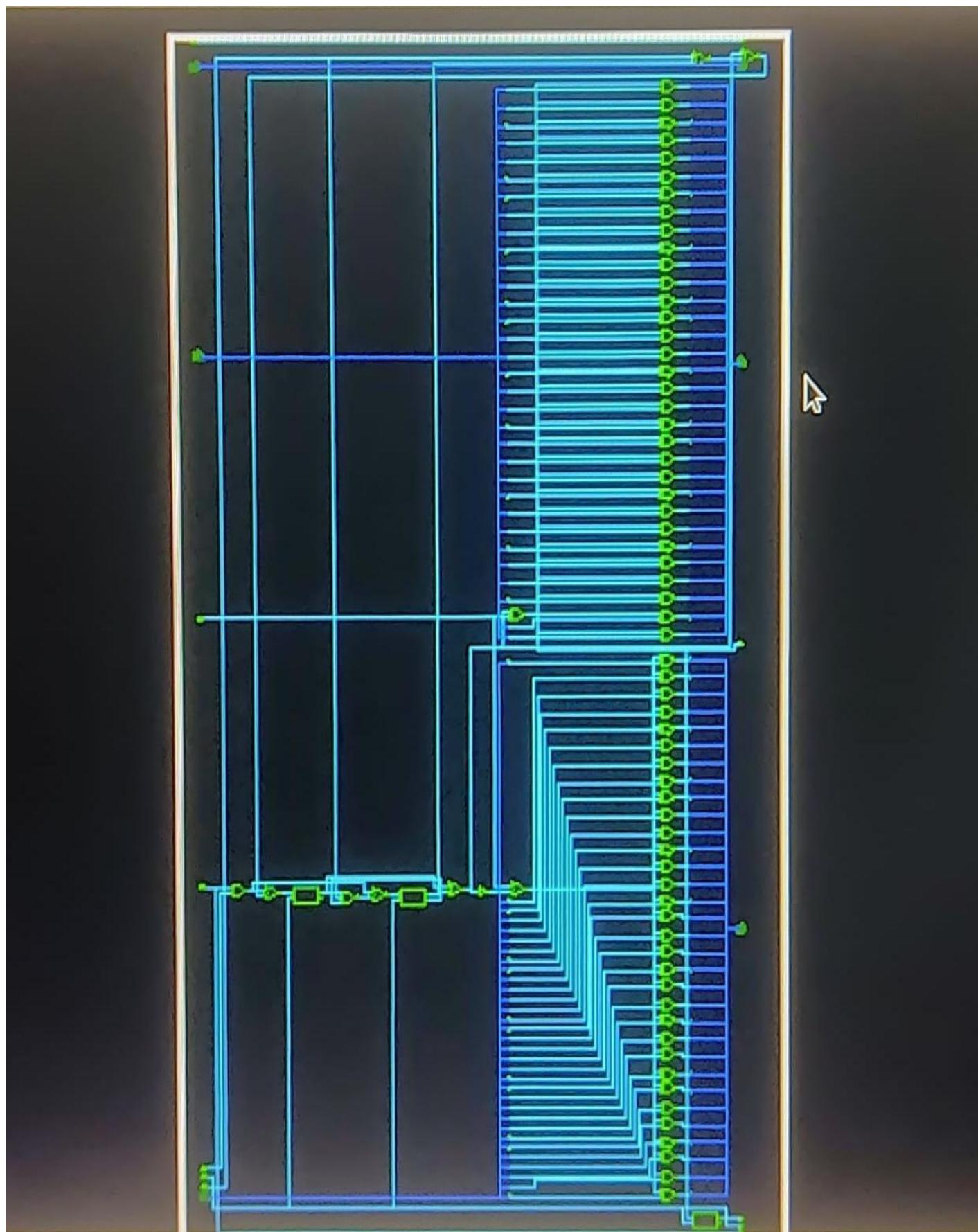
Tree Summary
-----


| Severity | Stage            | Tag               | Count |
|----------|------------------|-------------------|-------|
| info     | LANGUAGE_CHECK   | ReportPortInfo-ML | 1     |
| info     | STRUCTURAL_CHECK | RegInputOutput-ML | 15    |
| Total    |                  |                   | 16    |



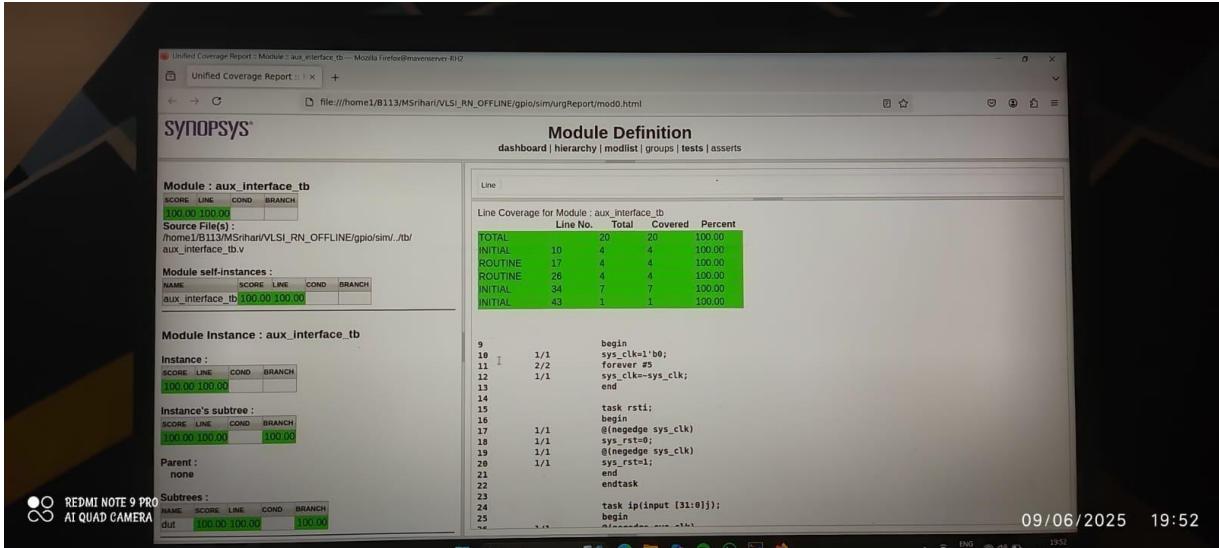
RegInputOutput-ML (15 infos/0 waived)
-----
Tag : RegInputOutput-ML
Description : Port '[Signal]' is not registered [Hierarchy: '[DesignObj]
```

Synthesis

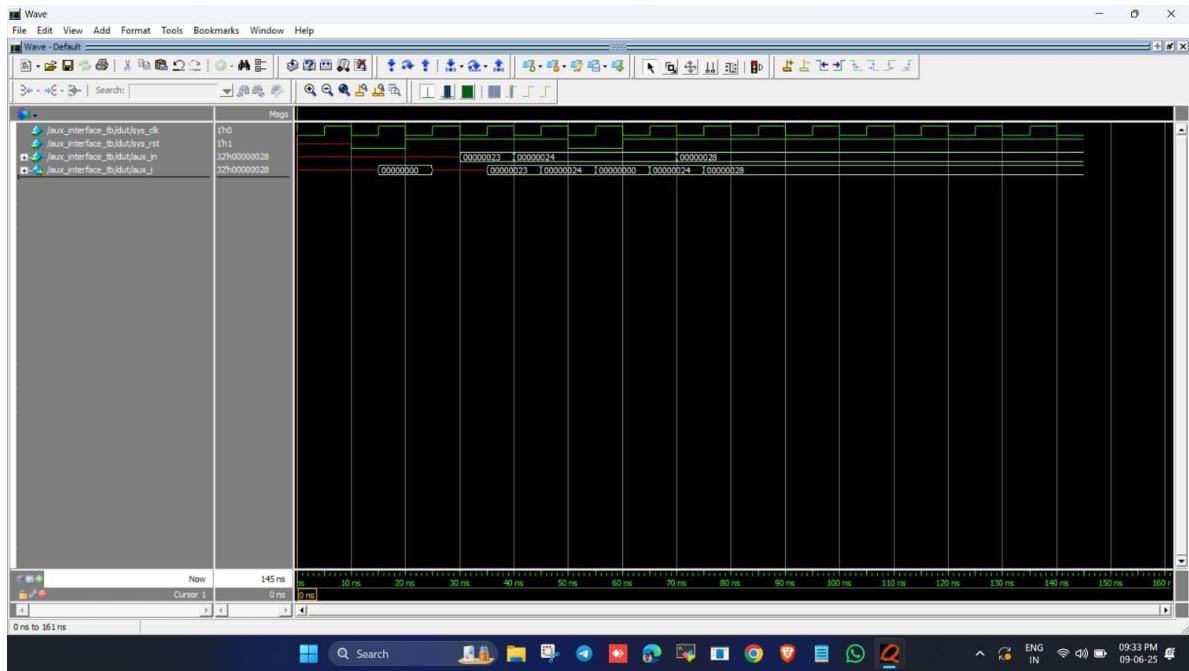


AUX Interface

Code Coverage Report



Simulation



Lint Report

report_lint_aux_interface.txt (~VLSI_RN_OFFLINE/gpio/linting) - GVIM@mavenserver-RH2

File Edit Tools Syntax Buffers Window Help

Report Summary

Product Info

Name : VC Static Master Shell
Version : T-2022.06 -- May 30, 2022

Report Info

Created : Jun 09, 2025 19:47:10

TopLevelModule: aux_interface

Management Summary

Stage	Family	Fatals	Errors	Warnings	Infos
LANGUAGE_CHECK	CODING	0	0	0	1
STRUCTURAL_CHECK	CODING	0	0	0	2
Total		0	0	0	3

Tree Summary

Severity	Stage	Tag	Count
info	LANGUAGE_CHECK	ReportPortInfo-ML	1
info	STRUCTURAL_CHECK	RegInputOutput-ML	2
Total			3

RegInputOutput-ML (2 infos/0 waived)

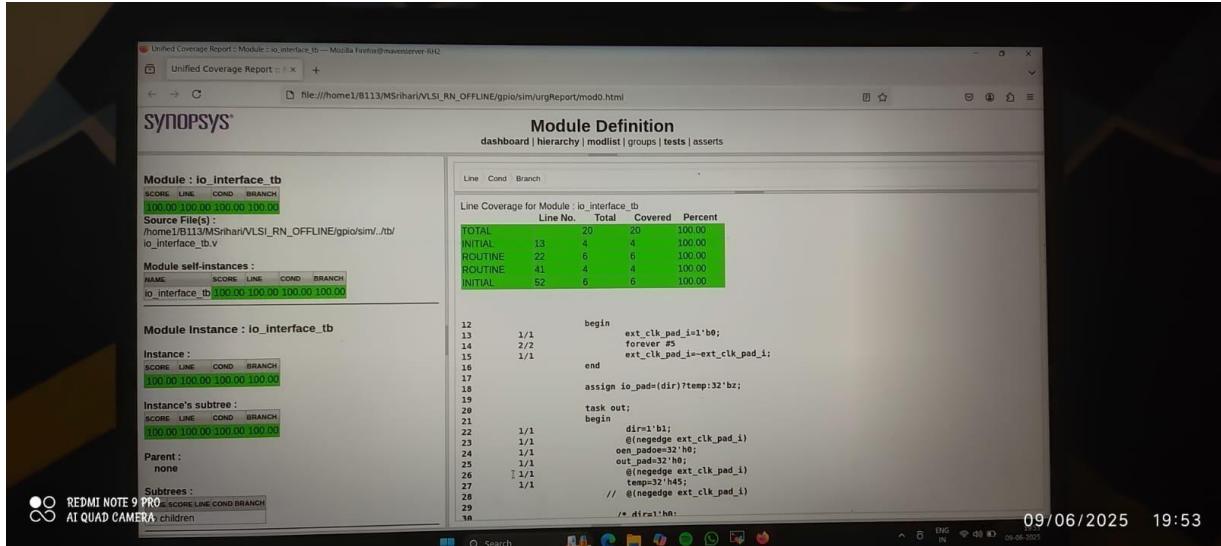
Tag	Description
RegInputOutput-ML	: Port '[Signal]' is not registered [Hierarchy: '[Des

Synthesis

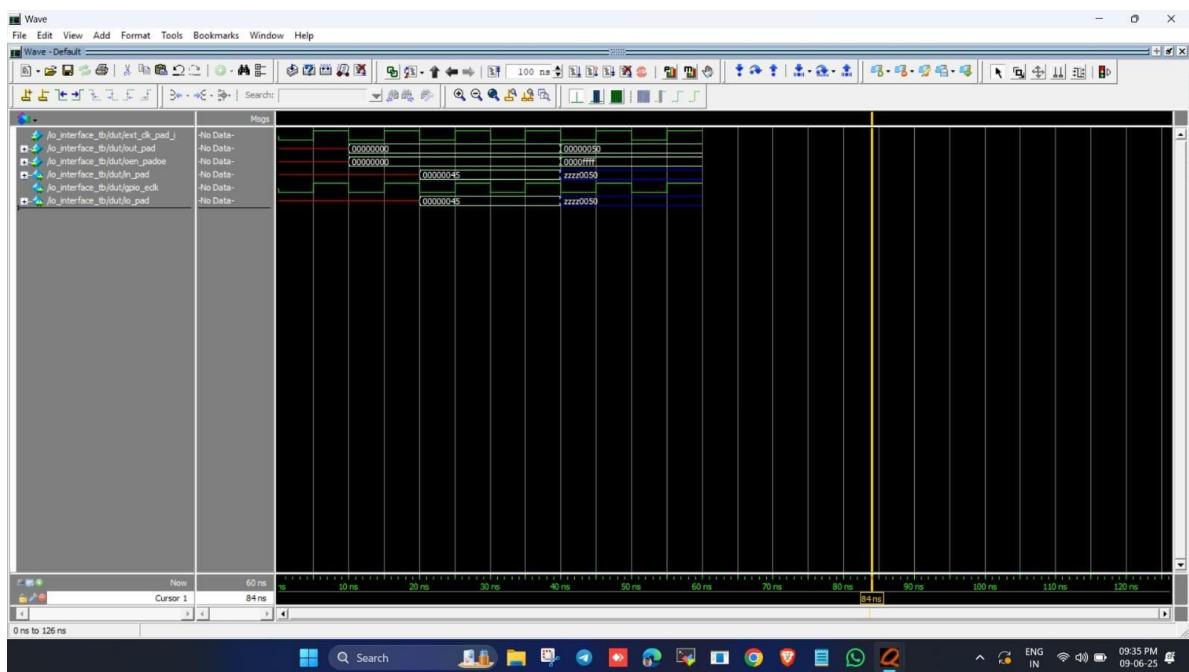


IO_Interface

Code Coverage Report



Simulation



Lint Report

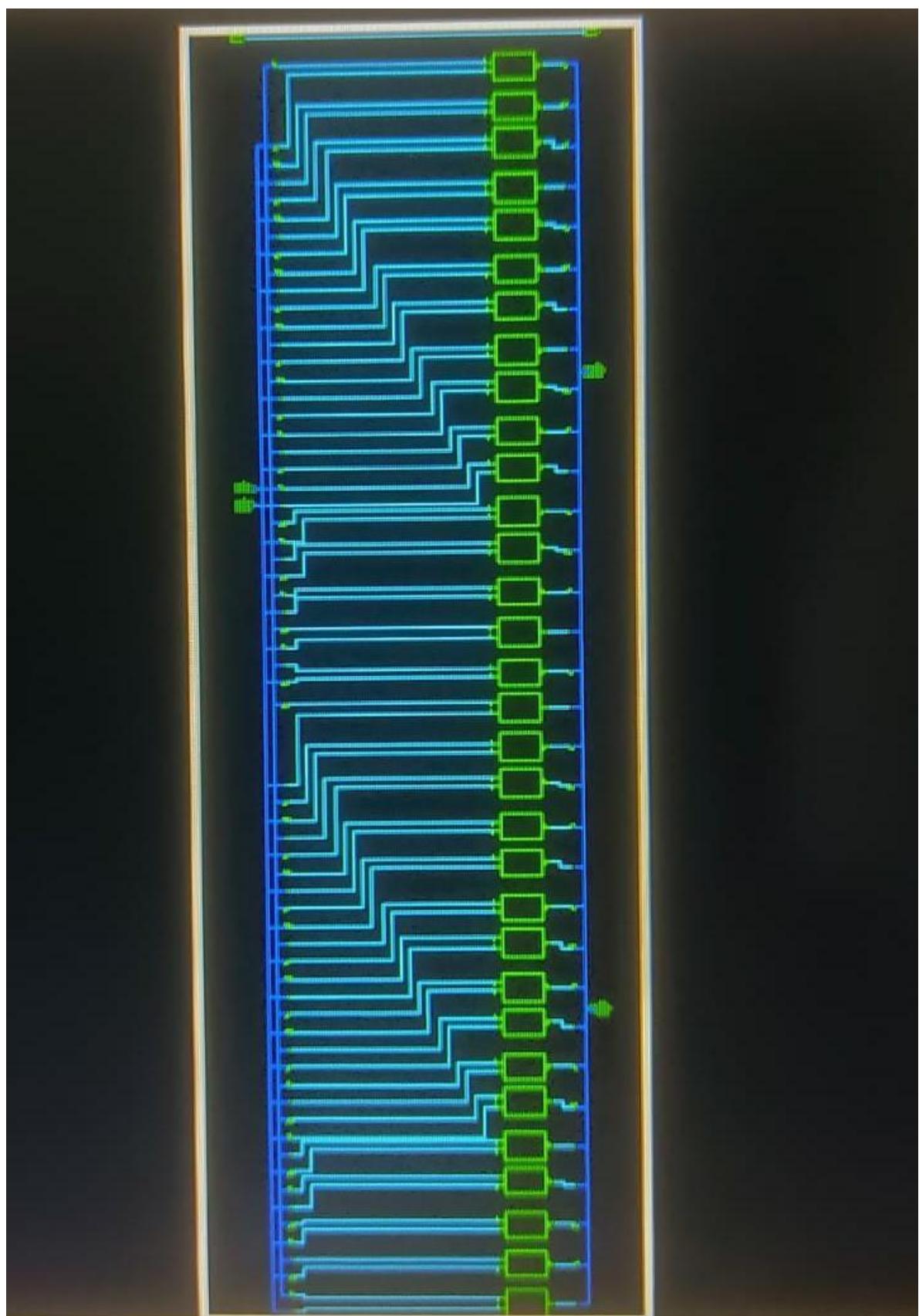
```
report_lint_io_interface.txt (~VLSI_RN_OFFLINE/gpio/linting) - GVIM@mavenserver-RH2
File Edit Tools Syntax Buffers Window Help
Report Summary
Product Info
Name : VC Static Master Shell
Version : T-2022.06 -- May 30, 2022
Report Info
Created : Jun 09, 2025 19:48:22
TopLevelModule: io_interface

Management Summary
Stage Family Fatals Errors Warnings Infos
LANGUAGE_CHECK CODING 0 0 0 1
STRUCTURAL_CHECK CODING 0 0 0 5
Total 0 0 0 6

Tree Summary
Severity Stage Tag Count
info LANGUAGE_CHECK ReportPortInfo-ML 1
info STRUCTURAL_CHECK RegInputOutput-ML 5
Total 6

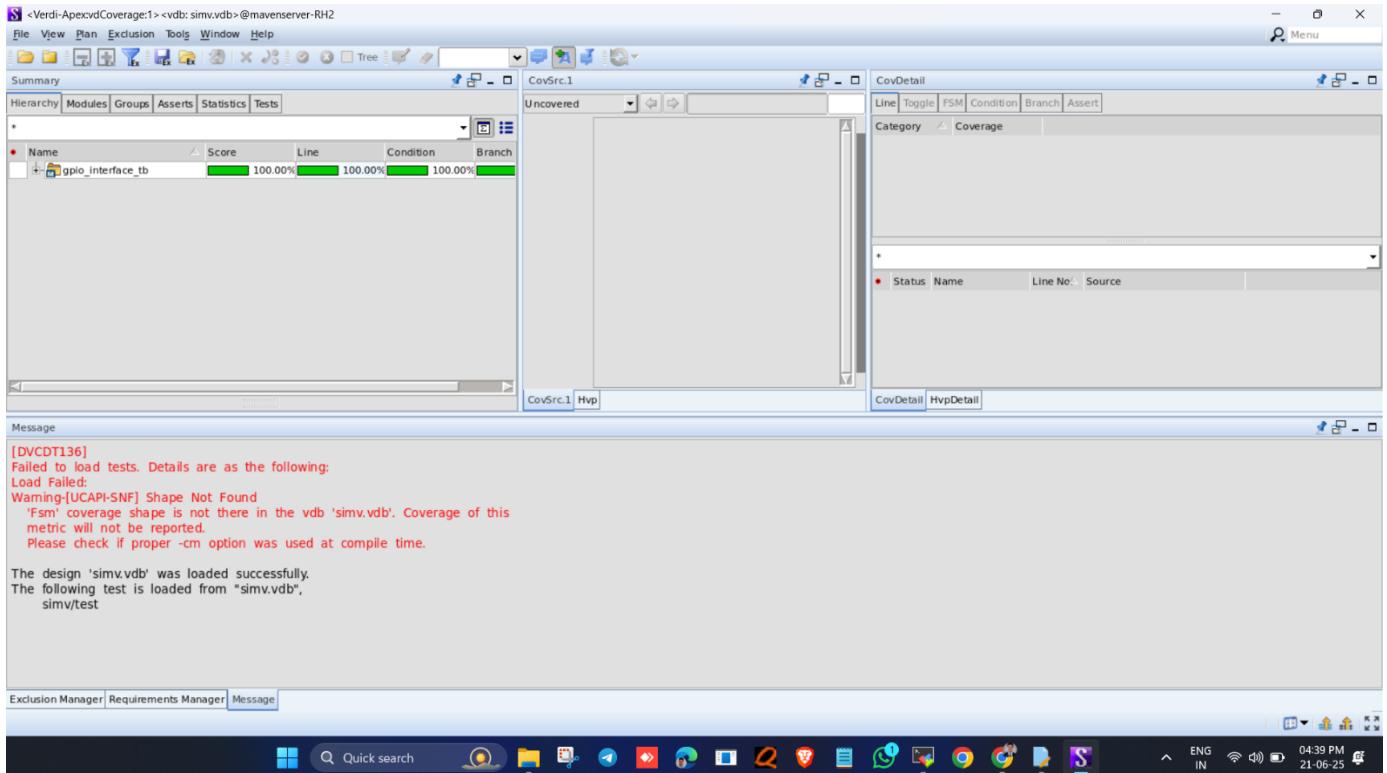
RegInputOutput-ML (5 infos/0 waived)
Tag : RegInputOutput-ML
Description : Port '[Signal]' is not registered [Hierarchy: '[DesignObjHi]
```

Synthesis

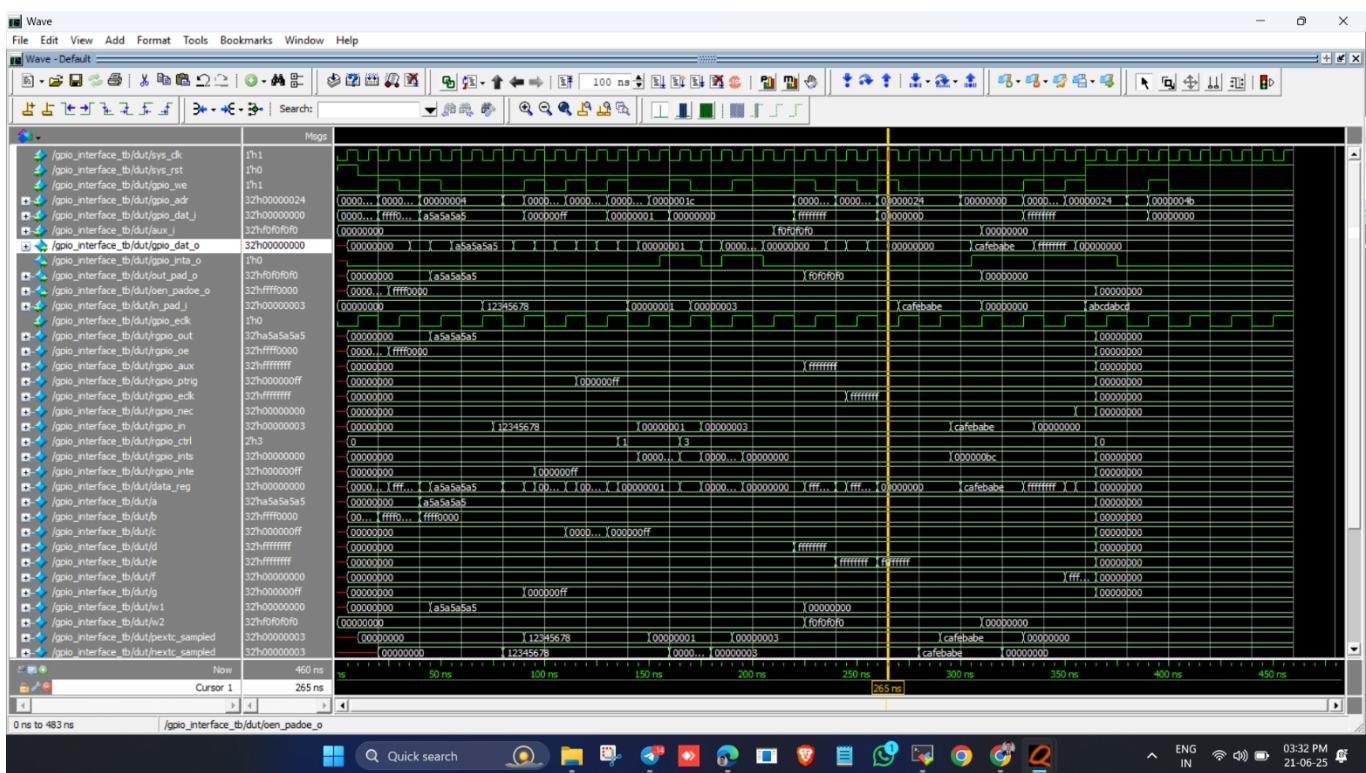


GPIO Registers

Code Coverage Report



Simulation



Lint Report

```
report_lint_gpio_interface.txt (~VLSI_RN_OFFLINE/gpio/linting) - GVIM@mavenserver-RH2
File Edit Tools Syntax Buffers Window Help
Report Summary
Product Info
Name : VC Static Master Shell
Version : T-2022.06 -- May 30, 2022
Report Info
Created : Jun 21, 2025 16:45:18
TopLevelModule: gpio_interface

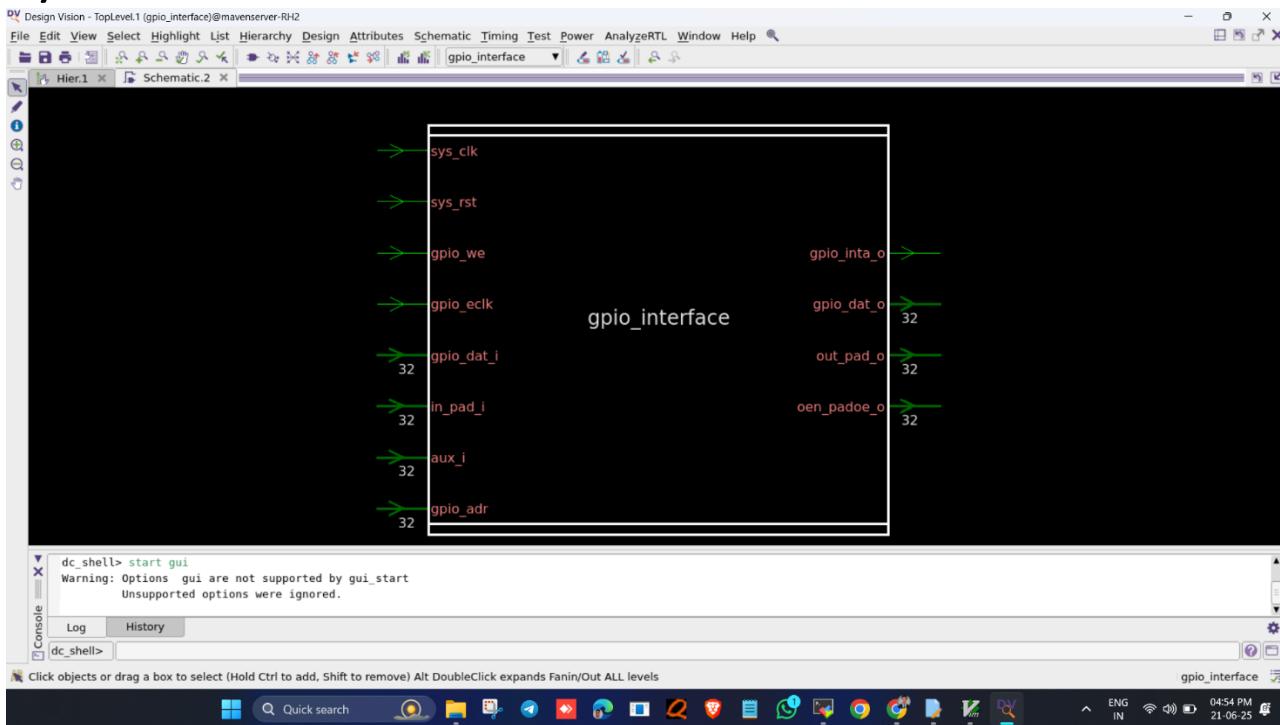
Management Summary
Stage Family Fatal Errors Warnings Infos
---- -----
LANGUAGE_CHECK CODING 0 0 0 1
STRUCTURAL_CHECK CODING 0 0 0 7
---- -----
Total 0 0 0 8

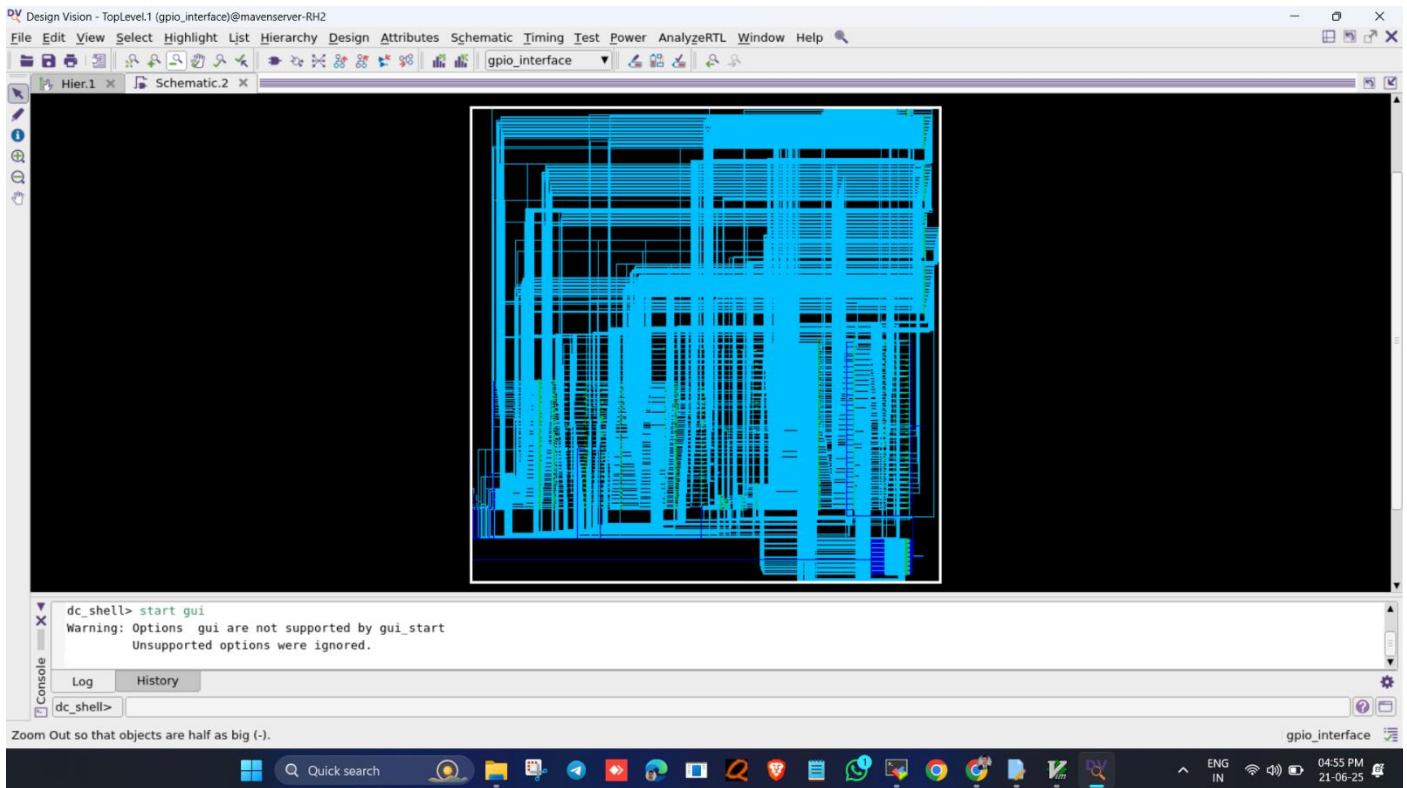
Tree Summary
Severity Stage Tag Count
---- -----
info LANGUAGE_CHECK ReportPortInfo-ML 1
info STRUCTURAL_CHECK RegInputOutput-ML 7
---- -----
Total 8

RegInputOutput-ML (7 infos/0 waived)
Tag : RegInputOutput-ML
Description : Port '[Signal]' is not registered [Hierarchy: '[DesignObjHierarchy]']

36,0-1 Top
Quick search 04:46 PM 21-06-25 ENG IN
```

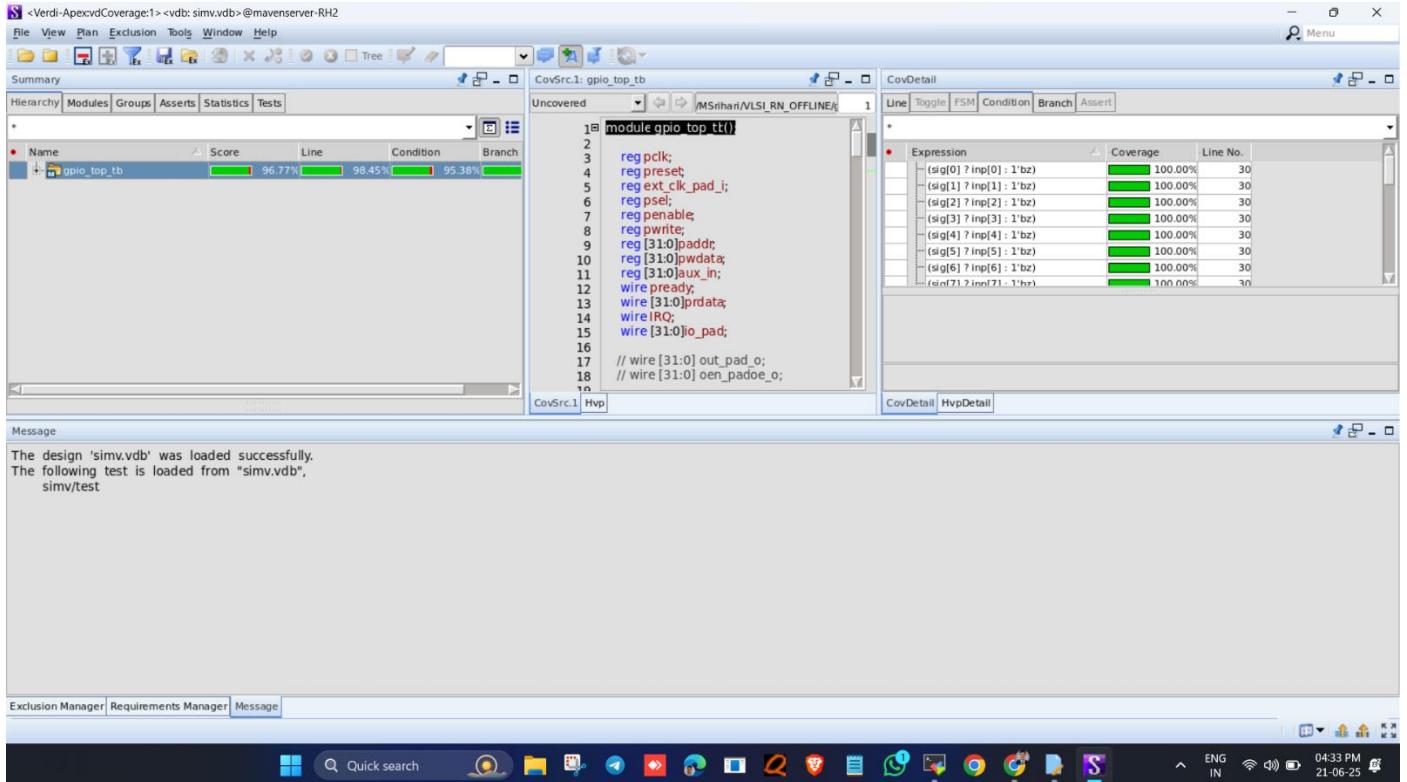
Synthesis



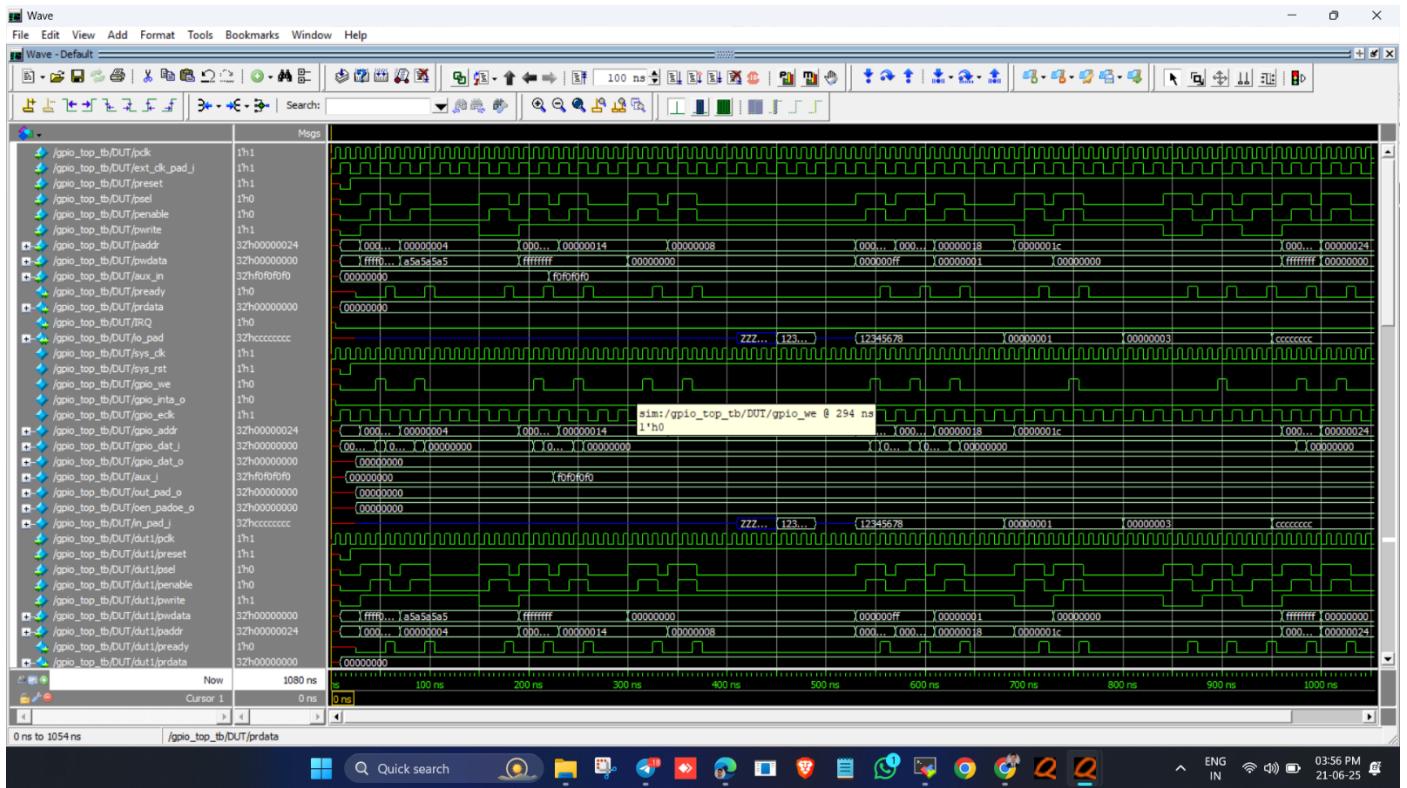


GPIO Top

Code Coverage Report



Simulation



Lint Report

report_lint_gpio_top.lint.txt (~\VLSI_RN_OFFLINE\gpio\linting) - GVIM@mavenserver-RH2

File Edit Tools Syntax Buffers Window Help

Report Summary

Product Info

Name : VC Static Master Shell
Version : T-2022.06 -- May 30, 2022

Report Info

Created : Jun 21, 2025 16:48:20

TopLevelModule: gpio_top

Management Summary

Stage	Family	Fatals	Errors	Warnings	Infos
---	---	---	---	---	---
LANGUAGE_CHECK	CODING	0	0	0	1
STRUCTURAL_CHECK	CODING	0	0	0	8
---	---	---	---	---	---
Total		0	0	0	9

Tree Summary

Severity	Stage	Tag	Count
---	---	---	---
info	LANGUAGE_CHECK	ReportPortInfo-ML	1
info	STRUCTURAL_CHECK	RegInputOutput-ML	8
---	---	---	---
Total			9

RegInputOutput-ML (8 infos/0 waived)

Tag : RegInputOutput-ML
Description : Port '[Signal]' is not registered [Hierarchy: '[DesignObjHierarchy]']

36,0-1 Top

Synthesis

