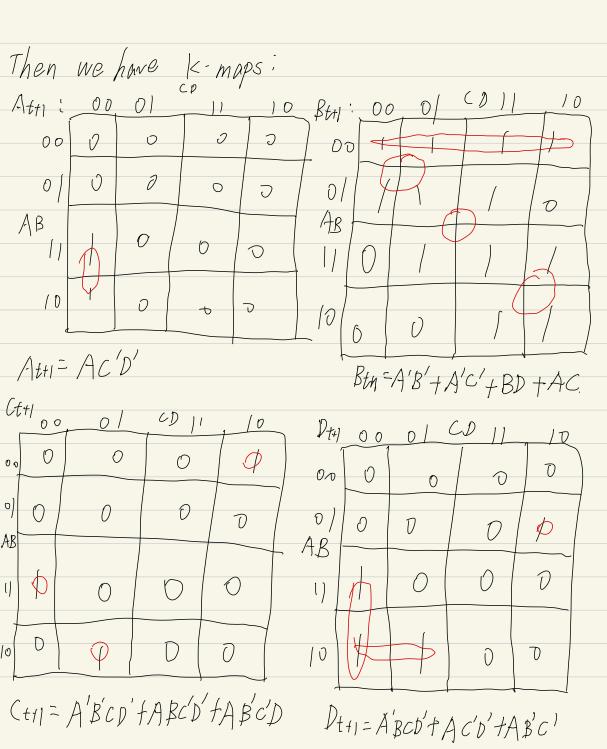
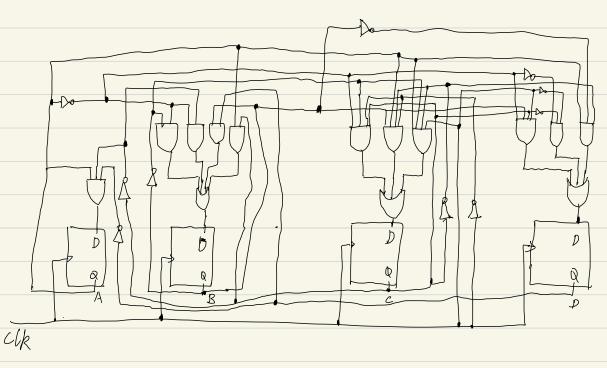
First we have touth table:



we have the circuit:



2, Let D, C, B, A be the outputs of the 4-bot binary ripple counter. ive have the truth table Deti Cti Ben Ati De Ce Be At 0 V 00/ 1 2/ 10 17  $\chi$   $\chi$ XXXX

We look at the truth table and found that if and only of Dt and Bt were both 1, Dtt1, Ct+1, Bt+1, At+1 Will be reset to 0000 Hence, we have the circuot: asynchronous clear 4-bit binary Vipple counter ARCD

First, we have the truth table. Atti Btillti Dti At Bt Ct Dt JAKA JBKB JCKC JOKO Out. O X O X I X I X O 0 000 0 0 1 0 / 1 X X X X / 1 00 0 1 0  $\chi \circ | \circ |$ 0 1 1 0 0 10  $X \mid X \mid X \mid$ 0 0 11 0 1 0 0 0 100 0 X 1 X 1 X  $\begin{pmatrix} 0 \end{pmatrix}$ / X X X X / 101 110 1618 0 0 X 110 χſ  $X \mid X$ / / / 000 0 X X X X X X X X 1000 1 0 0 | 100/ X O X O O X 1010 X 0 X/X/I 1 D [] D X ( X 1 (0/ 0 ) 1/00 XXXOO 0 / 1 / 0 / 110/ O 1 ( ( 0 χ ) 0 1 ) 0/ [110

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