

I was assigned the following devices for removal:

- dma-controller@ffa80000
- dma-controller@ffa90000
- dma-controller@ffaa0000
- dma-controller@ffab0000
- dma-controller@ffac0000
- dma-controller@ffad0000
- dma-controller@ffae0000
- dma-controller@ffaf0000

These are the DMA channels in the low-power domain.

There are two types of DMAs on the hardware board we are utilizing:

Processing-side (PS) DMAs - non-FPGA DMAs

<https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/18842528/Zynqmp+and+Versal+DMA>

Programmable-logic (PL) DMAs - FPGA DMAs

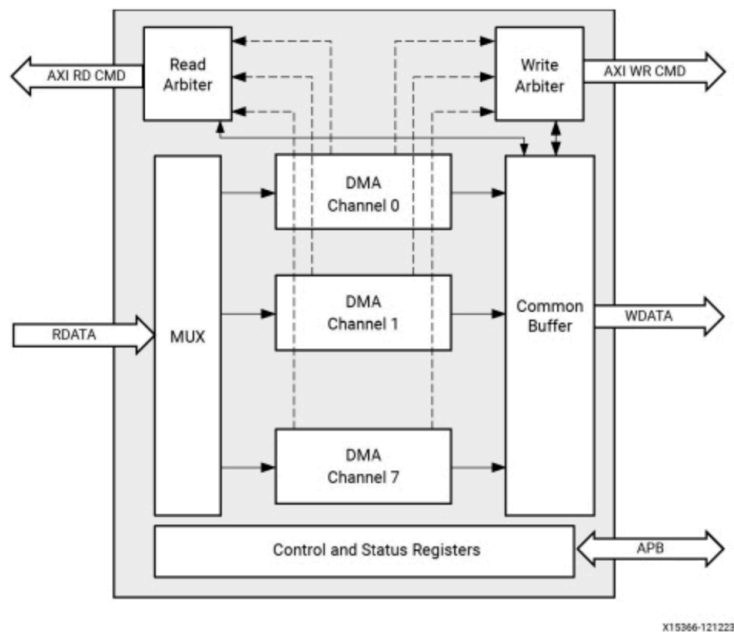
<https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/18842337/Linux+Soft+DMA+Driver#LinuxSoftDMA+Driver-Introduction>

However, the product will only use PS DMAs. On the PS side, there are two DMAs:

Full-power domain (FPD) DMA

Low-power domain (LPD) DMA

Both the FPD and LPD DMA have 8 channels that will be tested.



My goal is to perform regression testing on the FPD to ensure it works as intended, while verifying the LPD DMA is disabled.