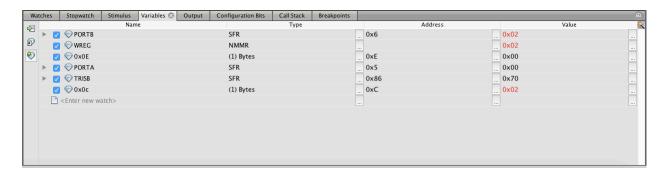
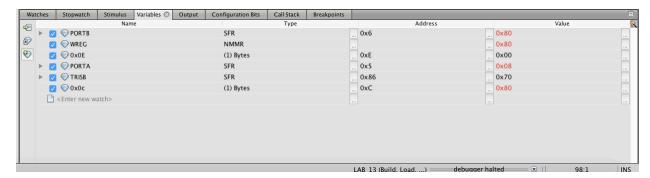
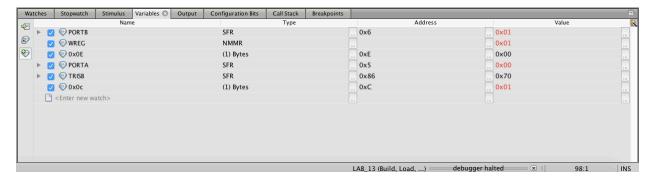
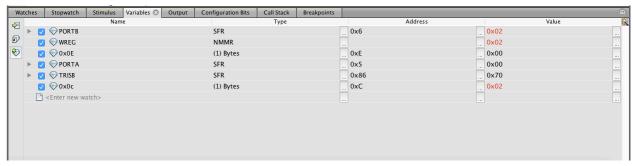


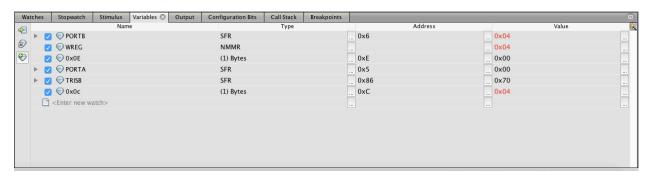
Wa	tches	Stopwatch	Stimulus	Variables 🔕	Output	Configuration Bits	Call Stack	Breakpoints					0
4			Name				Type			Address		Value	1
	▶ .	OPPORTB				SFR			0x6		0x04		
	U	WREG				NMMR					0x04		
0	U	2 ⊕ 0x0E				(1) Bytes			0xF		0x00		
	▶ .	O PORTA				SFR			0x5		0×00		
	▶ .	TRISB				SFR			0x86		0×70		
	U	2 ⊕ 0x0c				(1) Bytes			0xC		0x04		
		<enter new="" td="" wa<=""><td>itch></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></enter>	itch>										

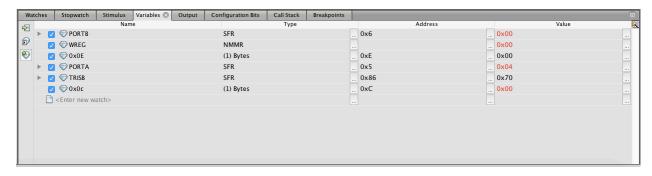


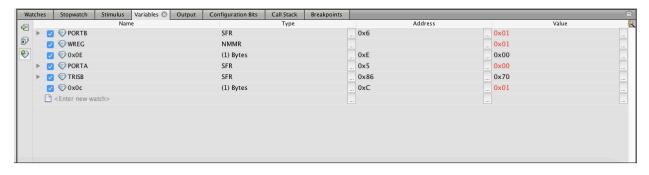


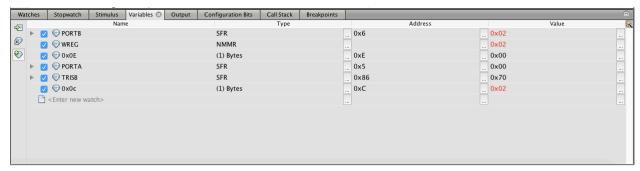


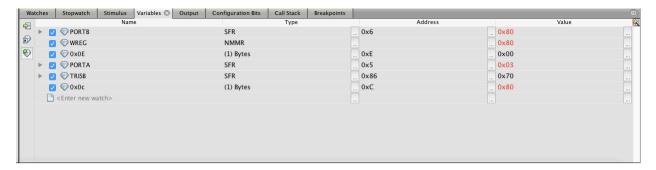


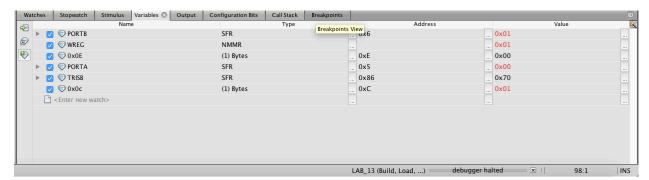


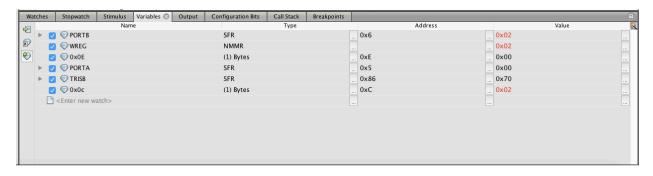


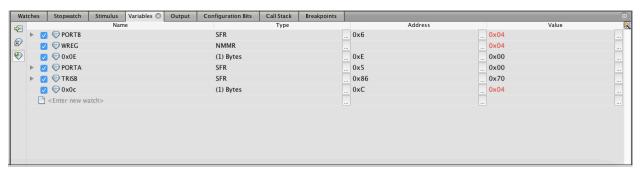












Wa	tches	Stopwatch	Stimulus	Variables 🛇	Output	Configuration Bits	Call Stack	Breakpoints			
4			Nam	ne .			Type			Address	Value 🔯
	>	PORTB				SFR			(0x6	0x08
a		✓ WREG				NMMR					0x08
@		✓ ⊕ 0x0E				(1) Bytes				0xE	0x00
	▶	✓ ⊕ PORTA				SFR			(0x5	0×00
	▶	✓ TRISB				SFR			(0x86	0x70
		✓ ⊕ 0x0c				(1) Bytes			(0xC	0x08
		CEnter new wa	atch>								