Flow

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Project							
Name:	Flow	**Creation time:**	6/9/2021 1:44:20 AM	**Last change**	12/3/2022 10:40:53 AM	**Author:**	home
Last modified by:	daveb	**Version:**					
Comment:							

Operating system	
Name	**Description**
Operating system	Microsoft Windows 11 Home Single Language
Version of the operating system	6.3.9600.0
Operating system service pack	
Version of the Internet Explorer	11.1.22621.0
Computer name	VU_THACH
User name	VU_THACH\daveb
Installation path of the TIA Portal	C:\Program Files\Siemens\Automation\Portal V16

Components		
Name	**Version**	**Release**
TIA Portal Project Server V16 - TIA Portal Project Server Single SetupPackage V16.0 (MUSERVERV16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - SIMATIC S7-PLCSIM V16.0 (S7_PLCSIM_V16)	V16.0	V16.00.00.00_31.00.13.01
TIA Administrator - AWB Licensing Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - AWB Software Management V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - TIA UMC Agent Configurator Module V1.0 + SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
TIA Administrator - TIA Administrator V1.0 SP2 (TIAADMIN)	V1.0 + SP2	V01.00.02.00_01.10.00.01
Siemens Totally Integrated Automation Portal V16 - HM All Editions Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - HM NoBasic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 0 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Multiuser Client Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Version Control Interface SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - STEP 7 Safety Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - STEP 7 Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 03 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package 04 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Support Base Package TO-02 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - Hardware Support Base Package WCF-01 V16.0 (TIAP16)	V16.0	V16.00.00.00_27.01.00.01
Siemens Totally Integrated Automation Portal V16 - TIACOMPCHECK Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Openness SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Openness SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Transfer Mandatory Single SetupPackage V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
User Management Component - UserManagementComponentx64 V2.7 (UMC64)	V2.7	V02.07.00.00_04.06.00.07
WinCC Runtime Advanced V16.0 - HMIRTM Tagging Package 01 Single SetupPackage V16.0 (HMIRTM_V11)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - Simatic Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
Siemens Totally Integrated Automation Portal V16 - WinCC Single SetupPackage 32 Bit V16.0 (TIAP16)	V16.0	V16.00.00.00_31.02.00.01
SIMATIC HMI License Manager Panel Plugin (x64)	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC WinCC Runtime Advanced Driver (x64)	16.0.0.0	V16.00.00.00_31.02.00.01
ETWEventCollector	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC NCM FWL 64	5.6.0.3	K5.6.0.3_1.1.0.2
NCM GPRS 64	01.02.00.00	V1.2.0.0_2.1.0.1
SIMATIC PLCSIM 64	16.00.00	16.00.00.00_01.00.02.01
SIMATIC Device Drivers	9.2	09.02.04.00_01.04.00.05
TelemetryConnector	1.0.2.57	V01.00.02.57_01.00.00.01
Automation Software Updater	02.05.0300	V02.05.03.00_01.01.00.29
SIMATIC HMIProvider	7.0	K07.00.03.01_01.01.00.01
SIEMENS OPC	3.9	03.09.10.00_01.04.00.08
SIMATIC WinCC OPC Alarm & Events Server	3.9	03.09.09.00_01.09.00.01
SIMATIC WinCC OPC Data Access Server	3.9	03.09.09.00_01.09.00.01
SIMATIC WinCC OPC Historical Data Access Server	3.9	03.09.09.00_01.09.00.01
SIMATIC WinCC OPC XML Client	3.9	03.09.09.00_01.09.00.01

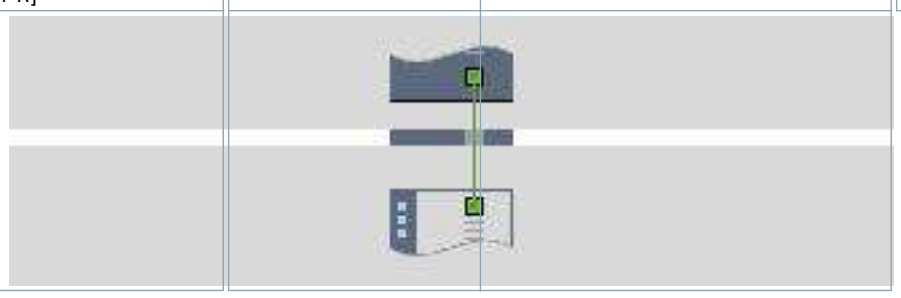
Name	Version	Release
PCS7 Common Classes	9.0	09.00.01.00_00.03.00.07
SIMATIC HMI ProSave	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC HMI Symbol Library	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC HMI Touch Input	16.0.0.0	V16.00.00.00_31.02.00.01
SIMATIC Runtime Interfaces	2.1	K02.01.00.03_01.01.00.01
SIMATIC Version View	1.7.10.0	K1.7.10.0_1.1.0.1
SIMATIC Device Drivers WoW	29.2	29.02.04.00_01.04.00.05
SIMATIC Event Database	5.6	05.06.02.00_01.01.00.01
SeCon	2.6	V02.06.01.00_01.08.00.01
SIMATIC Station Observer	K7.3.1.0	V07.03.01.00_01.01.00.14
SIMATIC SCS	K7.5.2.0	V07.05.02.00_01.21.00.02
SIMATIC WinCC Common Archiving	V7.5.0.0	V07.05.00.00_01.39.00.03
WinCC Runtime Advanced Simulator	16.0.0.0	V16.00.00.00_31.02.00.01

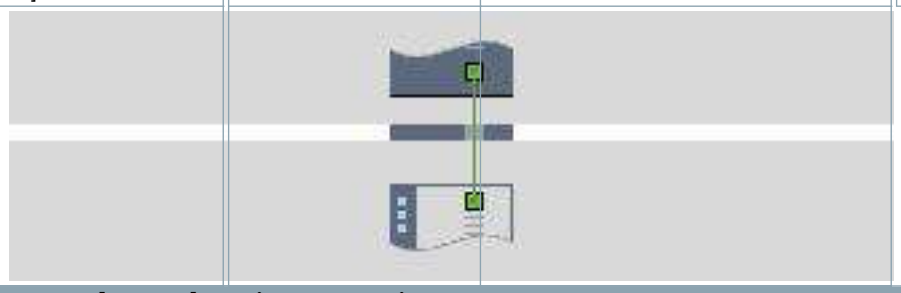
Products		
Name	**Version**	**Release**
TIA Portal Project Server	V16.0	V16.00.00.00_31.02.00.01
SIMATIC S7-PLCSIM	V16.0	V16.00.00.00_31.00.13.01
TIA Administrator	V1.0	01.00.02.00_01.10.00.01
SIMATIC STEP 7 Prof - STEP 7 Safety - WinCC Adv	V16.0	V16.00.00.00_31.02.00.01
User Management Component	V2.7	V02.07.00.00_00.00.00.00
SIMATIC WinCC Runtime Advanced Simulation	V16.0	V16.00.00.00_31.02.00.01
Automation License Manager	V6.0 + SP5 + Upd1	06.00.05.01_02.01.00.05
FORDM		
S7-PLCSIM	V5.4 + SP8	V05.04.08.01_01.24.00.01
SIMATIC ProSave	V16.0	V16.00.00.00_31.02.00.01
S7-PCT	V3.5 + SP1	K3.5.1.0_1.19.0.1
WinCC Runtime	V7.5	V07.05.00.00_01.39.00.03
WinCC Configuration	V7.5	V07.05.00.00_01.39.00.03
WinCC OPC Server	V3.9 + SP9	03.09.09.00_01.09.00.01
WinCC OPC-UA Client	V1.1	01.01.00.00_01.28.00.01
WinCC OPC-UA Server	V1.0 + SP7	01.00.07.00_01.14.00.01
SIMATIC WinCC Smart Tools	V7.5	V07.05.00.00_01.39.00.03

Flow

PLC_1 [CPU 314C-2 PN/DP]

PLC_1					
General					
Name	PLC_1	**Author**	home	**Comment**	
Rack	0	**Slot**	2		
General\Catalog information					
Short designation	CPU 314C-2 PN/DP	**Description**	Work memory 192KB; 0.6ms/1000 instructions; DI24/DO16; AI5/AO2 integrated; 4 pulse outputs (2.5kHz); 4 channels counting and measuring with 24 V (60kHz) incremental encoders; integrated positioning function; PROFINET interface and 2 Ports; MRP; PROFINET CBA; PROFINET CBA Proxy; TCP/IP transport protocol; combined MPI/DP interface (MPI or DP master or DP slave); multi-tier configuration up to 31 modules; capable of sending and receiving in direct data exchange; constant bus cycle time; routing; firmware V3.3	**Article number**	6ES7 314-6EH04-0AB0
Firmware version	V3.3				
General\Identification & Maintenance					
Plant designation		**Location identifier**			
MPI/DP interface [X1]\General					
Name	MPI/DP interface_1	**Comment**			
MPI/DP interface [X1]\MPI address\Interface networked with					
Subnet:	Not networked				
MPI/DP interface [X1]\MPI address\Parameters					
Interface type:	Mpi	**Address:**	2	**Highest address:**	
Transmission speed:					
MPI/DP interface [X1]\Time-of-day synchronization\SIMATIC mode					
Type of synchronization	None	**Time interval**	None		
MPI/DP interface [X1]\Diagnostics addresses\Diagnostics addresses					
Start address	2047				
PROFINET interface [X2]\General					
Name	PROFINET interface_1	**Comment**			
PROFINET interface [X2]\Ethernet addresses\Interface networked with					
Subnet:	Not connected				
PROFINET interface [X2]\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.1	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface [X2]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_1
Converted name:	plcxb1d0ed	**Device number:**	0		
PROFINET interface [X2]\Time-of-day synchronization\NTP mode					
Enable time synchronization via NTP server	False		IP addresses	**Server 1**	0.0.0.0
Server 2	0.0.0.0	**Server 3**	0.0.0.0	**Server 4**	0.0.0.0
Update interval	10s				
PROFINET interface [X2]\Operating mode					
IO controller	True	**IO system**		**Device number**	0
IO device	False				
PROFINET interface [X2]\Advanced options\Interface options					
Call the user program if communication errors occur	False	**Support device replacement without exchangeable medium**	True	**Use IEC V2.2 LLDP mode**	True
Keep-Alive connection monitoring:	30s				
PROFINET interface [X2]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X2]\Advanced options\Real time settings\Synchronization					
RT class:	RT,IRT				
PROFINET interface [X2]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%		
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\General					
Name	Port_1	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X2]\Port_1 [X2 P1 R]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Diagnostics addresses\Diagnostics addresses					
Start address	2045				
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\General					
Name	Port_2	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X2]\Port_2 [X2 P2 R]	**Medium:**	Copper	**Cable name:**	---
					
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Diagnostics addresses\Diagnostics addresses					
Start address	2044				
PROFINET interface [X2]\Diagnostics addresses\Diagnostics addresses					
Start address	2046				
DI 24/DO 16\General					
Name	DI 24/DO 16_1	**Comment**			
DI 24/DO 16\General\Catalog information					
Short designation	DI 24/DO 16	**Description**	Digital input/output DI24 + DO16		
DI 24/DO 16\Inputs\Channel group 0 - 3					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Rising (positive) edge					
Rising (positive) edge	False				

DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Rising (positive) edge	
Rising (positive) edge	False

DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\I/O addresses\Input addresses					
Start address	136.0	**End address**	138.7	**Process image**	OB1-PI
Interrupt OB number	40				
DI 24/DO 16\I/O addresses\Output addresses					
Start address	136.0	**End address**	137.7	**Process image**	OB1-PI
AI 5/AO 2\General					
Name	AI 5/AO 2_1	**Comment**			
AI 5/AO 2\General\Catalog information					
Short designation	AI 5/AO 2	**Description**	Analog I/O AI5 + AO2		
AI 5/AO 2\Inputs					
Temperature unit	Degrees Celsius				
AI 5/AO 2\Inputs\Channel 0					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 1					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 2					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 3					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 4					
Measurement type	Resistor (2-wire)	**Measuring range**	600 ohmsOhm		
AI 5/AO 2\Outputs\Output 0					
Output type	Voltage	**Output range**	+/- 10V		
AI 5/AO 2\Outputs\Output 1					
Output type	Voltage	**Output range**	+/- 10V		
AI 5/AO 2\I/O addresses\Input addresses					
Start address	800	**End address**	809	**Process image**	None
Interrupt OB number	40				
AI 5/AO 2\I/O addresses\Output addresses					
Start address	800	**End address**	803	**Process image**	None
Count\General					
Name	Count_1	**Comment**			
Count\General\Catalog information					
Short designation	Count	**Description**	4 channels; counting and frequency measurement at 60 kHz, pulse width modulation at 2.5 kHz switching frequency		
Count\Interrupt selection					
Interrupt selection	None				
Count\Channel 0					
Operating mode	Not configured				
Count\Channel 1					
Operating mode	Not configured				
Count\Channel 2					
Operating mode	Not configured				
Count\Channel 3					
Operating mode	Not configured				
Count\I/O addresses\Input addresses					
Start address	816	**End address**	831	**Process image**	None
Interrupt OB number	40				
Count\I/O addresses\Output addresses					
Start address	816	**End address**	831	**Process image**	None
Positioning\General					
Name	Positioning_1	**Comment**			
Positioning\General\Catalog information					
Short designation	Positioning	**Description**	1 channel; positioning with analog and digital outputs, counting frequency		
Positioning\Interrupt selection					
Interrupt selection	None				
Positioning\Channel 0					
Operating mode	None				
Positioning\I/O addresses\Input addresses					
Start address	832	**End address**	847	**Process image**	None
Interrupt OB number	40				
Positioning\I/O addresses\Output addresses					
Start address	832	**End address**	847	**Process image**	None
Startup					
Startup if preset configuration does not match actual configuration	True	**Startup after POWER ON**	Warm restart		
Startup\Monitoring time for					
Ready message from modules	650x 100 ms	**Parameter transfer to modules**	100x 100 ms		

Cycle					
Cycle monitoring time	150ms	**Cycle load due to communication**	20%	**Size of the process image input:**	256
Size of the process image output:	256	**OB85 call if I/O access error occurs**	No OB85 call		
Clock memory					
Clock memory	False	**Memory byte**	0		

Interrupts\Isochronous mode interrupts

OB number	Priority	Distributed I/O	Process image partition(s)	Delay time (ms)	Automatic setting
OB 61	25	0		0.000	True

Interrupts\Isochronous mode interrupts\OB 61

Application cycle:	0ms	**Delay time:**	0.000ms	**Automatic setting**	True
Distributed I/O:	0				

Interrupts\Isochronous mode interrupts\OB 61\Process image partition

PIP:	

Interrupts\Time-of-day interrupts

OB number	Priority	Active	Execution	Start time
OB 10	2	False	None	1994-01-01 00:00:00.000

Interrupts\Time-delay interrupts

OB number	Priority	Process image partition(s)
OB 20	3	None
OB 21	4	None

Interrupts\Cyclic interrupts

OB number	Priority	Execution	Phase offset	Unit
OB 32	9	1000	0	ms
OB 33	10	500	0	ms
OB 34	11	200	0	ms
OB 35	12	500	0	ms

Interrupts\Hardware interrupts

OB number	Priority
OB 40	16

Interrupts\Interrupts for DPV1

OB number	Priority
OB 55	2
OB 56	2
OB 57	2

Interrupts\Asynchronous error interrupts

OB number	Priority
OB 82	26
OB 83	26
OB 85	26
OB 86	26
OB 87	26

Retentive memory					
Number of memory bytes starting at MB 0	16	**Number of S7 timers starting at T 0**	0	**Number of S7 counters starting at C 0**	8
Protection					
Password		**Confirm password**			
Protection					
Level of protection	No protection				
Protection\ \Can be canceled with password					
Can be canceled with password	False				
Diagnostics system					
Report cause of STOP	True	**Number of alarms in the diagnostics buffer**	10		
System diagnostics\General					
Activate system diagnostics for this device	True				
System diagnostics\Diagnostic support					
Query for status "activated/deactivated" after startup	False	**Send alarm if status changes from/to activated or deactivated**	False		

Additional blocks for diagnostic data	Create	Block name	Block number
Diagnostic status DB:	True	RSE_DIAGNOSTIC_STATUS_DB	127

System diagnostics\System diagnostic blocks

System diagnostic blocks	Block name	Block number
FB:	RSE_FB	49
DB:	RSE_DB	49
Global DB:	RSE_GLOBAL_DB	50
FC:	RSE_FC	49

Time of day					
Correction factor	0ms				
Time of day\Synchronization on PLC					
Type of synchronization	None	**Time interval**	None		
Time of day\Synchronization on MPI					
Type of synchronization	None	**Time interval**	None		
Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	False		
Web server\Automatic update					
Enable automatic update	False	**Update interval**	0s		

Web server\Languages		
Active	**Web server language**	**Assign project language**
False	German	None
False	English	None
False	French	None
False	Spanish	None
False	Italian	None
False	Japanese	None
False	Chinese (simplified)	None

Web server\User management	
User name	**User rights**
Everybody	

Web server\User-defined web pages					
Application name	**HTML source path**	**Default HTML page**	**Files with dynamic content**	**Web DB number**	**Fragment DB number**
		index.htm	.htm;.html	333	334

Web server\Text_Display_classes_of_messages	
Display class	**Active**
0	True
1	True
2	True
3	True
4	True
5	True
6	True
7	True
8	True
9	True
10	True
11	True
12	True
13	True
14	True
15	True
16	True

Connection resources					
PG communication:	1	**OP communication:**	1	**S7 basic communication:**	0
S7 communication:	0	**Maximum number of S7 connection resources:**	12		

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I*	2047	2047	MPI/DP interface_1	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X1
I*	2046	2046	PROFINET interface_1	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X2
I*	2045	2045	Port_1	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X2 P1 R
I*	2044	2044	Port_2	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X2 P2 R
I	136	138	DI 24/DO 16_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	3 Bytes	-	0	2 5
O	136	137	DI 24/DO 16_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	2 Bytes	-	0	2 5
I	800	809	AI 5/AO 2_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	10 Bytes	-	0	2 6
O	800	803	AI 5/AO 2_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	4 Bytes	-	0	2 6
I	816	831	Count_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 7
O	816	831	Count_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 7
I	832	847	Positioning_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 8
O	832	847	Positioning_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 8
I	256	259	AI 2x12BIT_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	4 Bytes	-	0	4
O	272	275	AO 2x12BIT_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	4 Bytes	-	0	5

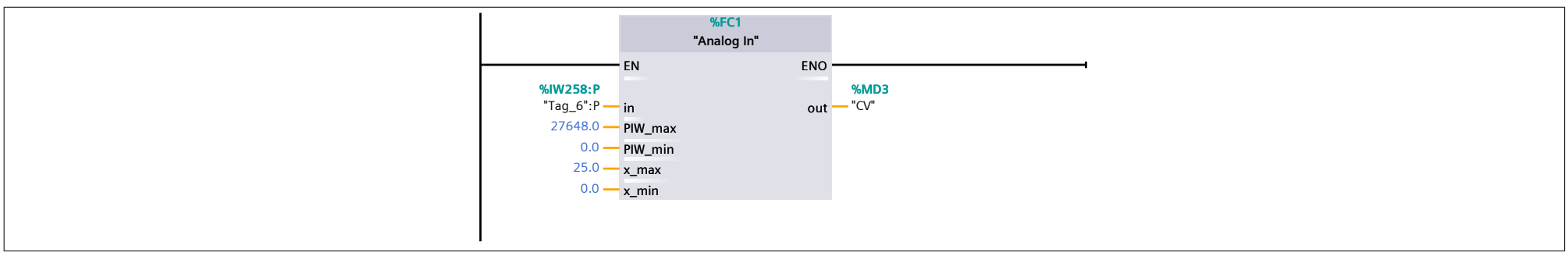
Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

Main [OB1]

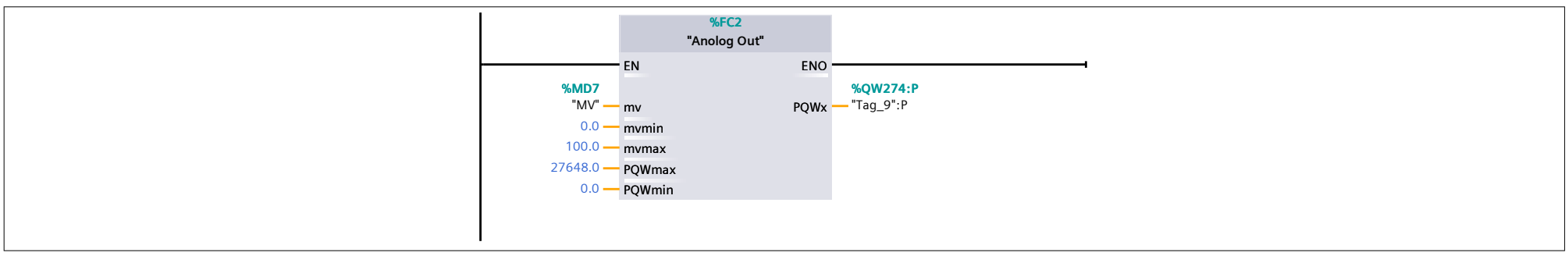
Main Properties							
General							
Name	Main	**Number**	1	**Type**	OB	**Language**	LAD
Numbering	Manual						
Information							
Title	"Main Program Sweep (Cycle)"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB1_EV_CLASS	Byte	0.0		Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)
OB1_SCAN_1	Byte	1.0		1 (Cold restart scan 1 of OB 1), 3 (Scan 2-n of OB 1)
OB1_PRIORITY	Byte	2.0		Priority of OB Execution
OB1_OB_NUMBR	Byte	3.0		1 (Organization block 1, OB1)
OB1_RESERVED_1	Byte	4.0		Reserved for system
OB1_RESERVED_2	Byte	5.0		Reserved for system
OB1_PREV_CYCLE	Int	6.0		Cycle time of previous OB1 scan (milliseconds)
OB1_MIN_CYCLE	Int	8.0		Minimum cycle time of OB1 (milliseconds)
OB1_MAX_CYCLE	Int	10.0		Maximum cycle time of OB1 (milliseconds)
OB1_DATE_TIME	Date_And_Time	12.0		Date and time OB1 started
Constant				

Network 1:

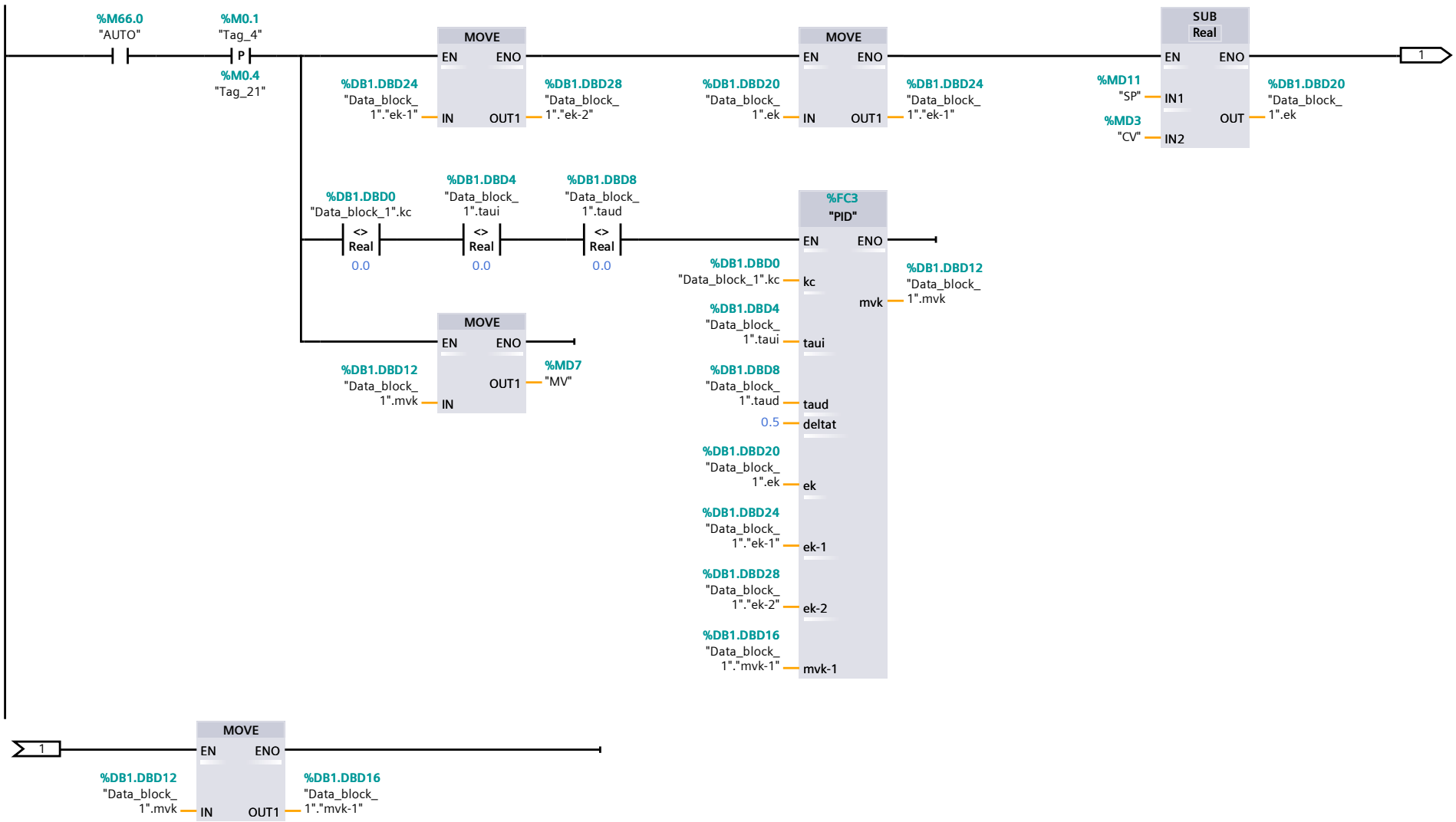


Network 2:

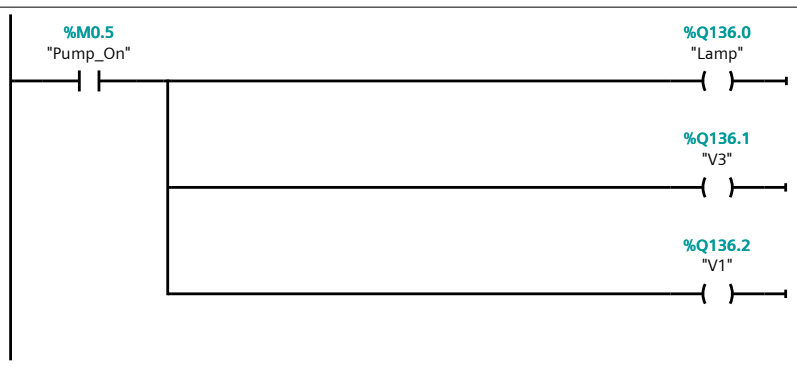


Network 3:

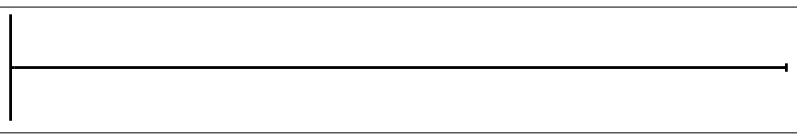
Network 3:



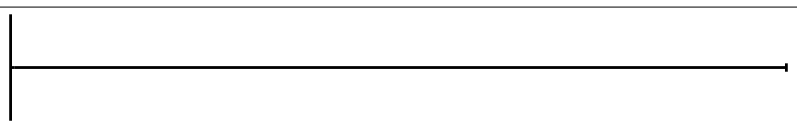
Network 4:



Network 5:



Network 6:



Network 7: Call system diagnostics block

```
0001        CALL  "RSE_FB", "RSE_DB"
```

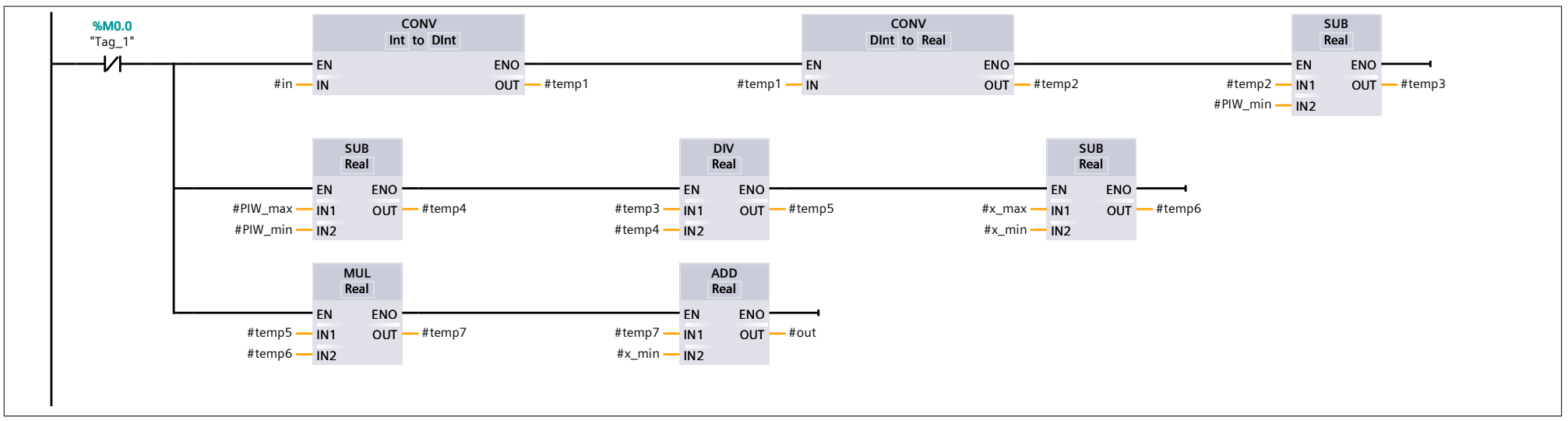
Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

Analog In [FC1]

Analog In Properties							
General							
Name	Analog In	**Number**	1	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Input				
in	Int			
PIW_max	Real			
PIW_min	Real			
x_max	Real			
x_min	Real			
▼ Output				
out	Real			
InOut				
▼ Temp				
temp1	DInt	0.0		
temp2	Real	4.0		
temp3	Real	8.0		
temp4	Real	12.0		
temp5	Real	16.0		
temp6	Real	20.0		
temp7	Real	24.0		
Constant				
▼ Return				
Analog In	Void			

Network 1:



Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

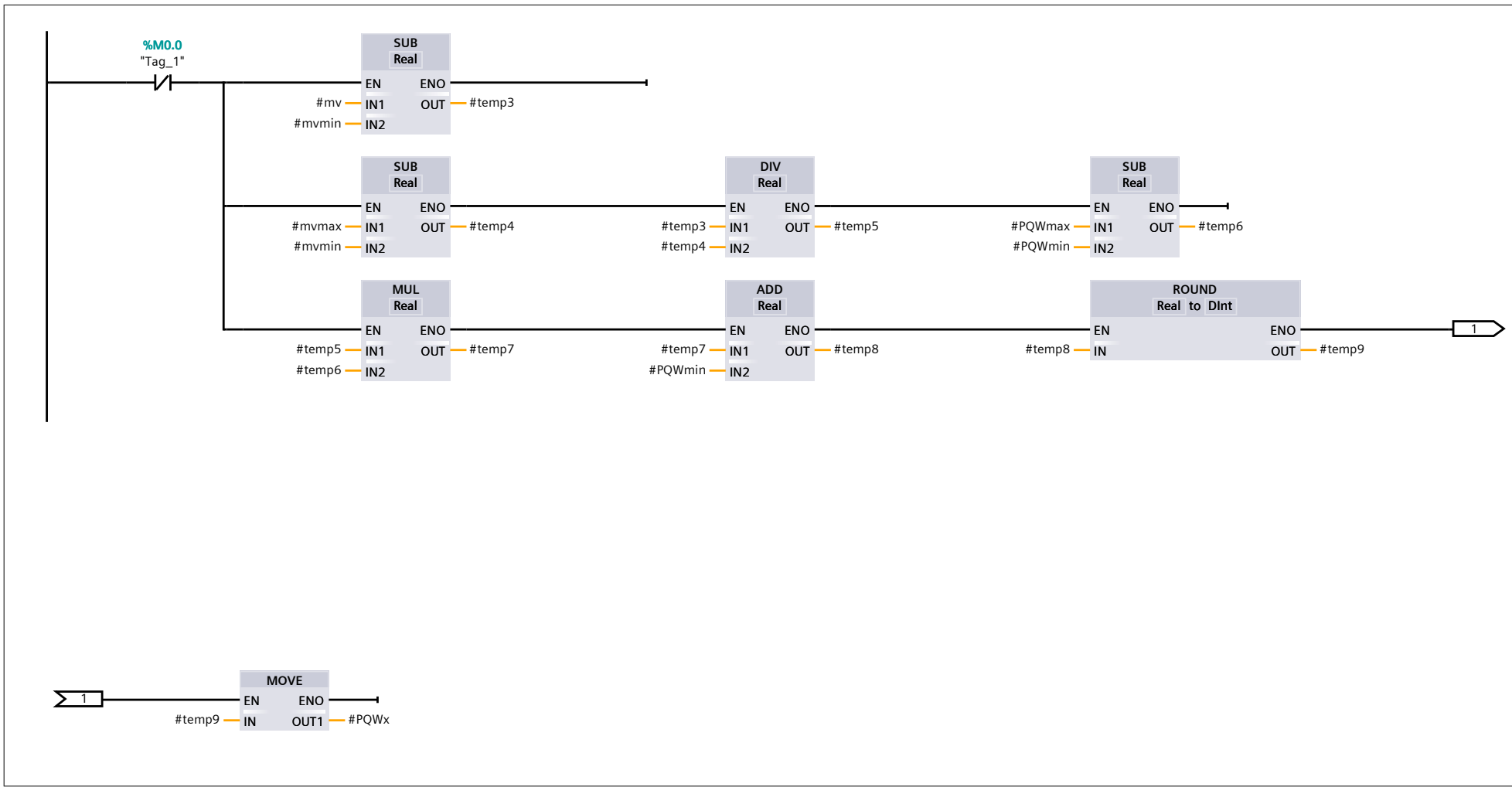
Anolog Out [FC2]

Anolog Out Properties							
General							
Name	Anolog Out	**Number**	2	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Input				
mv	Real			
mvmin	Real			
mvmax	Real			
PQWmax	Real			
PQWmin	Real			
▼ Output				
PQWx	Int			
InOut				
▼ Temp				
temp3	Real	0.0		
temp4	Real	4.0		
temp5	Real	8.0		
temp6	Real	12.0		
temp7	Real	16.0		
temp8	Real	20.0		
temp9	DInt	24.0		
Constant				
▼ Return				
Anolog Out	Void			

Network 1:

Network 1:



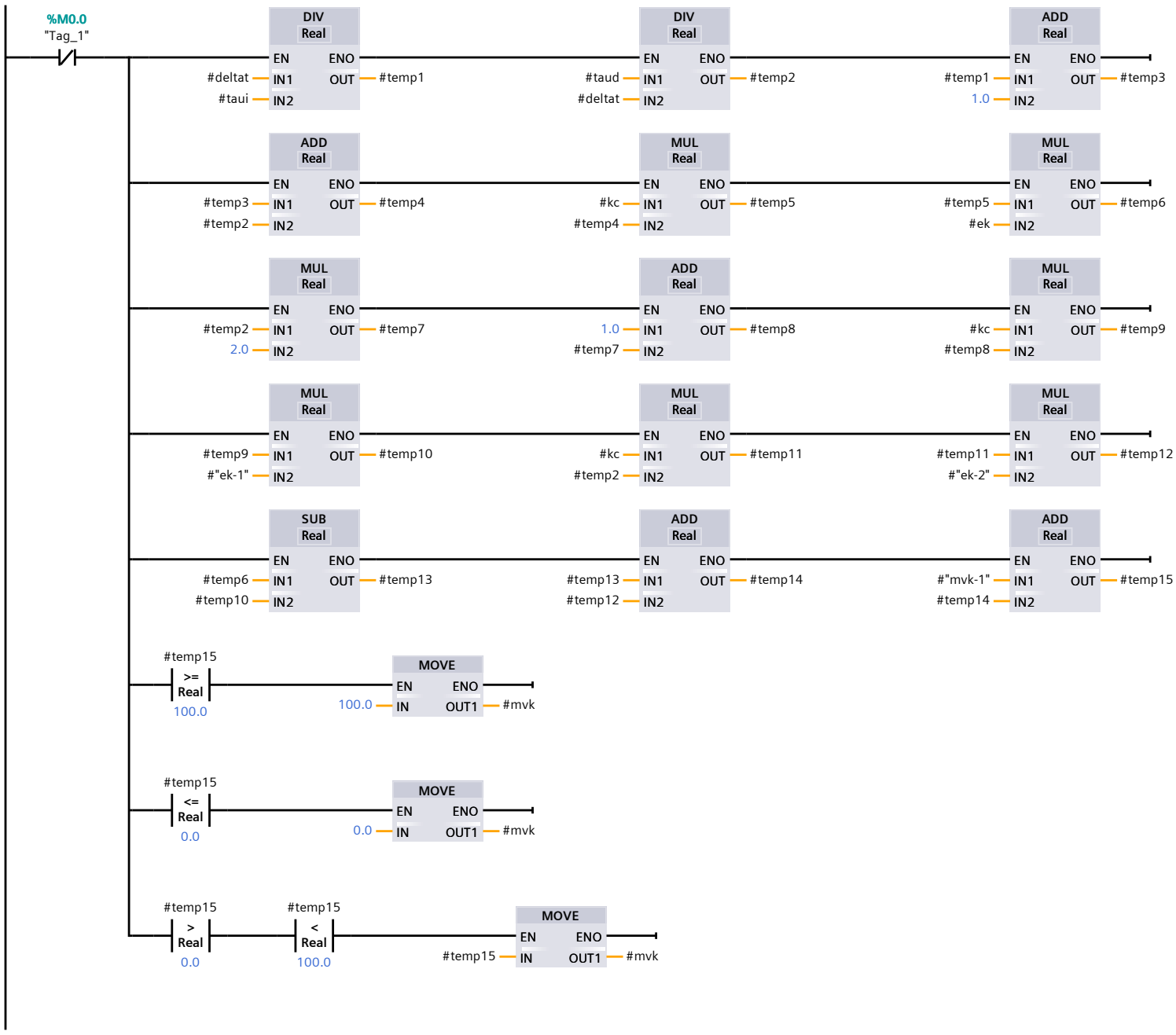
Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

PID [FC3]

PID Properties							
General							
Name	PID	**Number**	3	**Type**	FC	**Language**	LAD
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Input				
kc	Real			
taui	Real			
taud	Real			
deltat	Real			
ek	Real			
ek-1	Real			
ek-2	Real			
mvk-1	Real			
▼ Output				
mvk	Real			
InOut				
▼ Temp				
temp1	Real	0.0		
temp2	Real	4.0		
temp3	Real	8.0		
temp4	Real	12.0		
temp5	Real	16.0		
temp6	Real	20.0		
temp7	Real	24.0		
temp8	Real	28.0		
temp9	Real	32.0		
temp10	Real	36.0		
temp11	Real	40.0		
temp12	Real	44.0		
temp13	Real	48.0		
temp14	Real	52.0		
temp15	Real	56.0		
Constant				
▼ Return				
PID	Void			

Network 1:


%M0.0
"Tag_1"
DIV Real
#deltat
#taui
#temp1
DIV Real
#taud
#deltat
#temp2
ADD Real
#temp1
1.0
#temp3
ADD Real
#temp3
#temp2
#temp4
MUL Real
#kc
#temp4
#temp5
MUL Real
#temp5
#ek
#temp6
MUL Real
#temp2
2.0
#temp7
ADD Real
1.0
#temp7
#temp8
MUL Real
#kc
#temp8
#temp9
MUL Real
#temp9
#"ek-1"
#temp10
MUL Real
#kc
#temp2
#temp11
MUL Real
#temp11
#"ek-2"
#temp12
SUB Real
#temp6
#temp10
#temp13
ADD Real
#temp13
#temp12
#temp14
ADD Real
#"mvk-1"
#temp14
#temp15
#temp15
>= Real
100.0
MOVE
100.0
#mvk
#temp15
<= Real
0.0
MOVE
0.0
#mvk
#temp15
> Real
0.0
#temp15
< Real
100.0
MOVE
#temp15
#mvk

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

Data_block_1 [DB1]

Data_block_1 Properties							
General							
Name	Data_block_1	**Number**	1	**Type**	DB	**Language**	DB
Numbering	Automatic						
Information							
Title		**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
▼ Static										
kc	Real	0.0	0.0	True	True	True	True	False		
taui	Real	4.0	0.0	True	True	True	True	False		
taud	Real	8.0	0.0	True	True	True	True	False		
mvk	Real	12.0	0.0	True	True	True	True	False		
mvk-1	Real	16.0	0.0	True	True	True	True	False		
ek	Real	20.0	0.0	True	True	True	True	False		
ek-1	Real	24.0	0.0	True	True	True	True	False		
ek-2	Real	28.0	0.0	True	True	True	True	False		
deltat	Real	32.0	0.0	True	True	True	True	False		

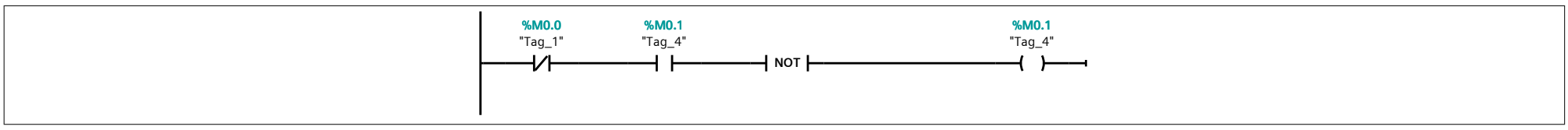
Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

CYC_INT5 [OB35]

CYC_INT5 Properties							
General							
Name	CYC_INT5	**Number**	35	**Type**	OB	**Language**	LAD
Numbering	Manual						
Information							
Title	"Cyclic Interrupt"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB35_EV_CLASS	Byte	0.0		Bits 0-3 = 1 (Coming event), Bits 4-7 = 1 (Event class 1)
OB35_STRT_INF	Byte	1.0		16#36 (OB 35 has started)
OB35_PRIORITY	Byte	2.0		Priority of OB Execution
OB35_OB_NUMBR	Byte	3.0		35 (Organization block 35, OB35)
OB35_RESERVED_1	Byte	4.0		Reserved for system
OB35_RESERVED_2	Byte	5.0		Reserved for system
OB35_PHASE_OFFSET	Word	6.0		Phase offset (msec)
OB35_RESERVED_3	Int	8.0		Reserved for system
OB35_EXC_FREQ	Int	10.0		Frequency of execution (msec)
OB35_DATE_TIME	Date_And_Time	12.0		Date and time OB35 started
Constant				

Network 1:



Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

I/O_FLT1 [OB82]

I/O_FLT1 Properties							
General							
Name	I/O_FLT1	**Number**	82	**Type**	OB	**Language**	STL
Numbering	Manual						
Information							
Title	"I/O Point Fault"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB82_EV_CLASS	Byte	0.0		16#39, Event class 3, Entering event state, Internal fault event
OB82_FLT_ID	Byte	1.0		16#XX, Fault identifcation code
OB82_PRIORITY	Byte	2.0		Priority of OB Execution
OB82_OB_NUMBR	Byte	3.0		82 (Organization block 82, OB82)
OB82_RESERVED_1	Byte	4.0		Reserved for system
OB82_IO_FLAG	Byte	5.0		Input (01010100), Output (01010101)
OB82_MDL_ADDR	Word	6.0		Base address of module with fault
OB82_MDL_DEFECT	Bool	8.0		Module defective
OB82_INT_FAULT	Bool	8.1		Internal fault
OB82_EXT_FAULT	Bool	8.2		External fault
OB82_PNT_INFO	Bool	8.3		Point information
OB82_EXT_VOLTAGE	Bool	8.4		External voltage low
OB82_FLD_CONNCTR	Bool	8.5		Field wiring connector missing
OB82_NO_CONFIG	Bool	8.6		Module has no configuration data
OB82_CONFIG_ERR	Bool	8.7		Module has configuration error
OB82_MDL_TYPE	Byte	9.0		Type of module
OB82_SUB_MDL_ERR	Bool	10.0		Sub-Module is missing or has error
OB82_COMM_FAULT	Bool	10.1		Communication fault
OB82_MDL_STOP	Bool	10.2		Module is stopped
OB82_WTCH_DOG_FLT	Bool	10.3		Watch dog timer stopped module
OB82_INT_PS_FLT	Bool	10.4		Internal power supply fault
OB82_PRIM_BATT_FLT	Bool	10.5		Primary battery is in fault
OB82_BCKUP_BATT_FLT	Bool	10.6		Backup battery is in fault
OB82_RESERVED_2	Bool	10.7		Reserved for system
OB82_RACK_FLT	Bool	11.0		Rack fault, only for bus interface module
OB82_PROC_FLT	Bool	11.1		Processor fault
OB82_EPROM_FLT	Bool	11.2		EPROM fault
OB82_RAM_FLT	Bool	11.3		RAM fault
OB82_ADU_FLT	Bool	11.4		ADU fault
OB82_FUSE_FLT	Bool	11.5		Fuse fault
OB82_HW_INTR_FLT	Bool	11.6		Hardware interupt input in fault
OB82_RESERVED_3	Bool	11.7		Reserved for system
OB82_DATE_TIME	Date_And_Time	12.0		Date and time OB82 started
Constant				

Network 1: Call system diagnostics block

```
0001       CALL  "RSE_FB", "RSE_DB"
```

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

I/O_FLT2 [OB83]

I/O_FLT2 Properties							
General							
Name	I/O_FLT2	**Number**	83	**Type**	OB	**Language**	STL
Numbering	Manual						
Information							
Title	"I/O Point Fault"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB83_EV_CLASS	Byte	0.0		16#38/39, Event class 3, module inserted/removed (8/9)
OB83_FLT_ID	Byte	1.0		16#XX, Fault identifcation code
OB83_PRIORITY	Byte	2.0		Priority of OB Execution
OB83_OB_NUMBR	Byte	3.0		83 (Organization block 83, OB83)
OB83_RESERVED_1	Byte	4.0		Reserved for system
OB83_MDL_ID	Byte	5.0		Input module (01010100), Output module (01010101)
OB83_MDL_ADDR	Word	6.0		Base address of module with point fault
OB83_RACK_NUM	Int	8.0		Number of rack that has module with point fault
OB83_MDL_TYPE	Word	10.0		Module type with point fault
OB83_DATE_TIME	Date_And_Time	12.0		Date and time OB83 started
Constant				

Network 1: Call system diagnostics block

```
0001       CALL  "RSE_FB", "RSE_DB"
```

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

RACK_FLT [OB86]

RACK_FLT Properties							
General							
Name	RACK_FLT	**Number**	86	**Type**	OB	**Language**	STL
Numbering	Manual						
Information							
Title	"Loss Of Rack Fault"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB86_EV_CLASS	Byte	0.0		16#38/39 Event class 3
OB86_FLT_ID	Byte	1.0		16#C1/C4/C5, Fault identification code
OB86_PRIORITY	Byte	2.0		Priority of OB Execution
OB86_OB_NUMBR	Byte	3.0		86 (Organization block 86, OB86)
OB86_RESERVED_1	Byte	4.0		Reserved for system
OB86_RESERVED_2	Byte	5.0		Reserved for system
OB86_MDL_ADDR	Word	6.0		Base address of IM module in rack with fault
▼ OB86_RACKS_FLTD	Array[0..31] of Bool	8.0		Racks in fault
OB86_RACKS_FLTD[0]	Bool	8.0		Racks in fault
OB86_RACKS_FLTD[1]	Bool	8.1		Racks in fault
OB86_RACKS_FLTD[2]	Bool	8.2		Racks in fault
OB86_RACKS_FLTD[3]	Bool	8.3		Racks in fault
OB86_RACKS_FLTD[4]	Bool	8.4		Racks in fault
OB86_RACKS_FLTD[5]	Bool	8.5		Racks in fault
OB86_RACKS_FLTD[6]	Bool	8.6		Racks in fault
OB86_RACKS_FLTD[7]	Bool	8.7		Racks in fault
OB86_RACKS_FLTD[8]	Bool	9.0		Racks in fault
OB86_RACKS_FLTD[9]	Bool	9.1		Racks in fault
OB86_RACKS_FLTD[10]	Bool	9.2		Racks in fault
OB86_RACKS_FLTD[11]	Bool	9.3		Racks in fault
OB86_RACKS_FLTD[12]	Bool	9.4		Racks in fault
OB86_RACKS_FLTD[13]	Bool	9.5		Racks in fault
OB86_RACKS_FLTD[14]	Bool	9.6		Racks in fault
OB86_RACKS_FLTD[15]	Bool	9.7		Racks in fault
OB86_RACKS_FLTD[16]	Bool	10.0		Racks in fault
OB86_RACKS_FLTD[17]	Bool	10.1		Racks in fault
OB86_RACKS_FLTD[18]	Bool	10.2		Racks in fault
OB86_RACKS_FLTD[19]	Bool	10.3		Racks in fault
OB86_RACKS_FLTD[20]	Bool	10.4		Racks in fault
OB86_RACKS_FLTD[21]	Bool	10.5		Racks in fault
OB86_RACKS_FLTD[22]	Bool	10.6		Racks in fault
OB86_RACKS_FLTD[23]	Bool	10.7		Racks in fault
OB86_RACKS_FLTD[24]	Bool	11.0		Racks in fault
OB86_RACKS_FLTD[25]	Bool	11.1		Racks in fault
OB86_RACKS_FLTD[26]	Bool	11.2		Racks in fault
OB86_RACKS_FLTD[27]	Bool	11.3		Racks in fault
OB86_RACKS_FLTD[28]	Bool	11.4		Racks in fault
OB86_RACKS_FLTD[29]	Bool	11.5		Racks in fault
OB86_RACKS_FLTD[30]	Bool	11.6		Racks in fault
OB86_RACKS_FLTD[31]	Bool	11.7		Racks in fault
OB86_DATE_TIME	Date_And_Time	12.0		Date and time OB86 started
Constant				

Network 1: Call system diagnostics block

```
0001       CALL  "RSE_FB", "RSE_DB"
```

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks

OBNL_FLT [OB85]

OBNL_FLT Properties							
General							
Name	OBNL_FLT	**Number**	85	**Type**	OB	**Language**	STL
Numbering	Manual						
Information							
Title	"Organization Block (OB) Not Loaded Fault"	**Author**		**Comment**		**Family**	
Version	0.1	**User-defined ID**					

Name	Data type	Offset	Default value	Comment
▼ Temp				
OB85_EV_CLASS	Byte	0.0		16#35 Event class 3
OB85_FLT_ID	Byte	1.0		16#XX, Fault identification code
OB85_PRIORITY	Byte	2.0		Priority of OB Execution
OB85_OB_NUMBR	Byte	3.0		85 (Organization block 85, OB85)
OB85_RESERVED_1	Byte	4.0		Reserved for system
OB85_RESERVED_2	Byte	5.0		Reserved for system
OB85_RESERVED_3	Int	6.0		Reserved for system
OB85_ERR_EV_CLASS	Byte	8.0		Class of event causing error
OB85_ERR_EV_NUM	Byte	9.0		Number of event causing error
OB85_OB_PRIOR	Byte	10.0		Priority of OB causing error
OB85_OB_NUM	Byte	11.0		Number of OB causing error
OB85_DATE_TIME	Date_And_Time	12.0		Date and time OB85 started
Constant				

Network 1:

```
0001        L     b#16#A1               //
0002        L     #OB85_FLT_ID
0003        ==I
0004        JC    K_OB
0005        L     b#16#A2               //
0006        ==I
0007        JC    K_OB
0008        L     b#16#A3               //
0009        L     #OB85_FLT_ID
0010        ==I
0011        JC    K_B
0012        JU    F_PA
0013
```

Network 2:

```
0001 K_OB:  JU    STOP
0002        JU    ENDE
0003
```

Network 3:

```
0001 K_B:   JU    STOP
0002        JU    ENDE
0003
```

Network 4:

```
0001 F_PA:  NOP 0
0002        JU    ENDE
0003
```

Network 5:

```
0001 STOP:  CALL  STP
0002
```

Network 6:

```
0001
0002 //
0003
0004 ENDE:  NOP 0
0005
```

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks / System blocks

Program resources

This folder is empty.

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks / System blocks / System diagnostics

RSE_DIAGNOSTIC_STATUS_DB [DB127]

RSE_DIAGNOSTIC_STATUS_DB Properties							
General							
Name	RSE_DIAGNOSTIC_STATUS_DB	**Number**	127	**Type**	DB	**Language**	DB
Numbering	Manual						
Information							
Title	Report System Error Diagnostic Block	**Author**	SIMATIC	**Comment**	This DB is part of report system error and will be updated by the RSE FB. It is an interface for support of system diagnosis via the CPU web server.	**Family**	RSEDIAG
Version	1.0	**User-defined ID**	RSE7				

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Static										
Directory	Struct	0.0		False	True	True	False	False		
D_Version	Word	0.0	WORD#16#0100	False	True	True	False	False		Version that supports RSE
D_pGlobalState	Word	2.0	WORD#16#0010	False	True	True	False	False		Byte offset to the start of the "GlobalState" section
D_pQuery	Word	4.0	WORD#16#0014	False	True	True	False	False		Byte offset to the start of the "Query" section
D_pComponent	Word	6.0	WORD#16#0020	False	True	True	False	False		Byte offset to the start of the "Component" section
D_pError	Word	8.0	WORD#16#0024	False	True	True	False	False		Byte offset to the start of the "Error" section
D_pState	Word	10.0	WORD#16#0028	False	True	True	False	False		Byte offset to the start of the "State" section
D_pAlarm	Word	12.0	WORD#16#005A	False	True	True	False	False		Byte offset to the start of the "Alarm" section
D_pSubComponent	Word	14.0	WORD#16#0082	False	True	True	False	False		Byte offset to the start of the "Subcomponent" section
GlobalState	Struct	16.0		False	True	True	False	False		
G_EventCount	Word	16.0	WORD#16#0000	False	True	True	False	False		ID of the last event (counter)
G_StartReporting	Bool	18.0	FALSE	False	True	True	False	False		Startup evaluation running
Query	Struct	20.0		False	True	True	False	False		
Q_ClientID_User	DWord	20.0	DWORD#16#0000_0000	False	True	True	False	False		ID of the client
Q_ClientID_Intern	DWord	24.0	DWORD#16#0000_0000	False	True	True	False	False		ID of the client (internal)
Q_WithSubComponent	Bool	28.0	FALSE	False	True	True	False	False		With/without status of the underlying components (slower)
Q_SubComponentAlarm	Bool	28.1	FALSE	False	True	True	False	False		AS-iMaster gibt AS-iSlave Alarme zurück
Q_Reserved2	Bool	28.2	FALSE	False	True	True	False	False		Reserved
Q_Reserved3	Bool	28.3	FALSE	False	True	True	False	False		Reserved
Q_Reserved4	Bool	28.4	FALSE	False	True	True	False	False		Reserved
Q_Reserved5	Bool	28.5	FALSE	False	True	True	False	False		Reserved
Q_Reserved6	Bool	28.6	FALSE	False	True	True	False	False		Reserved
Q_Reserved7	Bool	28.7	FALSE	False	True	True	False	False		Reserved
Q_Start	Bool	29.0	FALSE	False	True	True	False	False		Start query
Q_Error	Byte	30.0	BYTE#16#00	False	True	True	False	False		Internal error in query
Q_Reserved8	Byte	31.0	BYTE#16#00	False	True	True	False	False		Reserved
Component	Struct	32.0		False	True	True	False	False		
C_AddressMode	Byte	32.0	BYTE#16#00	False	True	True	False	False		Addressing mode of the module
C_Reserved1	Byte	33.0	BYTE#16#00	False	True	True	False	False		Reserved
C_ComponentID	Word	34.0	WORD#16#0000	False	True	True	False	False		Hardware ID of the component (internal)
Error	Struct	36.0		False	True	True	False	False		
E_ErrorNo	Word	36.0	WORD#16#0000	False	True	True	False	False		Index of the requested/actual error
E_LastError	Bool	38.0	FALSE	False	True	True	False	False		TRUE, if E_ErrorNo contains the indes of the last error, otherwise FALSE
State	Struct	40.0		False	True	True	False	False		
S_Hierarchy	Byte	40.0	BYTE#16#00	False	True	True	False	False		Reserved
S_Periphery	Byte	41.0	BYTE#16#00	False	True	True	False	False		Reserved
S_SupFault	Bool	42.0	FALSE	False	True	True	False	False		The component is not obtainable
S_NotAvailable	Bool	42.1	FALSE	False	True	True	False	False		The component does not exist
S_Faulty	Bool	42.2	FALSE	False	True	True	False	False		The component is disrupted; the "Alarm" section is not empty
S_MoreErrors	Bool	42.3	FALSE	False	True	True	False	False		There are more errors than RSE can store.
S_Maintenance1	Bool	42.4	FALSE	False	True	True	False	False		Maintenance required is indicated
S_Maintenance2	Bool	42.5	FALSE	False	True	True	False	False		Maintenance demand is indicated
S_Deactivated	Bool	42.6	FALSE	False	True	True	False	False		The component was deactivated

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_Reserved2	Bool	42.7	FALSE	False	True	True	False	False		Reserved
S_SubFault	Bool	43.0	FALSE	False	True	True	False	False		An underlying component is disrupted
S_SubMaintenance1	Bool	43.1	FALSE	False	True	True	False	False		Maintenance required is indicated for an underlying component
S_SubMaintenance2	Bool	43.2	FALSE	False	True	True	False	False		Maintenance demand is indicated for an underlying component
S_SubDeactivated	Bool	43.3	FALSE	False	True	True	False	False		At least one lower-level component is disabled
S_Reserved4	Bool	43.4	FALSE	False	True	True	False	False		Reserved
S_Reserved5	Bool	43.5	FALSE	False	True	True	False	False		Reserved
S_Reserved6	Bool	43.6	FALSE	False	True	True	False	False		Reserved
S_Reserved7	Bool	43.7	FALSE	False	True	True	False	False		Reserved
S_TIAMS	DWord	44.0	DWORD#16#0000_0000	False	True	True	False	False		Maintenance state of the component
S_TIAMSChannelExist	DWord	48.0	DWORD#16#0000_0000	False	True	True	False	False		Maintenance state: Configured channels
S_TIAMSChannelOK	DWord	52.0	DWORD#16#0000_0000	False	True	True	False	False		Maintenance State: Disrupted channels
S_ChannelCount	Word	56.0	WORD#16#0000	False	True	True	False	False		Number of channels; Valid only when Q_WithSubComponent is set
▼ S_ChannelVector	Array[0..255] of Bool	58.0		False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[0]	Bool	58.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[1]	Bool	58.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[2]	Bool	58.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[3]	Bool	58.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[4]	Bool	58.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[5]	Bool	58.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[6]	Bool	58.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[7]	Bool	58.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[8]	Bool	59.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[9]	Bool	59.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[10]	Bool	59.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[11]	Bool	59.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[12]	Bool	59.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[13]	Bool	59.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[14]	Bool	59.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[15]	Bool	59.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[16]	Bool	60.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[17]	Bool	60.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[18]	Bool	60.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[19]	Bool	60.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[20]	Bool	60.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[21]	Bool	60.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[22]	Bool	60.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[23]	Bool	60.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[24]	Bool	61.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[25]	Bool	61.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[26]	Bool	61.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[27]	Bool	61.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[28]	Bool	61.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[29]	Bool	61.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_ChannelVector[30]	Bool	61.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[31]	Bool	61.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[32]	Bool	62.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[33]	Bool	62.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[34]	Bool	62.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[35]	Bool	62.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[36]	Bool	62.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[37]	Bool	62.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[38]	Bool	62.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[39]	Bool	62.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[40]	Bool	63.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[41]	Bool	63.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[42]	Bool	63.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[43]	Bool	63.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[44]	Bool	63.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[45]	Bool	63.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[46]	Bool	63.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[47]	Bool	63.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[48]	Bool	64.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[49]	Bool	64.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[50]	Bool	64.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[51]	Bool	64.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[52]	Bool	64.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[53]	Bool	64.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[54]	Bool	64.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[55]	Bool	64.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[56]	Bool	65.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[57]	Bool	65.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[58]	Bool	65.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[59]	Bool	65.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[60]	Bool	65.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[61]	Bool	65.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[62]	Bool	65.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[63]	Bool	65.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[64]	Bool	66.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[65]	Bool	66.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[66]	Bool	66.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[67]	Bool	66.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[68]	Bool	66.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[69]	Bool	66.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[70]	Bool	66.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[71]	Bool	66.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_ChannelVector[72]	Bool	67.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[73]	Bool	67.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[74]	Bool	67.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[75]	Bool	67.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[76]	Bool	67.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[77]	Bool	67.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[78]	Bool	67.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[79]	Bool	67.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[80]	Bool	68.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[81]	Bool	68.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[82]	Bool	68.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[83]	Bool	68.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[84]	Bool	68.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[85]	Bool	68.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[86]	Bool	68.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[87]	Bool	68.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[88]	Bool	69.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[89]	Bool	69.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[90]	Bool	69.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[91]	Bool	69.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[92]	Bool	69.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[93]	Bool	69.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[94]	Bool	69.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[95]	Bool	69.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[96]	Bool	70.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[97]	Bool	70.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[98]	Bool	70.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[99]	Bool	70.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[100]	Bool	70.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[101]	Bool	70.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[102]	Bool	70.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[103]	Bool	70.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[104]	Bool	71.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[105]	Bool	71.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[106]	Bool	71.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[107]	Bool	71.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[108]	Bool	71.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[109]	Bool	71.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[110]	Bool	71.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[111]	Bool	71.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[112]	Bool	72.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[113]	Bool	72.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_ChannelVector[114]	Bool	72.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[115]	Bool	72.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[116]	Bool	72.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[117]	Bool	72.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[118]	Bool	72.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[119]	Bool	72.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[120]	Bool	73.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[121]	Bool	73.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[122]	Bool	73.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[123]	Bool	73.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[124]	Bool	73.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[125]	Bool	73.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[126]	Bool	73.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[127]	Bool	73.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[128]	Bool	74.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[129]	Bool	74.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[130]	Bool	74.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[131]	Bool	74.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[132]	Bool	74.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[133]	Bool	74.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[134]	Bool	74.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[135]	Bool	74.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[136]	Bool	75.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[137]	Bool	75.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[138]	Bool	75.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[139]	Bool	75.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[140]	Bool	75.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[141]	Bool	75.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[142]	Bool	75.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[143]	Bool	75.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[144]	Bool	76.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[145]	Bool	76.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[146]	Bool	76.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[147]	Bool	76.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[148]	Bool	76.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[149]	Bool	76.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[150]	Bool	76.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[151]	Bool	76.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[152]	Bool	77.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[153]	Bool	77.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[154]	Bool	77.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[155]	Bool	77.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_ChannelVector[156]	Bool	77.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[157]	Bool	77.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[158]	Bool	77.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[159]	Bool	77.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[160]	Bool	78.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[161]	Bool	78.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[162]	Bool	78.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[163]	Bool	78.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[164]	Bool	78.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[165]	Bool	78.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[166]	Bool	78.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[167]	Bool	78.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[168]	Bool	79.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[169]	Bool	79.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[170]	Bool	79.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[171]	Bool	79.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[172]	Bool	79.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[173]	Bool	79.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[174]	Bool	79.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[175]	Bool	79.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[176]	Bool	80.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[177]	Bool	80.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[178]	Bool	80.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[179]	Bool	80.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[180]	Bool	80.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[181]	Bool	80.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[182]	Bool	80.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[183]	Bool	80.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[184]	Bool	81.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[185]	Bool	81.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[186]	Bool	81.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[187]	Bool	81.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[188]	Bool	81.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[189]	Bool	81.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[190]	Bool	81.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[191]	Bool	81.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[192]	Bool	82.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[193]	Bool	82.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[194]	Bool	82.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[195]	Bool	82.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[196]	Bool	82.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[197]	Bool	82.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_ChannelVector[198]	Bool	82.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[199]	Bool	82.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[200]	Bool	83.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[201]	Bool	83.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[202]	Bool	83.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[203]	Bool	83.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[204]	Bool	83.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[205]	Bool	83.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[206]	Bool	83.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[207]	Bool	83.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[208]	Bool	84.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[209]	Bool	84.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[210]	Bool	84.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[211]	Bool	84.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[212]	Bool	84.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[213]	Bool	84.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[214]	Bool	84.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[215]	Bool	84.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[216]	Bool	85.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[217]	Bool	85.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[218]	Bool	85.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[219]	Bool	85.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[220]	Bool	85.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[221]	Bool	85.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[222]	Bool	85.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[223]	Bool	85.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[224]	Bool	86.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[225]	Bool	86.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[226]	Bool	86.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[227]	Bool	86.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[228]	Bool	86.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[229]	Bool	86.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[230]	Bool	86.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[231]	Bool	86.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[232]	Bool	87.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[233]	Bool	87.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[234]	Bool	87.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[235]	Bool	87.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[236]	Bool	87.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[237]	Bool	87.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[238]	Bool	87.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[239]	Bool	87.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set

Name	Data type	Offset	Start value	Retain	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
S_ChannelVector[240]	Bool	88.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[241]	Bool	88.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[242]	Bool	88.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[243]	Bool	88.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[244]	Bool	88.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[245]	Bool	88.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[246]	Bool	88.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[247]	Bool	88.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[248]	Bool	89.0	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[249]	Bool	89.1	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[250]	Bool	89.2	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[251]	Bool	89.3	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[252]	Bool	89.4	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[253]	Bool	89.5	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[254]	Bool	89.6	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
S_ChannelVector[255]	Bool	89.7	false	False	True	True	False	False		List of channels involved; Valid only when Q_WithSubComponent is set
▼ Alarm	Struct	90.0		False	True	True	False	False		
A_ComponentID	Word	90.0	WORD#16#0000	False	True	True	False	False		Hardware ID of the component (internal)
A_TextID1	Word	92.0	WORD#16#0000	False	True	True	False	False		ID of the first error text
A_TextLexikonID1	Word	94.0	WORD#16#0000	False	True	True	False	False		ID of the first error text lexicon
A_HelpTextLexikonID1	Word	96.0	WORD#16#0000	False	True	True	False	False		ID of the first help text lexicon
A_MapTextID	Word	98.0	WORD#16#0000	False	True	True	False	False		ID of the first error text in the export file (HMI)
A_MapHelpTextID	Word	100.0	WORD#16#0000	False	True	True	False	False		ID of the first help text in the export file (HMI)
A_TextID2	Word	102.0	WORD#16#0000	False	True	True	False	False		ID of the second error text
A_TextLexikonID2	Word	104.0	WORD#16#0000	False	True	True	False	False		ID of the second error text lexicon
A_HelpTextLexikonID2	Word	106.0	WORD#16#0000	False	True	True	False	False		ID of the second help text lexicon
A_MapTextID2	Word	108.0	WORD#16#0000	False	True	True	False	False		ID of the second error text in the export file (HMI)
A_MapHelpTextID2	Word	110.0	WORD#16#0000	False	True	True	False	False		ID of the second help text in the export file (HMI)
A_AlarmID	DWord	112.0	DWORD#16#0000_0000	False	True	True	False	False		Message number
A_ValueCount	Word	116.0	WORD#16#000C	False	True	True	False	False		Number of other occupied bytes
▼ A_AssociatedValue	Array[1..6] of Word	118.0		False	True	True	False	False		Associated values of the message
A_AssociatedValue[1]	Word	118.0	16#0	False	True	True	False	False		Associated values of the message
A_AssociatedValue[2]	Word	120.0	16#0	False	True	True	False	False		Associated values of the message
A_AssociatedValue[3]	Word	122.0	16#0	False	True	True	False	False		Associated values of the message
A_AssociatedValue[4]	Word	124.0	16#0	False	True	True	False	False		Associated values of the message
A_AssociatedValue[5]	Word	126.0	16#0	False	True	True	False	False		Associated values of the message
A_AssociatedValue[6]	Word	128.0	16#0	False	True	True	False	False		Associated values of the message
▼ SubComponent	Struct	130.0		False	True	True	False	False		
U_SubComponentCount	Word	130.0	WORD#16#0000	False	True	True	False	False		Number of underlying components
▼ U_SubComponentFault	Array[1..8] of Byte	132.0		False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[1]	Byte	132.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[2]	Byte	133.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[3]	Byte	134.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[4]	Byte	135.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[5]	Byte	136.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[6]	Byte	137.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[7]	Byte	138.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component
U_SubComponent-Fault[8]	Byte	139.0	16#0	False	True	True	False	False		List of status of subordinate components, 1 byte per component

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks / System blocks / System diagnostics

RSE_FB [FB49]

RSE_FB Properties							
General							
Name	RSE_FB	**Number**	49	**Type**	FB	**Language**	RSE
Numbering	Manual						
Information							
Title	Report System Error Diagnostic Block	**Author**	SIMATIC	**Comment**	This FB contains the code generated from STEP 7 Report System Errors. It is called in the error OBs and in OB1 or a cyclic interrupt OB. The FB evaluates the system errors and displays them by means of an Alarm_S to the display device. While the FB executes, all interrupts are delayed.	**Family**	RSEDIAG
Version	1.0	**User-defined ID**	RSE				

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Input									
Output									
InOut									
▼ Static									
dDB127BGID	DWord	0.0	16#0	True	True	False	False		
dDB127Vorgang	Byte	4.0	16#0	True	True	False	False		
wMaxASISlave_a	Byte	5.0	16#0	True	True	False	False		
wMaxASISlave_b	Byte	6.0	16#0	True	True	False	False		
CyclesDB127WorkOn	Byte	7.0	16#0	True	True	False	False		
dDB127Stuffe	Byte	8.0	16#0	True	True	False	False		
w127Timer_a	Byte	9.0	BYTE#16#80	True	True	False	False		
w127Timer_b	Byte	10.0	BYTE#16#00	True	True	False	False		
tDB127TIAMS_a	Byte	11.0	16#0	True	True	False	False		
tDB127TIAMS_b	Byte	12.0	16#0	True	True	False	False		
tDB127TIAMS_c	Byte	13.0	16#0	True	True	False	False		
tDB127TIAMS_d	Byte	14.0	16#0	True	True	False	False		
tDB127TIAChannelExist_a	Byte	15.0	16#0	True	True	False	False		
tDB127TIAChannelExist_b	Byte	16.0	16#0	True	True	False	False		
tDB127TIAChannelExist_c	Byte	17.0	16#0	True	True	False	False		
tDB127TIAChannelExist_d	Byte	18.0	16#0	True	True	False	False		
tDB127TIAChannelOK_a	Byte	19.0	16#0	True	True	False	False		
tDB127TIAChannelOK_b	Byte	20.0	16#0	True	True	False	False		
tDB127TIAChannelOK_c	Byte	21.0	16#0	True	True	False	False		
tDB127TIAChannelOK_d	Byte	22.0	16#0	True	True	False	False		
tDB127Schleife_a	Byte	23.0	16#0	True	True	False	False		
tDB127Schleife_b	Byte	24.0	16#0	True	True	False	False		
tDB127LoggedOn	Bool	25.0	false	True	True	False	False		
xAnwenderMeldungsSignal	Bool	25.1	false	True	True	False	False		
xAnwenderKanalFehler	Bool	25.2	false	True	True	False	False		
bAnwenderIO_Flag	Byte	26.0	BYTE#16#54	True	True	False	False		
dMeldeNummer	DWord	28.0	16#0	True	True	False	False		
wAnwenderTextID	Word	32.0	16#0	True	True	False	False		
wAnwenderFehlerNummer	Word	34.0	16#0	True	True	False	False		
wAnwenderKanalNummer	Word	36.0	16#0	True	True	False	False		
wAnwenderHErrClass	Word	38.0	WORD#16#0000	True	True	False	False		
iAnwenderSFCRetVal	Int	40.0	0	True	True	False	False		
wRackEntry1	Word	42.0	WORD#16#02E0	True	True	False	False		
xFrei	Bool	44.0	TRUE	True	True	False	False		
xADA	Bool	44.1	false	True	True	False	False		
xWieder	Bool	44.2	false	True	True	False	False		
xGehend	Bool	44.3	false	True	True	False	False		
iFunktion	Byte	45.0	16#0	True	True	False	False		
iDaten	Byte	46.0	16#0	True	True	False	False		
wBGID_a	Byte	47.0	16#0	True	True	False	False		
wBGID_b	Byte	48.0	16#0	True	True	False	False		
bFill0	Byte	49.0	BYTE#16#00	True	True	False	False		
▼ LDB54	RALRM	50.0		True	True	False	True		
▼ Input									
MODE	Int	50.0	0	True	True	False	False		
F_ID	DWord	52.0	16#0	True	True	False	False		
MLEN	Int	56.0	0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Output									
NEW	Bool	58.0	false	True	True	False	False		
STATUS	DWord	60.0	16#0	True	True	False	False		
ID	DWord	64.0	16#0	True	True	False	False		
LEN	Int	68.0	0	True	True	False	False		
InOut									
TINFO	Any	70.0		True	True	False	False		
AINFO	Any	80.0		True	True	False	False		
Static									
LDB52	RDREC	90.0		True	True	False	True		
Input									
REQ	Bool	90.0	false	True	True	False	False		
ID	DWord	92.0	16#0	True	True	False	False		
INDEX	Int	96.0	0	True	True	False	False		
MLEN	Int	98.0	0	True	True	False	False		
Output									
VALID	Bool	100.0	false	True	True	False	False		
BUSY	Bool	100.1	false	True	True	False	False		
ERROR	Bool	100.2	false	True	True	False	False		
STATUS	DWord	102.0	16#0	True	True	False	False		
LEN	Int	106.0	0	True	True	False	False		
InOut									
RECORD	Any	108.0		True	True	False	False		
Static									
VFTable0_Peri	Byte	118.0	BYTE#16#00	True	True	False	False		
VFTable0_Hiera	Byte	119.0	BYTE#16#01	True	True	False	False		
VFTable0_AsyncFct	Byte	120.0	BYTE#16#05	True	True	False	False		
VFTable0_AsyncDat	Byte	121.0	BYTE#16#09	True	True	False	False		
VFTable0_SyncFct	Byte	122.0	BYTE#16#05	True	True	False	False		
VFTable0_SyncDat	Byte	123.0	BYTE#16#09	True	True	False	False		
VFTable0_GehendDat	Byte	124.0	BYTE#16#09	True	True	False	False		
VFTable0_Fehlerklasse	Byte	125.0	BYTE#16#00	True	True	False	False		
VFTable0_TextListIndex	Byte	126.0	BYTE#16#00	True	True	False	False		
VFTable0_0C96Dat	Byte	127.0	BYTE#16#00	True	True	False	False		
VFTable1_Peri	Byte	128.0	BYTE#16#00	True	True	False	False		
VFTable1_Hiera	Byte	129.0	BYTE#16#12	True	True	False	False		
VFTable1_AsyncFct	Byte	130.0	BYTE#16#05	True	True	False	False		
VFTable1_AsyncDat	Byte	131.0	BYTE#16#09	True	True	False	False		
VFTable1_SyncFct	Byte	132.0	BYTE#16#05	True	True	False	False		
VFTable1_SyncDat	Byte	133.0	BYTE#16#09	True	True	False	False		
VFTable1_GehendDat	Byte	134.0	BYTE#16#09	True	True	False	False		
VFTable1_Fehlerklasse	Byte	135.0	BYTE#16#00	True	True	False	False		
VFTable1_TextListIndex	Byte	136.0	BYTE#16#00	True	True	False	False		
VFTable1_0C96Dat	Byte	137.0	BYTE#16#09	True	True	False	False		
VFTable2_Peri	Byte	138.0	BYTE#16#00	True	True	False	False		
VFTable2_Hiera	Byte	139.0	BYTE#16#12	True	True	False	False		
VFTable2_AsyncFct	Byte	140.0	BYTE#16#04	True	True	False	False		
VFTable2_AsyncDat	Byte	141.0	BYTE#16#08	True	True	False	False		
VFTable2_SyncFct	Byte	142.0	BYTE#16#05	True	True	False	False		
VFTable2_SyncDat	Byte	143.0	BYTE#16#09	True	True	False	False		
VFTable2_GehendDat	Byte	144.0	BYTE#16#06	True	True	False	False		
VFTable2_Fehlerklasse	Byte	145.0	BYTE#16#00	True	True	False	False		
VFTable2_TextListIndex	Byte	146.0	BYTE#16#00	True	True	False	False		
VFTable2_0C96Dat	Byte	147.0	BYTE#16#00	True	True	False	False		
VFTable3_Peri	Byte	148.0	BYTE#16#10	True	True	False	False		
VFTable3_Hiera	Byte	149.0	BYTE#16#12	True	True	False	False		
VFTable3_AsyncFct	Byte	150.0	BYTE#16#00	True	True	False	False		
VFTable3_AsyncDat	Byte	151.0	BYTE#16#01	True	True	False	False		
VFTable3_SyncFct	Byte	152.0	BYTE#16#00	True	True	False	False		
VFTable3_SyncDat	Byte	153.0	BYTE#16#01	True	True	False	False		
VFTable3_GehendDat	Byte	154.0	BYTE#16#07	True	True	False	False		
VFTable3_Fehlerklasse	Byte	155.0	BYTE#16#00	True	True	False	False		
VFTable3_TextListIndex	Byte	156.0	BYTE#16#00	True	True	False	False		
VFTable3_0C96Dat	Byte	157.0	BYTE#16#03	True	True	False	False		
VFTable4_Peri	Byte	158.0	BYTE#16#10	True	True	False	False		
VFTable4_Hiera	Byte	159.0	BYTE#16#12	True	True	False	False		
VFTable4_AsyncFct	Byte	160.0	BYTE#16#00	True	True	False	False		
VFTable4_AsyncDat	Byte	161.0	BYTE#16#01	True	True	False	False		
VFTable4_SyncFct	Byte	162.0	BYTE#16#00	True	True	False	False		
VFTable4_SyncDat	Byte	163.0	BYTE#16#01	True	True	False	False		
VFTable4_GehendDat	Byte	164.0	BYTE#16#07	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
VFTable4_Fehlerklasse	Byte	165.0	BYTE#16#00	True	True	False	False		
VFTable4_TextListIndex	Byte	166.0	BYTE#16#03	True	True	False	False		
VFTable4_0C96Dat	Byte	167.0	BYTE#16#03	True	True	False	False		
VFTable5_Peri	Byte	168.0	BYTE#16#02	True	True	False	False		
VFTable5_Hiera	Byte	169.0	BYTE#16#12	True	True	False	False		
VFTable5_AsyncFct	Byte	170.0	BYTE#16#02	True	True	False	False		
VFTable5_AsyncDat	Byte	171.0	BYTE#16#05	True	True	False	False		
VFTable5_SyncFct	Byte	172.0	BYTE#16#01	True	True	False	False		
VFTable5_SyncDat	Byte	173.0	BYTE#16#04	True	True	False	False		
VFTable5_GehendDat	Byte	174.0	BYTE#16#07	True	True	False	False		
VFTable5_Fehlerklasse	Byte	175.0	BYTE#16#00	True	True	False	False		
VFTable5_TextListIndex	Byte	176.0	BYTE#16#03	True	True	False	False		
VFTable5_0C96Dat	Byte	177.0	BYTE#16#00	True	True	False	False		
▼ ADA	Array[1..5] of Byte	178.0		True	True	False	False		
ADA[1]	Byte	178.0	16#0	True	True	False	False		
ADA[2]	Byte	179.0	16#0	True	True	False	False		
ADA[3]	Byte	180.0	16#0	True	True	False	False		
ADA[4]	Byte	181.0	16#0	True	True	False	False		
ADA[5]	Byte	182.0	16#0	True	True	False	False		
▼ DiagPuffer	Array[1..272] of Byte	184.0		True	True	False	False		
DiagPuffer[1]	Byte	184.0	16#0	True	True	False	False		
DiagPuffer[2]	Byte	185.0	16#0	True	True	False	False		
DiagPuffer[3]	Byte	186.0	16#0	True	True	False	False		
DiagPuffer[4]	Byte	187.0	16#0	True	True	False	False		
DiagPuffer[5]	Byte	188.0	16#0	True	True	False	False		
DiagPuffer[6]	Byte	189.0	16#0	True	True	False	False		
DiagPuffer[7]	Byte	190.0	16#0	True	True	False	False		
DiagPuffer[8]	Byte	191.0	16#0	True	True	False	False		
DiagPuffer[9]	Byte	192.0	16#0	True	True	False	False		
DiagPuffer[10]	Byte	193.0	16#0	True	True	False	False		
DiagPuffer[11]	Byte	194.0	16#0	True	True	False	False		
DiagPuffer[12]	Byte	195.0	16#0	True	True	False	False		
DiagPuffer[13]	Byte	196.0	16#0	True	True	False	False		
DiagPuffer[14]	Byte	197.0	16#0	True	True	False	False		
DiagPuffer[15]	Byte	198.0	16#0	True	True	False	False		
DiagPuffer[16]	Byte	199.0	16#0	True	True	False	False		
DiagPuffer[17]	Byte	200.0	16#0	True	True	False	False		
DiagPuffer[18]	Byte	201.0	16#0	True	True	False	False		
DiagPuffer[19]	Byte	202.0	16#0	True	True	False	False		
DiagPuffer[20]	Byte	203.0	16#0	True	True	False	False		
DiagPuffer[21]	Byte	204.0	16#0	True	True	False	False		
DiagPuffer[22]	Byte	205.0	16#0	True	True	False	False		
DiagPuffer[23]	Byte	206.0	16#0	True	True	False	False		
DiagPuffer[24]	Byte	207.0	16#0	True	True	False	False		
DiagPuffer[25]	Byte	208.0	16#0	True	True	False	False		
DiagPuffer[26]	Byte	209.0	16#0	True	True	False	False		
DiagPuffer[27]	Byte	210.0	16#0	True	True	False	False		
DiagPuffer[28]	Byte	211.0	16#0	True	True	False	False		
DiagPuffer[29]	Byte	212.0	16#0	True	True	False	False		
DiagPuffer[30]	Byte	213.0	16#0	True	True	False	False		
DiagPuffer[31]	Byte	214.0	16#0	True	True	False	False		
DiagPuffer[32]	Byte	215.0	16#0	True	True	False	False		
DiagPuffer[33]	Byte	216.0	16#0	True	True	False	False		
DiagPuffer[34]	Byte	217.0	16#0	True	True	False	False		
DiagPuffer[35]	Byte	218.0	16#0	True	True	False	False		
DiagPuffer[36]	Byte	219.0	16#0	True	True	False	False		
DiagPuffer[37]	Byte	220.0	16#0	True	True	False	False		
DiagPuffer[38]	Byte	221.0	16#0	True	True	False	False		
DiagPuffer[39]	Byte	222.0	16#0	True	True	False	False		
DiagPuffer[40]	Byte	223.0	16#0	True	True	False	False		
DiagPuffer[41]	Byte	224.0	16#0	True	True	False	False		
DiagPuffer[42]	Byte	225.0	16#0	True	True	False	False		
DiagPuffer[43]	Byte	226.0	16#0	True	True	False	False		
DiagPuffer[44]	Byte	227.0	16#0	True	True	False	False		
DiagPuffer[45]	Byte	228.0	16#0	True	True	False	False		
DiagPuffer[46]	Byte	229.0	16#0	True	True	False	False		
DiagPuffer[47]	Byte	230.0	16#0	True	True	False	False		
DiagPuffer[48]	Byte	231.0	16#0	True	True	False	False		
DiagPuffer[49]	Byte	232.0	16#0	True	True	False	False		
DiagPuffer[50]	Byte	233.0	16#0	True	True	False	False		
DiagPuffer[51]	Byte	234.0	16#0	True	True	False	False		
DiagPuffer[52]	Byte	235.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DiagPuffer[53]	Byte	236.0	16#0	True	True	False	False		
DiagPuffer[54]	Byte	237.0	16#0	True	True	False	False		
DiagPuffer[55]	Byte	238.0	16#0	True	True	False	False		
DiagPuffer[56]	Byte	239.0	16#0	True	True	False	False		
DiagPuffer[57]	Byte	240.0	16#0	True	True	False	False		
DiagPuffer[58]	Byte	241.0	16#0	True	True	False	False		
DiagPuffer[59]	Byte	242.0	16#0	True	True	False	False		
DiagPuffer[60]	Byte	243.0	16#0	True	True	False	False		
DiagPuffer[61]	Byte	244.0	16#0	True	True	False	False		
DiagPuffer[62]	Byte	245.0	16#0	True	True	False	False		
DiagPuffer[63]	Byte	246.0	16#0	True	True	False	False		
DiagPuffer[64]	Byte	247.0	16#0	True	True	False	False		
DiagPuffer[65]	Byte	248.0	16#0	True	True	False	False		
DiagPuffer[66]	Byte	249.0	16#0	True	True	False	False		
DiagPuffer[67]	Byte	250.0	16#0	True	True	False	False		
DiagPuffer[68]	Byte	251.0	16#0	True	True	False	False		
DiagPuffer[69]	Byte	252.0	16#0	True	True	False	False		
DiagPuffer[70]	Byte	253.0	16#0	True	True	False	False		
DiagPuffer[71]	Byte	254.0	16#0	True	True	False	False		
DiagPuffer[72]	Byte	255.0	16#0	True	True	False	False		
DiagPuffer[73]	Byte	256.0	16#0	True	True	False	False		
DiagPuffer[74]	Byte	257.0	16#0	True	True	False	False		
DiagPuffer[75]	Byte	258.0	16#0	True	True	False	False		
DiagPuffer[76]	Byte	259.0	16#0	True	True	False	False		
DiagPuffer[77]	Byte	260.0	16#0	True	True	False	False		
DiagPuffer[78]	Byte	261.0	16#0	True	True	False	False		
DiagPuffer[79]	Byte	262.0	16#0	True	True	False	False		
DiagPuffer[80]	Byte	263.0	16#0	True	True	False	False		
DiagPuffer[81]	Byte	264.0	16#0	True	True	False	False		
DiagPuffer[82]	Byte	265.0	16#0	True	True	False	False		
DiagPuffer[83]	Byte	266.0	16#0	True	True	False	False		
DiagPuffer[84]	Byte	267.0	16#0	True	True	False	False		
DiagPuffer[85]	Byte	268.0	16#0	True	True	False	False		
DiagPuffer[86]	Byte	269.0	16#0	True	True	False	False		
DiagPuffer[87]	Byte	270.0	16#0	True	True	False	False		
DiagPuffer[88]	Byte	271.0	16#0	True	True	False	False		
DiagPuffer[89]	Byte	272.0	16#0	True	True	False	False		
DiagPuffer[90]	Byte	273.0	16#0	True	True	False	False		
DiagPuffer[91]	Byte	274.0	16#0	True	True	False	False		
DiagPuffer[92]	Byte	275.0	16#0	True	True	False	False		
DiagPuffer[93]	Byte	276.0	16#0	True	True	False	False		
DiagPuffer[94]	Byte	277.0	16#0	True	True	False	False		
DiagPuffer[95]	Byte	278.0	16#0	True	True	False	False		
DiagPuffer[96]	Byte	279.0	16#0	True	True	False	False		
DiagPuffer[97]	Byte	280.0	16#0	True	True	False	False		
DiagPuffer[98]	Byte	281.0	16#0	True	True	False	False		
DiagPuffer[99]	Byte	282.0	16#0	True	True	False	False		
DiagPuffer[100]	Byte	283.0	16#0	True	True	False	False		
DiagPuffer[101]	Byte	284.0	16#0	True	True	False	False		
DiagPuffer[102]	Byte	285.0	16#0	True	True	False	False		
DiagPuffer[103]	Byte	286.0	16#0	True	True	False	False		
DiagPuffer[104]	Byte	287.0	16#0	True	True	False	False		
DiagPuffer[105]	Byte	288.0	16#0	True	True	False	False		
DiagPuffer[106]	Byte	289.0	16#0	True	True	False	False		
DiagPuffer[107]	Byte	290.0	16#0	True	True	False	False		
DiagPuffer[108]	Byte	291.0	16#0	True	True	False	False		
DiagPuffer[109]	Byte	292.0	16#0	True	True	False	False		
DiagPuffer[110]	Byte	293.0	16#0	True	True	False	False		
DiagPuffer[111]	Byte	294.0	16#0	True	True	False	False		
DiagPuffer[112]	Byte	295.0	16#0	True	True	False	False		
DiagPuffer[113]	Byte	296.0	16#0	True	True	False	False		
DiagPuffer[114]	Byte	297.0	16#0	True	True	False	False		
DiagPuffer[115]	Byte	298.0	16#0	True	True	False	False		
DiagPuffer[116]	Byte	299.0	16#0	True	True	False	False		
DiagPuffer[117]	Byte	300.0	16#0	True	True	False	False		
DiagPuffer[118]	Byte	301.0	16#0	True	True	False	False		
DiagPuffer[119]	Byte	302.0	16#0	True	True	False	False		
DiagPuffer[120]	Byte	303.0	16#0	True	True	False	False		
DiagPuffer[121]	Byte	304.0	16#0	True	True	False	False		
DiagPuffer[122]	Byte	305.0	16#0	True	True	False	False		
DiagPuffer[123]	Byte	306.0	16#0	True	True	False	False		
DiagPuffer[124]	Byte	307.0	16#0	True	True	False	False		
DiagPuffer[125]	Byte	308.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DiagPuffer[126]	Byte	309.0	16#0	True	True	False	False		
DiagPuffer[127]	Byte	310.0	16#0	True	True	False	False		
DiagPuffer[128]	Byte	311.0	16#0	True	True	False	False		
DiagPuffer[129]	Byte	312.0	16#0	True	True	False	False		
DiagPuffer[130]	Byte	313.0	16#0	True	True	False	False		
DiagPuffer[131]	Byte	314.0	16#0	True	True	False	False		
DiagPuffer[132]	Byte	315.0	16#0	True	True	False	False		
DiagPuffer[133]	Byte	316.0	16#0	True	True	False	False		
DiagPuffer[134]	Byte	317.0	16#0	True	True	False	False		
DiagPuffer[135]	Byte	318.0	16#0	True	True	False	False		
DiagPuffer[136]	Byte	319.0	16#0	True	True	False	False		
DiagPuffer[137]	Byte	320.0	16#0	True	True	False	False		
DiagPuffer[138]	Byte	321.0	16#0	True	True	False	False		
DiagPuffer[139]	Byte	322.0	16#0	True	True	False	False		
DiagPuffer[140]	Byte	323.0	16#0	True	True	False	False		
DiagPuffer[141]	Byte	324.0	16#0	True	True	False	False		
DiagPuffer[142]	Byte	325.0	16#0	True	True	False	False		
DiagPuffer[143]	Byte	326.0	16#0	True	True	False	False		
DiagPuffer[144]	Byte	327.0	16#0	True	True	False	False		
DiagPuffer[145]	Byte	328.0	16#0	True	True	False	False		
DiagPuffer[146]	Byte	329.0	16#0	True	True	False	False		
DiagPuffer[147]	Byte	330.0	16#0	True	True	False	False		
DiagPuffer[148]	Byte	331.0	16#0	True	True	False	False		
DiagPuffer[149]	Byte	332.0	16#0	True	True	False	False		
DiagPuffer[150]	Byte	333.0	16#0	True	True	False	False		
DiagPuffer[151]	Byte	334.0	16#0	True	True	False	False		
DiagPuffer[152]	Byte	335.0	16#0	True	True	False	False		
DiagPuffer[153]	Byte	336.0	16#0	True	True	False	False		
DiagPuffer[154]	Byte	337.0	16#0	True	True	False	False		
DiagPuffer[155]	Byte	338.0	16#0	True	True	False	False		
DiagPuffer[156]	Byte	339.0	16#0	True	True	False	False		
DiagPuffer[157]	Byte	340.0	16#0	True	True	False	False		
DiagPuffer[158]	Byte	341.0	16#0	True	True	False	False		
DiagPuffer[159]	Byte	342.0	16#0	True	True	False	False		
DiagPuffer[160]	Byte	343.0	16#0	True	True	False	False		
DiagPuffer[161]	Byte	344.0	16#0	True	True	False	False		
DiagPuffer[162]	Byte	345.0	16#0	True	True	False	False		
DiagPuffer[163]	Byte	346.0	16#0	True	True	False	False		
DiagPuffer[164]	Byte	347.0	16#0	True	True	False	False		
DiagPuffer[165]	Byte	348.0	16#0	True	True	False	False		
DiagPuffer[166]	Byte	349.0	16#0	True	True	False	False		
DiagPuffer[167]	Byte	350.0	16#0	True	True	False	False		
DiagPuffer[168]	Byte	351.0	16#0	True	True	False	False		
DiagPuffer[169]	Byte	352.0	16#0	True	True	False	False		
DiagPuffer[170]	Byte	353.0	16#0	True	True	False	False		
DiagPuffer[171]	Byte	354.0	16#0	True	True	False	False		
DiagPuffer[172]	Byte	355.0	16#0	True	True	False	False		
DiagPuffer[173]	Byte	356.0	16#0	True	True	False	False		
DiagPuffer[174]	Byte	357.0	16#0	True	True	False	False		
DiagPuffer[175]	Byte	358.0	16#0	True	True	False	False		
DiagPuffer[176]	Byte	359.0	16#0	True	True	False	False		
DiagPuffer[177]	Byte	360.0	16#0	True	True	False	False		
DiagPuffer[178]	Byte	361.0	16#0	True	True	False	False		
DiagPuffer[179]	Byte	362.0	16#0	True	True	False	False		
DiagPuffer[180]	Byte	363.0	16#0	True	True	False	False		
DiagPuffer[181]	Byte	364.0	16#0	True	True	False	False		
DiagPuffer[182]	Byte	365.0	16#0	True	True	False	False		
DiagPuffer[183]	Byte	366.0	16#0	True	True	False	False		
DiagPuffer[184]	Byte	367.0	16#0	True	True	False	False		
DiagPuffer[185]	Byte	368.0	16#0	True	True	False	False		
DiagPuffer[186]	Byte	369.0	16#0	True	True	False	False		
DiagPuffer[187]	Byte	370.0	16#0	True	True	False	False		
DiagPuffer[188]	Byte	371.0	16#0	True	True	False	False		
DiagPuffer[189]	Byte	372.0	16#0	True	True	False	False		
DiagPuffer[190]	Byte	373.0	16#0	True	True	False	False		
DiagPuffer[191]	Byte	374.0	16#0	True	True	False	False		
DiagPuffer[192]	Byte	375.0	16#0	True	True	False	False		
DiagPuffer[193]	Byte	376.0	16#0	True	True	False	False		
DiagPuffer[194]	Byte	377.0	16#0	True	True	False	False		
DiagPuffer[195]	Byte	378.0	16#0	True	True	False	False		
DiagPuffer[196]	Byte	379.0	16#0	True	True	False	False		
DiagPuffer[197]	Byte	380.0	16#0	True	True	False	False		
DiagPuffer[198]	Byte	381.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DiagPuffer[199]	Byte	382.0	16#0	True	True	False	False		
DiagPuffer[200]	Byte	383.0	16#0	True	True	False	False		
DiagPuffer[201]	Byte	384.0	16#0	True	True	False	False		
DiagPuffer[202]	Byte	385.0	16#0	True	True	False	False		
DiagPuffer[203]	Byte	386.0	16#0	True	True	False	False		
DiagPuffer[204]	Byte	387.0	16#0	True	True	False	False		
DiagPuffer[205]	Byte	388.0	16#0	True	True	False	False		
DiagPuffer[206]	Byte	389.0	16#0	True	True	False	False		
DiagPuffer[207]	Byte	390.0	16#0	True	True	False	False		
DiagPuffer[208]	Byte	391.0	16#0	True	True	False	False		
DiagPuffer[209]	Byte	392.0	16#0	True	True	False	False		
DiagPuffer[210]	Byte	393.0	16#0	True	True	False	False		
DiagPuffer[211]	Byte	394.0	16#0	True	True	False	False		
DiagPuffer[212]	Byte	395.0	16#0	True	True	False	False		
DiagPuffer[213]	Byte	396.0	16#0	True	True	False	False		
DiagPuffer[214]	Byte	397.0	16#0	True	True	False	False		
DiagPuffer[215]	Byte	398.0	16#0	True	True	False	False		
DiagPuffer[216]	Byte	399.0	16#0	True	True	False	False		
DiagPuffer[217]	Byte	400.0	16#0	True	True	False	False		
DiagPuffer[218]	Byte	401.0	16#0	True	True	False	False		
DiagPuffer[219]	Byte	402.0	16#0	True	True	False	False		
DiagPuffer[220]	Byte	403.0	16#0	True	True	False	False		
DiagPuffer[221]	Byte	404.0	16#0	True	True	False	False		
DiagPuffer[222]	Byte	405.0	16#0	True	True	False	False		
DiagPuffer[223]	Byte	406.0	16#0	True	True	False	False		
DiagPuffer[224]	Byte	407.0	16#0	True	True	False	False		
DiagPuffer[225]	Byte	408.0	16#0	True	True	False	False		
DiagPuffer[226]	Byte	409.0	16#0	True	True	False	False		
DiagPuffer[227]	Byte	410.0	16#0	True	True	False	False		
DiagPuffer[228]	Byte	411.0	16#0	True	True	False	False		
DiagPuffer[229]	Byte	412.0	16#0	True	True	False	False		
DiagPuffer[230]	Byte	413.0	16#0	True	True	False	False		
DiagPuffer[231]	Byte	414.0	16#0	True	True	False	False		
DiagPuffer[232]	Byte	415.0	16#0	True	True	False	False		
DiagPuffer[233]	Byte	416.0	16#0	True	True	False	False		
DiagPuffer[234]	Byte	417.0	16#0	True	True	False	False		
DiagPuffer[235]	Byte	418.0	16#0	True	True	False	False		
DiagPuffer[236]	Byte	419.0	16#0	True	True	False	False		
DiagPuffer[237]	Byte	420.0	16#0	True	True	False	False		
DiagPuffer[238]	Byte	421.0	16#0	True	True	False	False		
DiagPuffer[239]	Byte	422.0	16#0	True	True	False	False		
DiagPuffer[240]	Byte	423.0	16#0	True	True	False	False		
DiagPuffer[241]	Byte	424.0	16#0	True	True	False	False		
DiagPuffer[242]	Byte	425.0	16#0	True	True	False	False		
DiagPuffer[243]	Byte	426.0	16#0	True	True	False	False		
DiagPuffer[244]	Byte	427.0	16#0	True	True	False	False		
DiagPuffer[245]	Byte	428.0	16#0	True	True	False	False		
DiagPuffer[246]	Byte	429.0	16#0	True	True	False	False		
DiagPuffer[247]	Byte	430.0	16#0	True	True	False	False		
DiagPuffer[248]	Byte	431.0	16#0	True	True	False	False		
DiagPuffer[249]	Byte	432.0	16#0	True	True	False	False		
DiagPuffer[250]	Byte	433.0	16#0	True	True	False	False		
DiagPuffer[251]	Byte	434.0	16#0	True	True	False	False		
DiagPuffer[252]	Byte	435.0	16#0	True	True	False	False		
DiagPuffer[253]	Byte	436.0	16#0	True	True	False	False		
DiagPuffer[254]	Byte	437.0	16#0	True	True	False	False		
DiagPuffer[255]	Byte	438.0	16#0	True	True	False	False		
DiagPuffer[256]	Byte	439.0	16#0	True	True	False	False		
DiagPuffer[257]	Byte	440.0	16#0	True	True	False	False		
DiagPuffer[258]	Byte	441.0	16#0	True	True	False	False		
DiagPuffer[259]	Byte	442.0	16#0	True	True	False	False		
DiagPuffer[260]	Byte	443.0	16#0	True	True	False	False		
DiagPuffer[261]	Byte	444.0	16#0	True	True	False	False		
DiagPuffer[262]	Byte	445.0	16#0	True	True	False	False		
DiagPuffer[263]	Byte	446.0	16#0	True	True	False	False		
DiagPuffer[264]	Byte	447.0	16#0	True	True	False	False		
DiagPuffer[265]	Byte	448.0	16#0	True	True	False	False		
DiagPuffer[266]	Byte	449.0	16#0	True	True	False	False		
DiagPuffer[267]	Byte	450.0	16#0	True	True	False	False		
DiagPuffer[268]	Byte	451.0	16#0	True	True	False	False		
DiagPuffer[269]	Byte	452.0	16#0	True	True	False	False		
DiagPuffer[270]	Byte	453.0	16#0	True	True	False	False		
DiagPuffer[271]	Byte	454.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
DiagPuffer[272]	Byte	455.0	16#0	True	True	False	False		
▼ PufferSFB52	Array[1..240] of Byte	456.0		True	True	False	False		
PufferSFB52[1]	Byte	456.0	16#0	True	True	False	False		
PufferSFB52[2]	Byte	457.0	16#0	True	True	False	False		
PufferSFB52[3]	Byte	458.0	16#0	True	True	False	False		
PufferSFB52[4]	Byte	459.0	16#0	True	True	False	False		
PufferSFB52[5]	Byte	460.0	16#0	True	True	False	False		
PufferSFB52[6]	Byte	461.0	16#0	True	True	False	False		
PufferSFB52[7]	Byte	462.0	16#0	True	True	False	False		
PufferSFB52[8]	Byte	463.0	16#0	True	True	False	False		
PufferSFB52[9]	Byte	464.0	16#0	True	True	False	False		
PufferSFB52[10]	Byte	465.0	16#0	True	True	False	False		
PufferSFB52[11]	Byte	466.0	16#0	True	True	False	False		
PufferSFB52[12]	Byte	467.0	16#0	True	True	False	False		
PufferSFB52[13]	Byte	468.0	16#0	True	True	False	False		
PufferSFB52[14]	Byte	469.0	16#0	True	True	False	False		
PufferSFB52[15]	Byte	470.0	16#0	True	True	False	False		
PufferSFB52[16]	Byte	471.0	16#0	True	True	False	False		
PufferSFB52[17]	Byte	472.0	16#0	True	True	False	False		
PufferSFB52[18]	Byte	473.0	16#0	True	True	False	False		
PufferSFB52[19]	Byte	474.0	16#0	True	True	False	False		
PufferSFB52[20]	Byte	475.0	16#0	True	True	False	False		
PufferSFB52[21]	Byte	476.0	16#0	True	True	False	False		
PufferSFB52[22]	Byte	477.0	16#0	True	True	False	False		
PufferSFB52[23]	Byte	478.0	16#0	True	True	False	False		
PufferSFB52[24]	Byte	479.0	16#0	True	True	False	False		
PufferSFB52[25]	Byte	480.0	16#0	True	True	False	False		
PufferSFB52[26]	Byte	481.0	16#0	True	True	False	False		
PufferSFB52[27]	Byte	482.0	16#0	True	True	False	False		
PufferSFB52[28]	Byte	483.0	16#0	True	True	False	False		
PufferSFB52[29]	Byte	484.0	16#0	True	True	False	False		
PufferSFB52[30]	Byte	485.0	16#0	True	True	False	False		
PufferSFB52[31]	Byte	486.0	16#0	True	True	False	False		
PufferSFB52[32]	Byte	487.0	16#0	True	True	False	False		
PufferSFB52[33]	Byte	488.0	16#0	True	True	False	False		
PufferSFB52[34]	Byte	489.0	16#0	True	True	False	False		
PufferSFB52[35]	Byte	490.0	16#0	True	True	False	False		
PufferSFB52[36]	Byte	491.0	16#0	True	True	False	False		
PufferSFB52[37]	Byte	492.0	16#0	True	True	False	False		
PufferSFB52[38]	Byte	493.0	16#0	True	True	False	False		
PufferSFB52[39]	Byte	494.0	16#0	True	True	False	False		
PufferSFB52[40]	Byte	495.0	16#0	True	True	False	False		
PufferSFB52[41]	Byte	496.0	16#0	True	True	False	False		
PufferSFB52[42]	Byte	497.0	16#0	True	True	False	False		
PufferSFB52[43]	Byte	498.0	16#0	True	True	False	False		
PufferSFB52[44]	Byte	499.0	16#0	True	True	False	False		
PufferSFB52[45]	Byte	500.0	16#0	True	True	False	False		
PufferSFB52[46]	Byte	501.0	16#0	True	True	False	False		
PufferSFB52[47]	Byte	502.0	16#0	True	True	False	False		
PufferSFB52[48]	Byte	503.0	16#0	True	True	False	False		
PufferSFB52[49]	Byte	504.0	16#0	True	True	False	False		
PufferSFB52[50]	Byte	505.0	16#0	True	True	False	False		
PufferSFB52[51]	Byte	506.0	16#0	True	True	False	False		
PufferSFB52[52]	Byte	507.0	16#0	True	True	False	False		
PufferSFB52[53]	Byte	508.0	16#0	True	True	False	False		
PufferSFB52[54]	Byte	509.0	16#0	True	True	False	False		
PufferSFB52[55]	Byte	510.0	16#0	True	True	False	False		
PufferSFB52[56]	Byte	511.0	16#0	True	True	False	False		
PufferSFB52[57]	Byte	512.0	16#0	True	True	False	False		
PufferSFB52[58]	Byte	513.0	16#0	True	True	False	False		
PufferSFB52[59]	Byte	514.0	16#0	True	True	False	False		
PufferSFB52[60]	Byte	515.0	16#0	True	True	False	False		
PufferSFB52[61]	Byte	516.0	16#0	True	True	False	False		
PufferSFB52[62]	Byte	517.0	16#0	True	True	False	False		
PufferSFB52[63]	Byte	518.0	16#0	True	True	False	False		
PufferSFB52[64]	Byte	519.0	16#0	True	True	False	False		
PufferSFB52[65]	Byte	520.0	16#0	True	True	False	False		
PufferSFB52[66]	Byte	521.0	16#0	True	True	False	False		
PufferSFB52[67]	Byte	522.0	16#0	True	True	False	False		
PufferSFB52[68]	Byte	523.0	16#0	True	True	False	False		
PufferSFB52[69]	Byte	524.0	16#0	True	True	False	False		
PufferSFB52[70]	Byte	525.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
PufferSFB52[71]	Byte	526.0	16#0	True	True	False	False		
PufferSFB52[72]	Byte	527.0	16#0	True	True	False	False		
PufferSFB52[73]	Byte	528.0	16#0	True	True	False	False		
PufferSFB52[74]	Byte	529.0	16#0	True	True	False	False		
PufferSFB52[75]	Byte	530.0	16#0	True	True	False	False		
PufferSFB52[76]	Byte	531.0	16#0	True	True	False	False		
PufferSFB52[77]	Byte	532.0	16#0	True	True	False	False		
PufferSFB52[78]	Byte	533.0	16#0	True	True	False	False		
PufferSFB52[79]	Byte	534.0	16#0	True	True	False	False		
PufferSFB52[80]	Byte	535.0	16#0	True	True	False	False		
PufferSFB52[81]	Byte	536.0	16#0	True	True	False	False		
PufferSFB52[82]	Byte	537.0	16#0	True	True	False	False		
PufferSFB52[83]	Byte	538.0	16#0	True	True	False	False		
PufferSFB52[84]	Byte	539.0	16#0	True	True	False	False		
PufferSFB52[85]	Byte	540.0	16#0	True	True	False	False		
PufferSFB52[86]	Byte	541.0	16#0	True	True	False	False		
PufferSFB52[87]	Byte	542.0	16#0	True	True	False	False		
PufferSFB52[88]	Byte	543.0	16#0	True	True	False	False		
PufferSFB52[89]	Byte	544.0	16#0	True	True	False	False		
PufferSFB52[90]	Byte	545.0	16#0	True	True	False	False		
PufferSFB52[91]	Byte	546.0	16#0	True	True	False	False		
PufferSFB52[92]	Byte	547.0	16#0	True	True	False	False		
PufferSFB52[93]	Byte	548.0	16#0	True	True	False	False		
PufferSFB52[94]	Byte	549.0	16#0	True	True	False	False		
PufferSFB52[95]	Byte	550.0	16#0	True	True	False	False		
PufferSFB52[96]	Byte	551.0	16#0	True	True	False	False		
PufferSFB52[97]	Byte	552.0	16#0	True	True	False	False		
PufferSFB52[98]	Byte	553.0	16#0	True	True	False	False		
PufferSFB52[99]	Byte	554.0	16#0	True	True	False	False		
PufferSFB52[100]	Byte	555.0	16#0	True	True	False	False		
PufferSFB52[101]	Byte	556.0	16#0	True	True	False	False		
PufferSFB52[102]	Byte	557.0	16#0	True	True	False	False		
PufferSFB52[103]	Byte	558.0	16#0	True	True	False	False		
PufferSFB52[104]	Byte	559.0	16#0	True	True	False	False		
PufferSFB52[105]	Byte	560.0	16#0	True	True	False	False		
PufferSFB52[106]	Byte	561.0	16#0	True	True	False	False		
PufferSFB52[107]	Byte	562.0	16#0	True	True	False	False		
PufferSFB52[108]	Byte	563.0	16#0	True	True	False	False		
PufferSFB52[109]	Byte	564.0	16#0	True	True	False	False		
PufferSFB52[110]	Byte	565.0	16#0	True	True	False	False		
PufferSFB52[111]	Byte	566.0	16#0	True	True	False	False		
PufferSFB52[112]	Byte	567.0	16#0	True	True	False	False		
PufferSFB52[113]	Byte	568.0	16#0	True	True	False	False		
PufferSFB52[114]	Byte	569.0	16#0	True	True	False	False		
PufferSFB52[115]	Byte	570.0	16#0	True	True	False	False		
PufferSFB52[116]	Byte	571.0	16#0	True	True	False	False		
PufferSFB52[117]	Byte	572.0	16#0	True	True	False	False		
PufferSFB52[118]	Byte	573.0	16#0	True	True	False	False		
PufferSFB52[119]	Byte	574.0	16#0	True	True	False	False		
PufferSFB52[120]	Byte	575.0	16#0	True	True	False	False		
PufferSFB52[121]	Byte	576.0	16#0	True	True	False	False		
PufferSFB52[122]	Byte	577.0	16#0	True	True	False	False		
PufferSFB52[123]	Byte	578.0	16#0	True	True	False	False		
PufferSFB52[124]	Byte	579.0	16#0	True	True	False	False		
PufferSFB52[125]	Byte	580.0	16#0	True	True	False	False		
PufferSFB52[126]	Byte	581.0	16#0	True	True	False	False		
PufferSFB52[127]	Byte	582.0	16#0	True	True	False	False		
PufferSFB52[128]	Byte	583.0	16#0	True	True	False	False		
PufferSFB52[129]	Byte	584.0	16#0	True	True	False	False		
PufferSFB52[130]	Byte	585.0	16#0	True	True	False	False		
PufferSFB52[131]	Byte	586.0	16#0	True	True	False	False		
PufferSFB52[132]	Byte	587.0	16#0	True	True	False	False		
PufferSFB52[133]	Byte	588.0	16#0	True	True	False	False		
PufferSFB52[134]	Byte	589.0	16#0	True	True	False	False		
PufferSFB52[135]	Byte	590.0	16#0	True	True	False	False		
PufferSFB52[136]	Byte	591.0	16#0	True	True	False	False		
PufferSFB52[137]	Byte	592.0	16#0	True	True	False	False		
PufferSFB52[138]	Byte	593.0	16#0	True	True	False	False		
PufferSFB52[139]	Byte	594.0	16#0	True	True	False	False		
PufferSFB52[140]	Byte	595.0	16#0	True	True	False	False		
PufferSFB52[141]	Byte	596.0	16#0	True	True	False	False		
PufferSFB52[142]	Byte	597.0	16#0	True	True	False	False		
PufferSFB52[143]	Byte	598.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
PufferSFB52[144]	Byte	599.0	16#0	True	True	False	False		
PufferSFB52[145]	Byte	600.0	16#0	True	True	False	False		
PufferSFB52[146]	Byte	601.0	16#0	True	True	False	False		
PufferSFB52[147]	Byte	602.0	16#0	True	True	False	False		
PufferSFB52[148]	Byte	603.0	16#0	True	True	False	False		
PufferSFB52[149]	Byte	604.0	16#0	True	True	False	False		
PufferSFB52[150]	Byte	605.0	16#0	True	True	False	False		
PufferSFB52[151]	Byte	606.0	16#0	True	True	False	False		
PufferSFB52[152]	Byte	607.0	16#0	True	True	False	False		
PufferSFB52[153]	Byte	608.0	16#0	True	True	False	False		
PufferSFB52[154]	Byte	609.0	16#0	True	True	False	False		
PufferSFB52[155]	Byte	610.0	16#0	True	True	False	False		
PufferSFB52[156]	Byte	611.0	16#0	True	True	False	False		
PufferSFB52[157]	Byte	612.0	16#0	True	True	False	False		
PufferSFB52[158]	Byte	613.0	16#0	True	True	False	False		
PufferSFB52[159]	Byte	614.0	16#0	True	True	False	False		
PufferSFB52[160]	Byte	615.0	16#0	True	True	False	False		
PufferSFB52[161]	Byte	616.0	16#0	True	True	False	False		
PufferSFB52[162]	Byte	617.0	16#0	True	True	False	False		
PufferSFB52[163]	Byte	618.0	16#0	True	True	False	False		
PufferSFB52[164]	Byte	619.0	16#0	True	True	False	False		
PufferSFB52[165]	Byte	620.0	16#0	True	True	False	False		
PufferSFB52[166]	Byte	621.0	16#0	True	True	False	False		
PufferSFB52[167]	Byte	622.0	16#0	True	True	False	False		
PufferSFB52[168]	Byte	623.0	16#0	True	True	False	False		
PufferSFB52[169]	Byte	624.0	16#0	True	True	False	False		
PufferSFB52[170]	Byte	625.0	16#0	True	True	False	False		
PufferSFB52[171]	Byte	626.0	16#0	True	True	False	False		
PufferSFB52[172]	Byte	627.0	16#0	True	True	False	False		
PufferSFB52[173]	Byte	628.0	16#0	True	True	False	False		
PufferSFB52[174]	Byte	629.0	16#0	True	True	False	False		
PufferSFB52[175]	Byte	630.0	16#0	True	True	False	False		
PufferSFB52[176]	Byte	631.0	16#0	True	True	False	False		
PufferSFB52[177]	Byte	632.0	16#0	True	True	False	False		
PufferSFB52[178]	Byte	633.0	16#0	True	True	False	False		
PufferSFB52[179]	Byte	634.0	16#0	True	True	False	False		
PufferSFB52[180]	Byte	635.0	16#0	True	True	False	False		
PufferSFB52[181]	Byte	636.0	16#0	True	True	False	False		
PufferSFB52[182]	Byte	637.0	16#0	True	True	False	False		
PufferSFB52[183]	Byte	638.0	16#0	True	True	False	False		
PufferSFB52[184]	Byte	639.0	16#0	True	True	False	False		
PufferSFB52[185]	Byte	640.0	16#0	True	True	False	False		
PufferSFB52[186]	Byte	641.0	16#0	True	True	False	False		
PufferSFB52[187]	Byte	642.0	16#0	True	True	False	False		
PufferSFB52[188]	Byte	643.0	16#0	True	True	False	False		
PufferSFB52[189]	Byte	644.0	16#0	True	True	False	False		
PufferSFB52[190]	Byte	645.0	16#0	True	True	False	False		
PufferSFB52[191]	Byte	646.0	16#0	True	True	False	False		
PufferSFB52[192]	Byte	647.0	16#0	True	True	False	False		
PufferSFB52[193]	Byte	648.0	16#0	True	True	False	False		
PufferSFB52[194]	Byte	649.0	16#0	True	True	False	False		
PufferSFB52[195]	Byte	650.0	16#0	True	True	False	False		
PufferSFB52[196]	Byte	651.0	16#0	True	True	False	False		
PufferSFB52[197]	Byte	652.0	16#0	True	True	False	False		
PufferSFB52[198]	Byte	653.0	16#0	True	True	False	False		
PufferSFB52[199]	Byte	654.0	16#0	True	True	False	False		
PufferSFB52[200]	Byte	655.0	16#0	True	True	False	False		
PufferSFB52[201]	Byte	656.0	16#0	True	True	False	False		
PufferSFB52[202]	Byte	657.0	16#0	True	True	False	False		
PufferSFB52[203]	Byte	658.0	16#0	True	True	False	False		
PufferSFB52[204]	Byte	659.0	16#0	True	True	False	False		
PufferSFB52[205]	Byte	660.0	16#0	True	True	False	False		
PufferSFB52[206]	Byte	661.0	16#0	True	True	False	False		
PufferSFB52[207]	Byte	662.0	16#0	True	True	False	False		
PufferSFB52[208]	Byte	663.0	16#0	True	True	False	False		
PufferSFB52[209]	Byte	664.0	16#0	True	True	False	False		
PufferSFB52[210]	Byte	665.0	16#0	True	True	False	False		
PufferSFB52[211]	Byte	666.0	16#0	True	True	False	False		
PufferSFB52[212]	Byte	667.0	16#0	True	True	False	False		
PufferSFB52[213]	Byte	668.0	16#0	True	True	False	False		
PufferSFB52[214]	Byte	669.0	16#0	True	True	False	False		
PufferSFB52[215]	Byte	670.0	16#0	True	True	False	False		
PufferSFB52[216]	Byte	671.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
PufferSFB52[217]	Byte	672.0	16#0	True	True	False	False		
PufferSFB52[218]	Byte	673.0	16#0	True	True	False	False		
PufferSFB52[219]	Byte	674.0	16#0	True	True	False	False		
PufferSFB52[220]	Byte	675.0	16#0	True	True	False	False		
PufferSFB52[221]	Byte	676.0	16#0	True	True	False	False		
PufferSFB52[222]	Byte	677.0	16#0	True	True	False	False		
PufferSFB52[223]	Byte	678.0	16#0	True	True	False	False		
PufferSFB52[224]	Byte	679.0	16#0	True	True	False	False		
PufferSFB52[225]	Byte	680.0	16#0	True	True	False	False		
PufferSFB52[226]	Byte	681.0	16#0	True	True	False	False		
PufferSFB52[227]	Byte	682.0	16#0	True	True	False	False		
PufferSFB52[228]	Byte	683.0	16#0	True	True	False	False		
PufferSFB52[229]	Byte	684.0	16#0	True	True	False	False		
PufferSFB52[230]	Byte	685.0	16#0	True	True	False	False		
PufferSFB52[231]	Byte	686.0	16#0	True	True	False	False		
PufferSFB52[232]	Byte	687.0	16#0	True	True	False	False		
PufferSFB52[233]	Byte	688.0	16#0	True	True	False	False		
PufferSFB52[234]	Byte	689.0	16#0	True	True	False	False		
PufferSFB52[235]	Byte	690.0	16#0	True	True	False	False		
PufferSFB52[236]	Byte	691.0	16#0	True	True	False	False		
PufferSFB52[237]	Byte	692.0	16#0	True	True	False	False		
PufferSFB52[238]	Byte	693.0	16#0	True	True	False	False		
PufferSFB52[239]	Byte	694.0	16#0	True	True	False	False		
PufferSFB52[240]	Byte	695.0	16#0	True	True	False	False		
TextListe	Array[0..3] of DWord	696.0		True	True	False	False		
TextListe[0]	DWord	696.0	16#0	True	True	False	False		
TextListe[1]	DWord	700.0	16#0	True	True	False	False		
TextListe[2]	DWord	704.0	16#0	True	True	False	False		
TextListe[3]	DWord	708.0	16#0	True	True	False	False		
wMapItemLogAdr_Ein_0	Word	712.0	WORD#16#0088	True	True	False	False		
wMapItemLogAdr_Ein_1	Word	714.0	WORD#16#0100	True	True	False	False		
wMapItemLogAdr_Ein_2	Word	716.0	WORD#16#0320	True	True	False	False		
wMapItemLogAdr_Ein_3	Word	718.0	WORD#16#0330	True	True	False	False		
wMapItemLogAdr_Ein_4	Word	720.0	WORD#16#0340	True	True	False	False		
wMapItemLogAdr_Ein_5	Word	722.0	WORD#16#07FC	True	True	False	False		
wMapItemLogAdr_Ein_6	Word	724.0	WORD#16#07FD	True	True	False	False		
wMapItemLogAdr_Ein_7	Word	726.0	WORD#16#07FE	True	True	False	False		
wMapItemLogAdr_Ein_8	Word	728.0	WORD#16#07FF	True	True	False	False		
wMapItemLogAdr_Aus_0	Word	730.0	WORD#16#0110	True	True	False	False		
wMapItemLogAdr_KAd_0	Word	732.0	WORD#16#CFFF	True	True	False	False		
wMapItemLogAdr_KAd_1	Word	734.0	WORD#16#FFFC	True	True	False	False		
wMapItemLogAdr_KAd_2	Word	736.0	WORD#16#FFFD	True	True	False	False		
wMapItemOffset_Ein_0	Word	738.0	WORD#16#00B2	True	True	False	False		
wMapItemOffset_Ein_1	Word	740.0	WORD#16#0106	True	True	False	False		
wMapItemOffset_Ein_2	Word	742.0	WORD#16#00C7	True	True	False	False		
wMapItemOffset_Ein_3	Word	744.0	WORD#16#00DC	True	True	False	False		
wMapItemOffset_Ein_4	Word	746.0	WORD#16#00F1	True	True	False	False		
wMapItemOffset_Ein_5	Word	748.0	WORD#16#009A	True	True	False	False		
wMapItemOffset_Ein_6	Word	750.0	WORD#16#0082	True	True	False	False		
wMapItemOffset_Ein_7	Word	752.0	WORD#16#0069	True	True	False	False		
wMapItemOffset_Ein_8	Word	754.0	WORD#16#0054	True	True	False	False		
wMapItemOffset_Aus_0	Word	756.0	WORD#16#011B	True	True	False	False		
wMapItemOffset_KAd_0	Word	758.0	WORD#16#0032	True	True	False	False		
wMapItemOffset_KAd_1	Word	760.0	WORD#16#0022	True	True	False	False		
wMapItemOffset_KAd_2	Word	762.0	WORD#16#0010	True	True	False	False		
bMapItemDB_Ein_0	Word	764.0	WORD#16#0203	True	True	False	False		
bMapItemDB_Ein_1	Word	766.0	WORD#16#0103	True	True	False	False		
bMapItemDB_Ein_2	Word	768.0	WORD#16#0203	True	True	False	False		
bMapItemDB_Ein_3	Word	770.0	WORD#16#0203	True	True	False	False		
bMapItemDB_Ein_4	Word	772.0	WORD#16#0203	True	True	False	False		
bMapItemDB_Ein_5	Word	774.0	WORD#16#0205	True	True	False	False		
bMapItemDB_Ein_6	Word	776.0	WORD#16#0205	True	True	False	False		
bMapItemDB_Ein_7	Word	778.0	WORD#16#0204	True	True	False	False		
bMapItemDB_Ein_8	Word	780.0	WORD#16#0203	True	True	False	False		
bMapItemDB_Aus_0	Word	782.0	WORD#16#0103	True	True	False	False		
bMapItemDB_KAd_0	Word	784.0	WORD#16#0102	True	True	False	False		
bMapItemDB_KAd_1	Word	786.0	WORD#16#0101	True	True	False	False		
bMapItemDB_KAd_2	Word	788.0	WORD#16#0000	True	True	False	False		
wMapItemSubstAdr_0	Word	790.0	WORD#16#FFFF	True	True	False	False		
wMapItemSubstAdr_1	Word	792.0	WORD#16#FFFF	True	True	False	False		
wMapItemSubstAdr_2	Word	794.0	WORD#16#FFFF	True	True	False	False		
Hierarchie	Word	796.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Peripherie	Word	798.0	16#0	True	True	False	False		
bBGVeraltetTiefe	DWord	800.0	16#0	True	True	False	False		
bBGSubOrdinateDepth	DWord	804.0	16#0	True	True	False	False		
bBGVFTable	DWord	808.0	16#0	True	True	False	False		
pLogAdr	DWord	812.0	16#0	True	True	False	False		
wDatenDB	Word	816.0	16#0	True	True	False	False		
pDBOffset	DWord	818.0	16#0	True	True	False	False		
pTextListID	DWord	822.0	16#0	True	True	False	False		
wMap3	Word	826.0	16#0	True	True	False	False		
xLoadBGAR1Log	Bool	828.0	false	True	True	False	False		
xLoadBGAdrAus	Bool	828.1	false	True	True	False	False		
xLoadS7AdresseVirt	Bool	828.2	false	True	True	False	False		
bDBIndex	Byte	829.0	16#0	True	True	False	False		
wLoadHWID	Word	830.0	16#0	True	True	False	False		
dS7Adresse	DWord	832.0	16#0	True	True	False	False		
wMeldeNummer	Word	836.0	16#0	True	True	False	False		
wFehlerNummer	Word	838.0	16#0	True	True	False	False		
pContTypUndFlags	Word	840.0	16#0	True	True	False	False		
wDaten1	Word	842.0	16#0	True	True	False	False		
dDaten2	DWord	844.0	16#0	True	True	False	False		
wDaten3	Word	848.0	16#0	True	True	False	False		
dDS0Daten	DWord	850.0	16#0	True	True	False	False		
LoadAlarmAnzahl	Byte	854.0	16#0	True	True	False	False		
▼ LoadAlarmArray	Array[1..25] of DWord	856.0		True	True	False	False		
LoadAlarmArray[1]	DWord	856.0	16#0	True	True	False	False		
LoadAlarmArray[2]	DWord	860.0	16#0	True	True	False	False		
LoadAlarmArray[3]	DWord	864.0	16#0	True	True	False	False		
LoadAlarmArray[4]	DWord	868.0	16#0	True	True	False	False		
LoadAlarmArray[5]	DWord	872.0	16#0	True	True	False	False		
LoadAlarmArray[6]	DWord	876.0	16#0	True	True	False	False		
LoadAlarmArray[7]	DWord	880.0	16#0	True	True	False	False		
LoadAlarmArray[8]	DWord	884.0	16#0	True	True	False	False		
LoadAlarmArray[9]	DWord	888.0	16#0	True	True	False	False		
LoadAlarmArray[10]	DWord	892.0	16#0	True	True	False	False		
LoadAlarmArray[11]	DWord	896.0	16#0	True	True	False	False		
LoadAlarmArray[12]	DWord	900.0	16#0	True	True	False	False		
LoadAlarmArray[13]	DWord	904.0	16#0	True	True	False	False		
LoadAlarmArray[14]	DWord	908.0	16#0	True	True	False	False		
LoadAlarmArray[15]	DWord	912.0	16#0	True	True	False	False		
LoadAlarmArray[16]	DWord	916.0	16#0	True	True	False	False		
LoadAlarmArray[17]	DWord	920.0	16#0	True	True	False	False		
LoadAlarmArray[18]	DWord	924.0	16#0	True	True	False	False		
LoadAlarmArray[19]	DWord	928.0	16#0	True	True	False	False		
LoadAlarmArray[20]	DWord	932.0	16#0	True	True	False	False		
LoadAlarmArray[21]	DWord	936.0	16#0	True	True	False	False		
LoadAlarmArray[22]	DWord	940.0	16#0	True	True	False	False		
LoadAlarmArray[23]	DWord	944.0	16#0	True	True	False	False		
LoadAlarmArray[24]	DWord	948.0	16#0	True	True	False	False		
LoadAlarmArray[25]	DWord	952.0	16#0	True	True	False	False		
LoadAlarmStateError	Bool	956.0	false	True	True	False	False		
LoadAlarmStateMaint1	Bool	956.1	false	True	True	False	False		
LoadAlarmStateMaint2	Bool	956.2	false	True	True	False	False		
FilterAlarmAnzahl	Byte	957.0	16#0	True	True	False	False		
▼ FilterAlarmArray	Array[1..25] of DWord	958.0		True	True	False	False		
FilterAlarmArray[1]	DWord	958.0	16#0	True	True	False	False		
FilterAlarmArray[2]	DWord	962.0	16#0	True	True	False	False		
FilterAlarmArray[3]	DWord	966.0	16#0	True	True	False	False		
FilterAlarmArray[4]	DWord	970.0	16#0	True	True	False	False		
FilterAlarmArray[5]	DWord	974.0	16#0	True	True	False	False		
FilterAlarmArray[6]	DWord	978.0	16#0	True	True	False	False		
FilterAlarmArray[7]	DWord	982.0	16#0	True	True	False	False		
FilterAlarmArray[8]	DWord	986.0	16#0	True	True	False	False		
FilterAlarmArray[9]	DWord	990.0	16#0	True	True	False	False		
FilterAlarmArray[10]	DWord	994.0	16#0	True	True	False	False		
FilterAlarmArray[11]	DWord	998.0	16#0	True	True	False	False		
FilterAlarmArray[12]	DWord	1002.0	16#0	True	True	False	False		
FilterAlarmArray[13]	DWord	1006.0	16#0	True	True	False	False		
FilterAlarmArray[14]	DWord	1010.0	16#0	True	True	False	False		
FilterAlarmArray[15]	DWord	1014.0	16#0	True	True	False	False		
FilterAlarmArray[16]	DWord	1018.0	16#0	True	True	False	False		
FilterAlarmArray[17]	DWord	1022.0	16#0	True	True	False	False		
FilterAlarmArray[18]	DWord	1026.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
FilterAlarmArray[19]	DWord	1030.0	16#0	True	True	False	False		
FilterAlarmArray[20]	DWord	1034.0	16#0	True	True	False	False		
FilterAlarmArray[21]	DWord	1038.0	16#0	True	True	False	False		
FilterAlarmArray[22]	DWord	1042.0	16#0	True	True	False	False		
FilterAlarmArray[23]	DWord	1046.0	16#0	True	True	False	False		
FilterAlarmArray[24]	DWord	1050.0	16#0	True	True	False	False		
FilterAlarmArray[25]	DWord	1054.0	16#0	True	True	False	False		
FilterAlarmMeldendFehler	Byte	1058.0	16#0	True	True	False	False		
FilterAlarmMeldBWartung1	Byte	1059.0	16#0	True	True	False	False		
FilterAlarmMeldBWartung2	Byte	1060.0	16#0	True	True	False	False		
SubCompStateDBSubComp_a	Byte	1061.0	16#0	True	True	False	False		
SubCompStateDBSubComp_b	Byte	1062.0	16#0	True	True	False	False		
SubCompStatepSCompList_a	Byte	1063.0	16#0	True	True	False	False		
SubCompStatepSCompList_b	Byte	1064.0	16#0	True	True	False	False		
SubCompStatepSCompList_c	Byte	1065.0	16#0	True	True	False	False		
SubCompStatepSCompList_d	Byte	1066.0	16#0	True	True	False	False		
SubCompStateiSCompLeft_a	Byte	1067.0	16#0	True	True	False	False		
SubCompStateiSCompLeft_b	Byte	1068.0	16#0	True	True	False	False		
SubCompStatepOompState_a	Byte	1069.0	16#0	True	True	False	False		
SubCompStatepOompState_b	Byte	1070.0	16#0	True	True	False	False		
SubCompStatepOompState_c	Byte	1071.0	16#0	True	True	False	False		
SubCompStatepOompState_d	Byte	1072.0	16#0	True	True	False	False		
dAR1_a	Byte	1073.0	BYTE#16#00	True	True	False	False		
dAR1_b	Byte	1074.0	BYTE#16#00	True	True	False	False		
dAR1_c	Byte	1075.0	BYTE#16#00	True	True	False	False		
dAR1_d	Byte	1076.0	BYTE#16#00	True	True	False	False		
dAR2_a	Byte	1077.0	BYTE#16#00	True	True	False	False		
dAR2_b	Byte	1078.0	BYTE#16#00	True	True	False	False		
dAR2_c	Byte	1079.0	BYTE#16#00	True	True	False	False		
dAR2_d	Byte	1080.0	BYTE#16#00	True	True	False	False		
▼ FCTempDaten	Array[1..94] of Byte	1082.0		True	True	False	False		
FCTempDaten[1]	Byte	1082.0	16#0	True	True	False	False		
FCTempDaten[2]	Byte	1083.0	16#0	True	True	False	False		
FCTempDaten[3]	Byte	1084.0	16#0	True	True	False	False		
FCTempDaten[4]	Byte	1085.0	16#0	True	True	False	False		
FCTempDaten[5]	Byte	1086.0	16#0	True	True	False	False		
FCTempDaten[6]	Byte	1087.0	16#0	True	True	False	False		
FCTempDaten[7]	Byte	1088.0	16#0	True	True	False	False		
FCTempDaten[8]	Byte	1089.0	16#0	True	True	False	False		
FCTempDaten[9]	Byte	1090.0	16#0	True	True	False	False		
FCTempDaten[10]	Byte	1091.0	16#0	True	True	False	False		
FCTempDaten[11]	Byte	1092.0	16#0	True	True	False	False		
FCTempDaten[12]	Byte	1093.0	16#0	True	True	False	False		
FCTempDaten[13]	Byte	1094.0	16#0	True	True	False	False		
FCTempDaten[14]	Byte	1095.0	16#0	True	True	False	False		
FCTempDaten[15]	Byte	1096.0	16#0	True	True	False	False		
FCTempDaten[16]	Byte	1097.0	16#0	True	True	False	False		
FCTempDaten[17]	Byte	1098.0	16#0	True	True	False	False		
FCTempDaten[18]	Byte	1099.0	16#0	True	True	False	False		
FCTempDaten[19]	Byte	1100.0	16#0	True	True	False	False		
FCTempDaten[20]	Byte	1101.0	16#0	True	True	False	False		
FCTempDaten[21]	Byte	1102.0	16#0	True	True	False	False		
FCTempDaten[22]	Byte	1103.0	16#0	True	True	False	False		
FCTempDaten[23]	Byte	1104.0	16#0	True	True	False	False		
FCTempDaten[24]	Byte	1105.0	16#0	True	True	False	False		
FCTempDaten[25]	Byte	1106.0	16#0	True	True	False	False		
FCTempDaten[26]	Byte	1107.0	16#0	True	True	False	False		
FCTempDaten[27]	Byte	1108.0	16#0	True	True	False	False		
FCTempDaten[28]	Byte	1109.0	16#0	True	True	False	False		
FCTempDaten[29]	Byte	1110.0	16#0	True	True	False	False		
FCTempDaten[30]	Byte	1111.0	16#0	True	True	False	False		
FCTempDaten[31]	Byte	1112.0	16#0	True	True	False	False		
FCTempDaten[32]	Byte	1113.0	16#0	True	True	False	False		
FCTempDaten[33]	Byte	1114.0	16#0	True	True	False	False		
FCTempDaten[34]	Byte	1115.0	16#0	True	True	False	False		
FCTempDaten[35]	Byte	1116.0	16#0	True	True	False	False		
FCTempDaten[36]	Byte	1117.0	16#0	True	True	False	False		
FCTempDaten[37]	Byte	1118.0	16#0	True	True	False	False		
FCTempDaten[38]	Byte	1119.0	16#0	True	True	False	False		
FCTempDaten[39]	Byte	1120.0	16#0	True	True	False	False		
FCTempDaten[40]	Byte	1121.0	16#0	True	True	False	False		
FCTempDaten[41]	Byte	1122.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
FCTempDaten[42]	Byte	1123.0	16#0	True	True	False	False		
FCTempDaten[43]	Byte	1124.0	16#0	True	True	False	False		
FCTempDaten[44]	Byte	1125.0	16#0	True	True	False	False		
FCTempDaten[45]	Byte	1126.0	16#0	True	True	False	False		
FCTempDaten[46]	Byte	1127.0	16#0	True	True	False	False		
FCTempDaten[47]	Byte	1128.0	16#0	True	True	False	False		
FCTempDaten[48]	Byte	1129.0	16#0	True	True	False	False		
FCTempDaten[49]	Byte	1130.0	16#0	True	True	False	False		
FCTempDaten[50]	Byte	1131.0	16#0	True	True	False	False		
FCTempDaten[51]	Byte	1132.0	16#0	True	True	False	False		
FCTempDaten[52]	Byte	1133.0	16#0	True	True	False	False		
FCTempDaten[53]	Byte	1134.0	16#0	True	True	False	False		
FCTempDaten[54]	Byte	1135.0	16#0	True	True	False	False		
FCTempDaten[55]	Byte	1136.0	16#0	True	True	False	False		
FCTempDaten[56]	Byte	1137.0	16#0	True	True	False	False		
FCTempDaten[57]	Byte	1138.0	16#0	True	True	False	False		
FCTempDaten[58]	Byte	1139.0	16#0	True	True	False	False		
FCTempDaten[59]	Byte	1140.0	16#0	True	True	False	False		
FCTempDaten[60]	Byte	1141.0	16#0	True	True	False	False		
FCTempDaten[61]	Byte	1142.0	16#0	True	True	False	False		
FCTempDaten[62]	Byte	1143.0	16#0	True	True	False	False		
FCTempDaten[63]	Byte	1144.0	16#0	True	True	False	False		
FCTempDaten[64]	Byte	1145.0	16#0	True	True	False	False		
FCTempDaten[65]	Byte	1146.0	16#0	True	True	False	False		
FCTempDaten[66]	Byte	1147.0	16#0	True	True	False	False		
FCTempDaten[67]	Byte	1148.0	16#0	True	True	False	False		
FCTempDaten[68]	Byte	1149.0	16#0	True	True	False	False		
FCTempDaten[69]	Byte	1150.0	16#0	True	True	False	False		
FCTempDaten[70]	Byte	1151.0	16#0	True	True	False	False		
FCTempDaten[71]	Byte	1152.0	16#0	True	True	False	False		
FCTempDaten[72]	Byte	1153.0	16#0	True	True	False	False		
FCTempDaten[73]	Byte	1154.0	16#0	True	True	False	False		
FCTempDaten[74]	Byte	1155.0	16#0	True	True	False	False		
FCTempDaten[75]	Byte	1156.0	16#0	True	True	False	False		
FCTempDaten[76]	Byte	1157.0	16#0	True	True	False	False		
FCTempDaten[77]	Byte	1158.0	16#0	True	True	False	False		
FCTempDaten[78]	Byte	1159.0	16#0	True	True	False	False		
FCTempDaten[79]	Byte	1160.0	16#0	True	True	False	False		
FCTempDaten[80]	Byte	1161.0	16#0	True	True	False	False		
FCTempDaten[81]	Byte	1162.0	16#0	True	True	False	False		
FCTempDaten[82]	Byte	1163.0	16#0	True	True	False	False		
FCTempDaten[83]	Byte	1164.0	16#0	True	True	False	False		
FCTempDaten[84]	Byte	1165.0	16#0	True	True	False	False		
FCTempDaten[85]	Byte	1166.0	16#0	True	True	False	False		
FCTempDaten[86]	Byte	1167.0	16#0	True	True	False	False		
FCTempDaten[87]	Byte	1168.0	16#0	True	True	False	False		
FCTempDaten[88]	Byte	1169.0	16#0	True	True	False	False		
FCTempDaten[89]	Byte	1170.0	16#0	True	True	False	False		
FCTempDaten[90]	Byte	1171.0	16#0	True	True	False	False		
FCTempDaten[91]	Byte	1172.0	16#0	True	True	False	False		
FCTempDaten[92]	Byte	1173.0	16#0	True	True	False	False		
FCTempDaten[93]	Byte	1174.0	16#0	True	True	False	False		
FCTempDaten[94]	Byte	1175.0	16#0	True	True	False	False		
dAR1Save	DWord	1176.0	16#0	True	True	False	False		
dAR2Save	DWord	1180.0	16#0	True	True	False	False		
dOB_DATA1	DWord	1184.0	16#0	True	True	False	False		
dOB_DATA2	DWord	1188.0	16#0	True	True	False	False		
dOB_DATA3	DWord	1192.0	16#0	True	True	False	False		
dSyncMapAddress	DWord	1196.0	16#0	True	True	False	False		
wSyncDataDB	Word	1200.0	16#0	True	True	False	False		
dSyncDataAddress	DWord	1202.0	16#0	True	True	False	False		
dSyncVFTable	DWord	1206.0	16#0	True	True	False	False		
dSyncVeraltetTiefe	DWord	1210.0	16#0	True	True	False	False		
wSyncHierarchie	Word	1214.0	16#0	True	True	False	False		
wSyncPeripherie	Word	1216.0	16#0	True	True	False	False		
dSyncdS7Adresse	DWord	1218.0	16#0	True	True	False	False		
wSyncHWID	Word	1222.0	16#0	True	True	False	False		
xSyncSupError	Bool	1224.0	false	True	True	False	False		
xSyncVeraltet	Bool	1224.1	false	True	True	False	False		
xSyncWeiterleiten	Bool	1224.2	false	True	True	False	False		
xSyncContainer	Bool	1224.3	false	True	True	False	False		
xSyncAusfall	Bool	1224.4	false	True	True	False	False		
xSyncMoreError	Bool	1224.5	false	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
xSyncSubOld	Bool	1224.6	false	True	True	False	False		
xSyncSubFault	Bool	1224.7	false	True	True	False	False		
xSyncSubMaint1	Bool	1225.0	false	True	True	False	False		
xSyncSubMaint2	Bool	1225.1	false	True	True	False	False		
xSyncDisabled	Bool	1225.2	false	True	True	False	False		
xSyncBit_2_3	Bool	1225.3	false	True	True	False	False		
xSyncBit_2_4	Bool	1225.4	false	True	True	False	False		
xSyncBit_2_5	Bool	1225.5	false	True	True	False	False		
xSyncBit_2_6	Bool	1225.6	false	True	True	False	False		
xSyncBit_2_7	Bool	1225.7	false	True	True	False	False		
wSyncAnzahlUKomp	Word	1226.0	16#0	True	True	False	False		
dSyncpUKomp	DWord	1228.0	16#0	True	True	False	False		
wSyncGlobMeldung	Word	1232.0	16#0	True	True	False	False		
wSyncInfoMeldung	Word	1234.0	16#0	True	True	False	False		
dSyncpTMSDaten	DWord	1236.0	16#0	True	True	False	False		
dSyncpTMSDaten1	DWord	1240.0	16#0	True	True	False	False		
dSyncpChannelOK	DWord	1244.0	16#0	True	True	False	False		
wSyncKanalAnzahl	Word	1248.0	16#0	True	True	False	False		
wSyncWartAMeldung	Word	1250.0	16#0	True	True	False	False		
wSyncWartBMeldung	Word	1252.0	16#0	True	True	False	False		
wSyncMasterSysMapAddr	Word	1254.0	16#0	True	True	False	False		
wSyncHersMeldeNr	Word	1256.0	16#0	True	True	False	False		
bSyncData1	Byte	1258.0	16#0	True	True	False	False		
bSyncData2	Byte	1259.0	16#0	True	True	False	False		
bSyncData3	Byte	1260.0	16#0	True	True	False	False		
bSyncData4	Byte	1261.0	16#0	True	True	False	False		
bSyncData5	Byte	1262.0	16#0	True	True	False	False		
bSyncData6	Byte	1263.0	16#0	True	True	False	False		
bSyncData7	Byte	1264.0	16#0	True	True	False	False		
bSyncData8	Byte	1265.0	16#0	True	True	False	False		
bSyncData9	Byte	1266.0	16#0	True	True	False	False		
bSyncData10	Byte	1267.0	16#0	True	True	False	False		
bSyncData11	Byte	1268.0	16#0	True	True	False	False		
bSyncData12	Byte	1269.0	16#0	True	True	False	False		
bSyncData13	Byte	1270.0	16#0	True	True	False	False		
bSyncData14	Byte	1271.0	16#0	True	True	False	False		
bSyncData15	Byte	1272.0	16#0	True	True	False	False		
bSyncData16	Byte	1273.0	16#0	True	True	False	False		
bSyncData17	Byte	1274.0	16#0	True	True	False	False		
bSyncData18	Byte	1275.0	16#0	True	True	False	False		
bSyncData19	Byte	1276.0	16#0	True	True	False	False		
bSyncData20	Byte	1277.0	16#0	True	True	False	False		
bSyncData21	Byte	1278.0	16#0	True	True	False	False		
bSyncData22	Byte	1279.0	16#0	True	True	False	False		
bSyncData23	Byte	1280.0	16#0	True	True	False	False		
bSyncData24	Byte	1281.0	16#0	True	True	False	False		
bSyncData25	Byte	1282.0	16#0	True	True	False	False		
bSyncData26	Byte	1283.0	16#0	True	True	False	False		
bSyncData27	Byte	1284.0	16#0	True	True	False	False		
bSyncData28	Byte	1285.0	16#0	True	True	False	False		
bSyncData29	Byte	1286.0	16#0	True	True	False	False		
bSyncData30	Byte	1287.0	16#0	True	True	False	False		
bSyncData31	Byte	1288.0	16#0	True	True	False	False		
bSyncData32	Byte	1289.0	16#0	True	True	False	False		
bSyncData33	Byte	1290.0	16#0	True	True	False	False		
bSyncData34	Byte	1291.0	16#0	True	True	False	False		
bSyncData35	Byte	1292.0	16#0	True	True	False	False		
bSyncData36	Byte	1293.0	16#0	True	True	False	False		
bSyncData37	Byte	1294.0	16#0	True	True	False	False		
bSyncData38	Byte	1295.0	16#0	True	True	False	False		
bSyncData39	Byte	1296.0	16#0	True	True	False	False		
bSyncData40	Byte	1297.0	16#0	True	True	False	False		
bSyncData41	Byte	1298.0	16#0	True	True	False	False		
bSyncData42	Byte	1299.0	16#0	True	True	False	False		
bSyncData43	Byte	1300.0	16#0	True	True	False	False		
bSyncData44	Byte	1301.0	16#0	True	True	False	False		
bSyncData45	Byte	1302.0	16#0	True	True	False	False		
bSyncData46	Byte	1303.0	16#0	True	True	False	False		
bSyncData47	Byte	1304.0	16#0	True	True	False	False		
bSyncData48	Byte	1305.0	16#0	True	True	False	False		
bSyncData49	Byte	1306.0	16#0	True	True	False	False		
bSyncData50	Byte	1307.0	16#0	True	True	False	False		
bSyncData51	Byte	1308.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
bSyncData52	Byte	1309.0	16#0	True	True	False	False		
bSyncData53	Byte	1310.0	16#0	True	True	False	False		
bSyncData54	Byte	1311.0	16#0	True	True	False	False		
bSyncData55	Byte	1312.0	16#0	True	True	False	False		
bSyncData56	Byte	1313.0	16#0	True	True	False	False		
bSyncData57	Byte	1314.0	16#0	True	True	False	False		
bSyncData58	Byte	1315.0	16#0	True	True	False	False		
bSyncData59	Byte	1316.0	16#0	True	True	False	False		
bSyncData60	Byte	1317.0	16#0	True	True	False	False		
bSyncData61	Byte	1318.0	16#0	True	True	False	False		
bSyncData62	Byte	1319.0	16#0	True	True	False	False		
bSyncData63	Byte	1320.0	16#0	True	True	False	False		
bSyncData64	Byte	1321.0	16#0	True	True	False	False		
bSyncData65	Byte	1322.0	16#0	True	True	False	False		
bSyncData66	Byte	1323.0	16#0	True	True	False	False		
xVoll	Bool	1324.0	FALSE	True	True	False	False		
xUeberVoll	Bool	1324.1	FALSE	True	True	False	False		
xUeberfaelligAlarmID	Bool	1324.2	FALSE	True	True	False	False		
bVAlarmID	Byte	1325.0	BYTE#16#00	True	True	False	False		
bSAlarmID	Byte	1326.0	BYTE#16#00	True	True	False	False		
bAnzahlFreieEintraege	Byte	1327.0	BYTE#16#1E	True	True	False	False		
▼ Meldepuffer	Array[1..510] of Byte	1328.0		True	True	False	False		
Meldepuffer[1]	Byte	1328.0	16#0	True	True	False	False		
Meldepuffer[2]	Byte	1329.0	16#0	True	True	False	False		
Meldepuffer[3]	Byte	1330.0	16#0	True	True	False	False		
Meldepuffer[4]	Byte	1331.0	16#0	True	True	False	False		
Meldepuffer[5]	Byte	1332.0	16#0	True	True	False	False		
Meldepuffer[6]	Byte	1333.0	16#0	True	True	False	False		
Meldepuffer[7]	Byte	1334.0	16#0	True	True	False	False		
Meldepuffer[8]	Byte	1335.0	16#0	True	True	False	False		
Meldepuffer[9]	Byte	1336.0	16#0	True	True	False	False		
Meldepuffer[10]	Byte	1337.0	16#0	True	True	False	False		
Meldepuffer[11]	Byte	1338.0	16#0	True	True	False	False		
Meldepuffer[12]	Byte	1339.0	16#0	True	True	False	False		
Meldepuffer[13]	Byte	1340.0	16#0	True	True	False	False		
Meldepuffer[14]	Byte	1341.0	16#0	True	True	False	False		
Meldepuffer[15]	Byte	1342.0	16#0	True	True	False	False		
Meldepuffer[16]	Byte	1343.0	16#0	True	True	False	False		
Meldepuffer[17]	Byte	1344.0	16#0	True	True	False	False		
Meldepuffer[18]	Byte	1345.0	16#0	True	True	False	False		
Meldepuffer[19]	Byte	1346.0	16#0	True	True	False	False		
Meldepuffer[20]	Byte	1347.0	16#0	True	True	False	False		
Meldepuffer[21]	Byte	1348.0	16#0	True	True	False	False		
Meldepuffer[22]	Byte	1349.0	16#0	True	True	False	False		
Meldepuffer[23]	Byte	1350.0	16#0	True	True	False	False		
Meldepuffer[24]	Byte	1351.0	16#0	True	True	False	False		
Meldepuffer[25]	Byte	1352.0	16#0	True	True	False	False		
Meldepuffer[26]	Byte	1353.0	16#0	True	True	False	False		
Meldepuffer[27]	Byte	1354.0	16#0	True	True	False	False		
Meldepuffer[28]	Byte	1355.0	16#0	True	True	False	False		
Meldepuffer[29]	Byte	1356.0	16#0	True	True	False	False		
Meldepuffer[30]	Byte	1357.0	16#0	True	True	False	False		
Meldepuffer[31]	Byte	1358.0	16#0	True	True	False	False		
Meldepuffer[32]	Byte	1359.0	16#0	True	True	False	False		
Meldepuffer[33]	Byte	1360.0	16#0	True	True	False	False		
Meldepuffer[34]	Byte	1361.0	16#0	True	True	False	False		
Meldepuffer[35]	Byte	1362.0	16#0	True	True	False	False		
Meldepuffer[36]	Byte	1363.0	16#0	True	True	False	False		
Meldepuffer[37]	Byte	1364.0	16#0	True	True	False	False		
Meldepuffer[38]	Byte	1365.0	16#0	True	True	False	False		
Meldepuffer[39]	Byte	1366.0	16#0	True	True	False	False		
Meldepuffer[40]	Byte	1367.0	16#0	True	True	False	False		
Meldepuffer[41]	Byte	1368.0	16#0	True	True	False	False		
Meldepuffer[42]	Byte	1369.0	16#0	True	True	False	False		
Meldepuffer[43]	Byte	1370.0	16#0	True	True	False	False		
Meldepuffer[44]	Byte	1371.0	16#0	True	True	False	False		
Meldepuffer[45]	Byte	1372.0	16#0	True	True	False	False		
Meldepuffer[46]	Byte	1373.0	16#0	True	True	False	False		
Meldepuffer[47]	Byte	1374.0	16#0	True	True	False	False		
Meldepuffer[48]	Byte	1375.0	16#0	True	True	False	False		
Meldepuffer[49]	Byte	1376.0	16#0	True	True	False	False		
Meldepuffer[50]	Byte	1377.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[51]	Byte	1378.0	16#0	True	True	False	False		
Meldepuffer[52]	Byte	1379.0	16#0	True	True	False	False		
Meldepuffer[53]	Byte	1380.0	16#0	True	True	False	False		
Meldepuffer[54]	Byte	1381.0	16#0	True	True	False	False		
Meldepuffer[55]	Byte	1382.0	16#0	True	True	False	False		
Meldepuffer[56]	Byte	1383.0	16#0	True	True	False	False		
Meldepuffer[57]	Byte	1384.0	16#0	True	True	False	False		
Meldepuffer[58]	Byte	1385.0	16#0	True	True	False	False		
Meldepuffer[59]	Byte	1386.0	16#0	True	True	False	False		
Meldepuffer[60]	Byte	1387.0	16#0	True	True	False	False		
Meldepuffer[61]	Byte	1388.0	16#0	True	True	False	False		
Meldepuffer[62]	Byte	1389.0	16#0	True	True	False	False		
Meldepuffer[63]	Byte	1390.0	16#0	True	True	False	False		
Meldepuffer[64]	Byte	1391.0	16#0	True	True	False	False		
Meldepuffer[65]	Byte	1392.0	16#0	True	True	False	False		
Meldepuffer[66]	Byte	1393.0	16#0	True	True	False	False		
Meldepuffer[67]	Byte	1394.0	16#0	True	True	False	False		
Meldepuffer[68]	Byte	1395.0	16#0	True	True	False	False		
Meldepuffer[69]	Byte	1396.0	16#0	True	True	False	False		
Meldepuffer[70]	Byte	1397.0	16#0	True	True	False	False		
Meldepuffer[71]	Byte	1398.0	16#0	True	True	False	False		
Meldepuffer[72]	Byte	1399.0	16#0	True	True	False	False		
Meldepuffer[73]	Byte	1400.0	16#0	True	True	False	False		
Meldepuffer[74]	Byte	1401.0	16#0	True	True	False	False		
Meldepuffer[75]	Byte	1402.0	16#0	True	True	False	False		
Meldepuffer[76]	Byte	1403.0	16#0	True	True	False	False		
Meldepuffer[77]	Byte	1404.0	16#0	True	True	False	False		
Meldepuffer[78]	Byte	1405.0	16#0	True	True	False	False		
Meldepuffer[79]	Byte	1406.0	16#0	True	True	False	False		
Meldepuffer[80]	Byte	1407.0	16#0	True	True	False	False		
Meldepuffer[81]	Byte	1408.0	16#0	True	True	False	False		
Meldepuffer[82]	Byte	1409.0	16#0	True	True	False	False		
Meldepuffer[83]	Byte	1410.0	16#0	True	True	False	False		
Meldepuffer[84]	Byte	1411.0	16#0	True	True	False	False		
Meldepuffer[85]	Byte	1412.0	16#0	True	True	False	False		
Meldepuffer[86]	Byte	1413.0	16#0	True	True	False	False		
Meldepuffer[87]	Byte	1414.0	16#0	True	True	False	False		
Meldepuffer[88]	Byte	1415.0	16#0	True	True	False	False		
Meldepuffer[89]	Byte	1416.0	16#0	True	True	False	False		
Meldepuffer[90]	Byte	1417.0	16#0	True	True	False	False		
Meldepuffer[91]	Byte	1418.0	16#0	True	True	False	False		
Meldepuffer[92]	Byte	1419.0	16#0	True	True	False	False		
Meldepuffer[93]	Byte	1420.0	16#0	True	True	False	False		
Meldepuffer[94]	Byte	1421.0	16#0	True	True	False	False		
Meldepuffer[95]	Byte	1422.0	16#0	True	True	False	False		
Meldepuffer[96]	Byte	1423.0	16#0	True	True	False	False		
Meldepuffer[97]	Byte	1424.0	16#0	True	True	False	False		
Meldepuffer[98]	Byte	1425.0	16#0	True	True	False	False		
Meldepuffer[99]	Byte	1426.0	16#0	True	True	False	False		
Meldepuffer[100]	Byte	1427.0	16#0	True	True	False	False		
Meldepuffer[101]	Byte	1428.0	16#0	True	True	False	False		
Meldepuffer[102]	Byte	1429.0	16#0	True	True	False	False		
Meldepuffer[103]	Byte	1430.0	16#0	True	True	False	False		
Meldepuffer[104]	Byte	1431.0	16#0	True	True	False	False		
Meldepuffer[105]	Byte	1432.0	16#0	True	True	False	False		
Meldepuffer[106]	Byte	1433.0	16#0	True	True	False	False		
Meldepuffer[107]	Byte	1434.0	16#0	True	True	False	False		
Meldepuffer[108]	Byte	1435.0	16#0	True	True	False	False		
Meldepuffer[109]	Byte	1436.0	16#0	True	True	False	False		
Meldepuffer[110]	Byte	1437.0	16#0	True	True	False	False		
Meldepuffer[111]	Byte	1438.0	16#0	True	True	False	False		
Meldepuffer[112]	Byte	1439.0	16#0	True	True	False	False		
Meldepuffer[113]	Byte	1440.0	16#0	True	True	False	False		
Meldepuffer[114]	Byte	1441.0	16#0	True	True	False	False		
Meldepuffer[115]	Byte	1442.0	16#0	True	True	False	False		
Meldepuffer[116]	Byte	1443.0	16#0	True	True	False	False		
Meldepuffer[117]	Byte	1444.0	16#0	True	True	False	False		
Meldepuffer[118]	Byte	1445.0	16#0	True	True	False	False		
Meldepuffer[119]	Byte	1446.0	16#0	True	True	False	False		
Meldepuffer[120]	Byte	1447.0	16#0	True	True	False	False		
Meldepuffer[121]	Byte	1448.0	16#0	True	True	False	False		
Meldepuffer[122]	Byte	1449.0	16#0	True	True	False	False		
Meldepuffer[123]	Byte	1450.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[124]	Byte	1451.0	16#0	True	True	False	False		
Meldepuffer[125]	Byte	1452.0	16#0	True	True	False	False		
Meldepuffer[126]	Byte	1453.0	16#0	True	True	False	False		
Meldepuffer[127]	Byte	1454.0	16#0	True	True	False	False		
Meldepuffer[128]	Byte	1455.0	16#0	True	True	False	False		
Meldepuffer[129]	Byte	1456.0	16#0	True	True	False	False		
Meldepuffer[130]	Byte	1457.0	16#0	True	True	False	False		
Meldepuffer[131]	Byte	1458.0	16#0	True	True	False	False		
Meldepuffer[132]	Byte	1459.0	16#0	True	True	False	False		
Meldepuffer[133]	Byte	1460.0	16#0	True	True	False	False		
Meldepuffer[134]	Byte	1461.0	16#0	True	True	False	False		
Meldepuffer[135]	Byte	1462.0	16#0	True	True	False	False		
Meldepuffer[136]	Byte	1463.0	16#0	True	True	False	False		
Meldepuffer[137]	Byte	1464.0	16#0	True	True	False	False		
Meldepuffer[138]	Byte	1465.0	16#0	True	True	False	False		
Meldepuffer[139]	Byte	1466.0	16#0	True	True	False	False		
Meldepuffer[140]	Byte	1467.0	16#0	True	True	False	False		
Meldepuffer[141]	Byte	1468.0	16#0	True	True	False	False		
Meldepuffer[142]	Byte	1469.0	16#0	True	True	False	False		
Meldepuffer[143]	Byte	1470.0	16#0	True	True	False	False		
Meldepuffer[144]	Byte	1471.0	16#0	True	True	False	False		
Meldepuffer[145]	Byte	1472.0	16#0	True	True	False	False		
Meldepuffer[146]	Byte	1473.0	16#0	True	True	False	False		
Meldepuffer[147]	Byte	1474.0	16#0	True	True	False	False		
Meldepuffer[148]	Byte	1475.0	16#0	True	True	False	False		
Meldepuffer[149]	Byte	1476.0	16#0	True	True	False	False		
Meldepuffer[150]	Byte	1477.0	16#0	True	True	False	False		
Meldepuffer[151]	Byte	1478.0	16#0	True	True	False	False		
Meldepuffer[152]	Byte	1479.0	16#0	True	True	False	False		
Meldepuffer[153]	Byte	1480.0	16#0	True	True	False	False		
Meldepuffer[154]	Byte	1481.0	16#0	True	True	False	False		
Meldepuffer[155]	Byte	1482.0	16#0	True	True	False	False		
Meldepuffer[156]	Byte	1483.0	16#0	True	True	False	False		
Meldepuffer[157]	Byte	1484.0	16#0	True	True	False	False		
Meldepuffer[158]	Byte	1485.0	16#0	True	True	False	False		
Meldepuffer[159]	Byte	1486.0	16#0	True	True	False	False		
Meldepuffer[160]	Byte	1487.0	16#0	True	True	False	False		
Meldepuffer[161]	Byte	1488.0	16#0	True	True	False	False		
Meldepuffer[162]	Byte	1489.0	16#0	True	True	False	False		
Meldepuffer[163]	Byte	1490.0	16#0	True	True	False	False		
Meldepuffer[164]	Byte	1491.0	16#0	True	True	False	False		
Meldepuffer[165]	Byte	1492.0	16#0	True	True	False	False		
Meldepuffer[166]	Byte	1493.0	16#0	True	True	False	False		
Meldepuffer[167]	Byte	1494.0	16#0	True	True	False	False		
Meldepuffer[168]	Byte	1495.0	16#0	True	True	False	False		
Meldepuffer[169]	Byte	1496.0	16#0	True	True	False	False		
Meldepuffer[170]	Byte	1497.0	16#0	True	True	False	False		
Meldepuffer[171]	Byte	1498.0	16#0	True	True	False	False		
Meldepuffer[172]	Byte	1499.0	16#0	True	True	False	False		
Meldepuffer[173]	Byte	1500.0	16#0	True	True	False	False		
Meldepuffer[174]	Byte	1501.0	16#0	True	True	False	False		
Meldepuffer[175]	Byte	1502.0	16#0	True	True	False	False		
Meldepuffer[176]	Byte	1503.0	16#0	True	True	False	False		
Meldepuffer[177]	Byte	1504.0	16#0	True	True	False	False		
Meldepuffer[178]	Byte	1505.0	16#0	True	True	False	False		
Meldepuffer[179]	Byte	1506.0	16#0	True	True	False	False		
Meldepuffer[180]	Byte	1507.0	16#0	True	True	False	False		
Meldepuffer[181]	Byte	1508.0	16#0	True	True	False	False		
Meldepuffer[182]	Byte	1509.0	16#0	True	True	False	False		
Meldepuffer[183]	Byte	1510.0	16#0	True	True	False	False		
Meldepuffer[184]	Byte	1511.0	16#0	True	True	False	False		
Meldepuffer[185]	Byte	1512.0	16#0	True	True	False	False		
Meldepuffer[186]	Byte	1513.0	16#0	True	True	False	False		
Meldepuffer[187]	Byte	1514.0	16#0	True	True	False	False		
Meldepuffer[188]	Byte	1515.0	16#0	True	True	False	False		
Meldepuffer[189]	Byte	1516.0	16#0	True	True	False	False		
Meldepuffer[190]	Byte	1517.0	16#0	True	True	False	False		
Meldepuffer[191]	Byte	1518.0	16#0	True	True	False	False		
Meldepuffer[192]	Byte	1519.0	16#0	True	True	False	False		
Meldepuffer[193]	Byte	1520.0	16#0	True	True	False	False		
Meldepuffer[194]	Byte	1521.0	16#0	True	True	False	False		
Meldepuffer[195]	Byte	1522.0	16#0	True	True	False	False		
Meldepuffer[196]	Byte	1523.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[197]	Byte	1524.0	16#0	True	True	False	False		
Meldepuffer[198]	Byte	1525.0	16#0	True	True	False	False		
Meldepuffer[199]	Byte	1526.0	16#0	True	True	False	False		
Meldepuffer[200]	Byte	1527.0	16#0	True	True	False	False		
Meldepuffer[201]	Byte	1528.0	16#0	True	True	False	False		
Meldepuffer[202]	Byte	1529.0	16#0	True	True	False	False		
Meldepuffer[203]	Byte	1530.0	16#0	True	True	False	False		
Meldepuffer[204]	Byte	1531.0	16#0	True	True	False	False		
Meldepuffer[205]	Byte	1532.0	16#0	True	True	False	False		
Meldepuffer[206]	Byte	1533.0	16#0	True	True	False	False		
Meldepuffer[207]	Byte	1534.0	16#0	True	True	False	False		
Meldepuffer[208]	Byte	1535.0	16#0	True	True	False	False		
Meldepuffer[209]	Byte	1536.0	16#0	True	True	False	False		
Meldepuffer[210]	Byte	1537.0	16#0	True	True	False	False		
Meldepuffer[211]	Byte	1538.0	16#0	True	True	False	False		
Meldepuffer[212]	Byte	1539.0	16#0	True	True	False	False		
Meldepuffer[213]	Byte	1540.0	16#0	True	True	False	False		
Meldepuffer[214]	Byte	1541.0	16#0	True	True	False	False		
Meldepuffer[215]	Byte	1542.0	16#0	True	True	False	False		
Meldepuffer[216]	Byte	1543.0	16#0	True	True	False	False		
Meldepuffer[217]	Byte	1544.0	16#0	True	True	False	False		
Meldepuffer[218]	Byte	1545.0	16#0	True	True	False	False		
Meldepuffer[219]	Byte	1546.0	16#0	True	True	False	False		
Meldepuffer[220]	Byte	1547.0	16#0	True	True	False	False		
Meldepuffer[221]	Byte	1548.0	16#0	True	True	False	False		
Meldepuffer[222]	Byte	1549.0	16#0	True	True	False	False		
Meldepuffer[223]	Byte	1550.0	16#0	True	True	False	False		
Meldepuffer[224]	Byte	1551.0	16#0	True	True	False	False		
Meldepuffer[225]	Byte	1552.0	16#0	True	True	False	False		
Meldepuffer[226]	Byte	1553.0	16#0	True	True	False	False		
Meldepuffer[227]	Byte	1554.0	16#0	True	True	False	False		
Meldepuffer[228]	Byte	1555.0	16#0	True	True	False	False		
Meldepuffer[229]	Byte	1556.0	16#0	True	True	False	False		
Meldepuffer[230]	Byte	1557.0	16#0	True	True	False	False		
Meldepuffer[231]	Byte	1558.0	16#0	True	True	False	False		
Meldepuffer[232]	Byte	1559.0	16#0	True	True	False	False		
Meldepuffer[233]	Byte	1560.0	16#0	True	True	False	False		
Meldepuffer[234]	Byte	1561.0	16#0	True	True	False	False		
Meldepuffer[235]	Byte	1562.0	16#0	True	True	False	False		
Meldepuffer[236]	Byte	1563.0	16#0	True	True	False	False		
Meldepuffer[237]	Byte	1564.0	16#0	True	True	False	False		
Meldepuffer[238]	Byte	1565.0	16#0	True	True	False	False		
Meldepuffer[239]	Byte	1566.0	16#0	True	True	False	False		
Meldepuffer[240]	Byte	1567.0	16#0	True	True	False	False		
Meldepuffer[241]	Byte	1568.0	16#0	True	True	False	False		
Meldepuffer[242]	Byte	1569.0	16#0	True	True	False	False		
Meldepuffer[243]	Byte	1570.0	16#0	True	True	False	False		
Meldepuffer[244]	Byte	1571.0	16#0	True	True	False	False		
Meldepuffer[245]	Byte	1572.0	16#0	True	True	False	False		
Meldepuffer[246]	Byte	1573.0	16#0	True	True	False	False		
Meldepuffer[247]	Byte	1574.0	16#0	True	True	False	False		
Meldepuffer[248]	Byte	1575.0	16#0	True	True	False	False		
Meldepuffer[249]	Byte	1576.0	16#0	True	True	False	False		
Meldepuffer[250]	Byte	1577.0	16#0	True	True	False	False		
Meldepuffer[251]	Byte	1578.0	16#0	True	True	False	False		
Meldepuffer[252]	Byte	1579.0	16#0	True	True	False	False		
Meldepuffer[253]	Byte	1580.0	16#0	True	True	False	False		
Meldepuffer[254]	Byte	1581.0	16#0	True	True	False	False		
Meldepuffer[255]	Byte	1582.0	16#0	True	True	False	False		
Meldepuffer[256]	Byte	1583.0	16#0	True	True	False	False		
Meldepuffer[257]	Byte	1584.0	16#0	True	True	False	False		
Meldepuffer[258]	Byte	1585.0	16#0	True	True	False	False		
Meldepuffer[259]	Byte	1586.0	16#0	True	True	False	False		
Meldepuffer[260]	Byte	1587.0	16#0	True	True	False	False		
Meldepuffer[261]	Byte	1588.0	16#0	True	True	False	False		
Meldepuffer[262]	Byte	1589.0	16#0	True	True	False	False		
Meldepuffer[263]	Byte	1590.0	16#0	True	True	False	False		
Meldepuffer[264]	Byte	1591.0	16#0	True	True	False	False		
Meldepuffer[265]	Byte	1592.0	16#0	True	True	False	False		
Meldepuffer[266]	Byte	1593.0	16#0	True	True	False	False		
Meldepuffer[267]	Byte	1594.0	16#0	True	True	False	False		
Meldepuffer[268]	Byte	1595.0	16#0	True	True	False	False		
Meldepuffer[269]	Byte	1596.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[270]	Byte	1597.0	16#0	True	True	False	False		
Meldepuffer[271]	Byte	1598.0	16#0	True	True	False	False		
Meldepuffer[272]	Byte	1599.0	16#0	True	True	False	False		
Meldepuffer[273]	Byte	1600.0	16#0	True	True	False	False		
Meldepuffer[274]	Byte	1601.0	16#0	True	True	False	False		
Meldepuffer[275]	Byte	1602.0	16#0	True	True	False	False		
Meldepuffer[276]	Byte	1603.0	16#0	True	True	False	False		
Meldepuffer[277]	Byte	1604.0	16#0	True	True	False	False		
Meldepuffer[278]	Byte	1605.0	16#0	True	True	False	False		
Meldepuffer[279]	Byte	1606.0	16#0	True	True	False	False		
Meldepuffer[280]	Byte	1607.0	16#0	True	True	False	False		
Meldepuffer[281]	Byte	1608.0	16#0	True	True	False	False		
Meldepuffer[282]	Byte	1609.0	16#0	True	True	False	False		
Meldepuffer[283]	Byte	1610.0	16#0	True	True	False	False		
Meldepuffer[284]	Byte	1611.0	16#0	True	True	False	False		
Meldepuffer[285]	Byte	1612.0	16#0	True	True	False	False		
Meldepuffer[286]	Byte	1613.0	16#0	True	True	False	False		
Meldepuffer[287]	Byte	1614.0	16#0	True	True	False	False		
Meldepuffer[288]	Byte	1615.0	16#0	True	True	False	False		
Meldepuffer[289]	Byte	1616.0	16#0	True	True	False	False		
Meldepuffer[290]	Byte	1617.0	16#0	True	True	False	False		
Meldepuffer[291]	Byte	1618.0	16#0	True	True	False	False		
Meldepuffer[292]	Byte	1619.0	16#0	True	True	False	False		
Meldepuffer[293]	Byte	1620.0	16#0	True	True	False	False		
Meldepuffer[294]	Byte	1621.0	16#0	True	True	False	False		
Meldepuffer[295]	Byte	1622.0	16#0	True	True	False	False		
Meldepuffer[296]	Byte	1623.0	16#0	True	True	False	False		
Meldepuffer[297]	Byte	1624.0	16#0	True	True	False	False		
Meldepuffer[298]	Byte	1625.0	16#0	True	True	False	False		
Meldepuffer[299]	Byte	1626.0	16#0	True	True	False	False		
Meldepuffer[300]	Byte	1627.0	16#0	True	True	False	False		
Meldepuffer[301]	Byte	1628.0	16#0	True	True	False	False		
Meldepuffer[302]	Byte	1629.0	16#0	True	True	False	False		
Meldepuffer[303]	Byte	1630.0	16#0	True	True	False	False		
Meldepuffer[304]	Byte	1631.0	16#0	True	True	False	False		
Meldepuffer[305]	Byte	1632.0	16#0	True	True	False	False		
Meldepuffer[306]	Byte	1633.0	16#0	True	True	False	False		
Meldepuffer[307]	Byte	1634.0	16#0	True	True	False	False		
Meldepuffer[308]	Byte	1635.0	16#0	True	True	False	False		
Meldepuffer[309]	Byte	1636.0	16#0	True	True	False	False		
Meldepuffer[310]	Byte	1637.0	16#0	True	True	False	False		
Meldepuffer[311]	Byte	1638.0	16#0	True	True	False	False		
Meldepuffer[312]	Byte	1639.0	16#0	True	True	False	False		
Meldepuffer[313]	Byte	1640.0	16#0	True	True	False	False		
Meldepuffer[314]	Byte	1641.0	16#0	True	True	False	False		
Meldepuffer[315]	Byte	1642.0	16#0	True	True	False	False		
Meldepuffer[316]	Byte	1643.0	16#0	True	True	False	False		
Meldepuffer[317]	Byte	1644.0	16#0	True	True	False	False		
Meldepuffer[318]	Byte	1645.0	16#0	True	True	False	False		
Meldepuffer[319]	Byte	1646.0	16#0	True	True	False	False		
Meldepuffer[320]	Byte	1647.0	16#0	True	True	False	False		
Meldepuffer[321]	Byte	1648.0	16#0	True	True	False	False		
Meldepuffer[322]	Byte	1649.0	16#0	True	True	False	False		
Meldepuffer[323]	Byte	1650.0	16#0	True	True	False	False		
Meldepuffer[324]	Byte	1651.0	16#0	True	True	False	False		
Meldepuffer[325]	Byte	1652.0	16#0	True	True	False	False		
Meldepuffer[326]	Byte	1653.0	16#0	True	True	False	False		
Meldepuffer[327]	Byte	1654.0	16#0	True	True	False	False		
Meldepuffer[328]	Byte	1655.0	16#0	True	True	False	False		
Meldepuffer[329]	Byte	1656.0	16#0	True	True	False	False		
Meldepuffer[330]	Byte	1657.0	16#0	True	True	False	False		
Meldepuffer[331]	Byte	1658.0	16#0	True	True	False	False		
Meldepuffer[332]	Byte	1659.0	16#0	True	True	False	False		
Meldepuffer[333]	Byte	1660.0	16#0	True	True	False	False		
Meldepuffer[334]	Byte	1661.0	16#0	True	True	False	False		
Meldepuffer[335]	Byte	1662.0	16#0	True	True	False	False		
Meldepuffer[336]	Byte	1663.0	16#0	True	True	False	False		
Meldepuffer[337]	Byte	1664.0	16#0	True	True	False	False		
Meldepuffer[338]	Byte	1665.0	16#0	True	True	False	False		
Meldepuffer[339]	Byte	1666.0	16#0	True	True	False	False		
Meldepuffer[340]	Byte	1667.0	16#0	True	True	False	False		
Meldepuffer[341]	Byte	1668.0	16#0	True	True	False	False		
Meldepuffer[342]	Byte	1669.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[343]	Byte	1670.0	16#0	True	True	False	False		
Meldepuffer[344]	Byte	1671.0	16#0	True	True	False	False		
Meldepuffer[345]	Byte	1672.0	16#0	True	True	False	False		
Meldepuffer[346]	Byte	1673.0	16#0	True	True	False	False		
Meldepuffer[347]	Byte	1674.0	16#0	True	True	False	False		
Meldepuffer[348]	Byte	1675.0	16#0	True	True	False	False		
Meldepuffer[349]	Byte	1676.0	16#0	True	True	False	False		
Meldepuffer[350]	Byte	1677.0	16#0	True	True	False	False		
Meldepuffer[351]	Byte	1678.0	16#0	True	True	False	False		
Meldepuffer[352]	Byte	1679.0	16#0	True	True	False	False		
Meldepuffer[353]	Byte	1680.0	16#0	True	True	False	False		
Meldepuffer[354]	Byte	1681.0	16#0	True	True	False	False		
Meldepuffer[355]	Byte	1682.0	16#0	True	True	False	False		
Meldepuffer[356]	Byte	1683.0	16#0	True	True	False	False		
Meldepuffer[357]	Byte	1684.0	16#0	True	True	False	False		
Meldepuffer[358]	Byte	1685.0	16#0	True	True	False	False		
Meldepuffer[359]	Byte	1686.0	16#0	True	True	False	False		
Meldepuffer[360]	Byte	1687.0	16#0	True	True	False	False		
Meldepuffer[361]	Byte	1688.0	16#0	True	True	False	False		
Meldepuffer[362]	Byte	1689.0	16#0	True	True	False	False		
Meldepuffer[363]	Byte	1690.0	16#0	True	True	False	False		
Meldepuffer[364]	Byte	1691.0	16#0	True	True	False	False		
Meldepuffer[365]	Byte	1692.0	16#0	True	True	False	False		
Meldepuffer[366]	Byte	1693.0	16#0	True	True	False	False		
Meldepuffer[367]	Byte	1694.0	16#0	True	True	False	False		
Meldepuffer[368]	Byte	1695.0	16#0	True	True	False	False		
Meldepuffer[369]	Byte	1696.0	16#0	True	True	False	False		
Meldepuffer[370]	Byte	1697.0	16#0	True	True	False	False		
Meldepuffer[371]	Byte	1698.0	16#0	True	True	False	False		
Meldepuffer[372]	Byte	1699.0	16#0	True	True	False	False		
Meldepuffer[373]	Byte	1700.0	16#0	True	True	False	False		
Meldepuffer[374]	Byte	1701.0	16#0	True	True	False	False		
Meldepuffer[375]	Byte	1702.0	16#0	True	True	False	False		
Meldepuffer[376]	Byte	1703.0	16#0	True	True	False	False		
Meldepuffer[377]	Byte	1704.0	16#0	True	True	False	False		
Meldepuffer[378]	Byte	1705.0	16#0	True	True	False	False		
Meldepuffer[379]	Byte	1706.0	16#0	True	True	False	False		
Meldepuffer[380]	Byte	1707.0	16#0	True	True	False	False		
Meldepuffer[381]	Byte	1708.0	16#0	True	True	False	False		
Meldepuffer[382]	Byte	1709.0	16#0	True	True	False	False		
Meldepuffer[383]	Byte	1710.0	16#0	True	True	False	False		
Meldepuffer[384]	Byte	1711.0	16#0	True	True	False	False		
Meldepuffer[385]	Byte	1712.0	16#0	True	True	False	False		
Meldepuffer[386]	Byte	1713.0	16#0	True	True	False	False		
Meldepuffer[387]	Byte	1714.0	16#0	True	True	False	False		
Meldepuffer[388]	Byte	1715.0	16#0	True	True	False	False		
Meldepuffer[389]	Byte	1716.0	16#0	True	True	False	False		
Meldepuffer[390]	Byte	1717.0	16#0	True	True	False	False		
Meldepuffer[391]	Byte	1718.0	16#0	True	True	False	False		
Meldepuffer[392]	Byte	1719.0	16#0	True	True	False	False		
Meldepuffer[393]	Byte	1720.0	16#0	True	True	False	False		
Meldepuffer[394]	Byte	1721.0	16#0	True	True	False	False		
Meldepuffer[395]	Byte	1722.0	16#0	True	True	False	False		
Meldepuffer[396]	Byte	1723.0	16#0	True	True	False	False		
Meldepuffer[397]	Byte	1724.0	16#0	True	True	False	False		
Meldepuffer[398]	Byte	1725.0	16#0	True	True	False	False		
Meldepuffer[399]	Byte	1726.0	16#0	True	True	False	False		
Meldepuffer[400]	Byte	1727.0	16#0	True	True	False	False		
Meldepuffer[401]	Byte	1728.0	16#0	True	True	False	False		
Meldepuffer[402]	Byte	1729.0	16#0	True	True	False	False		
Meldepuffer[403]	Byte	1730.0	16#0	True	True	False	False		
Meldepuffer[404]	Byte	1731.0	16#0	True	True	False	False		
Meldepuffer[405]	Byte	1732.0	16#0	True	True	False	False		
Meldepuffer[406]	Byte	1733.0	16#0	True	True	False	False		
Meldepuffer[407]	Byte	1734.0	16#0	True	True	False	False		
Meldepuffer[408]	Byte	1735.0	16#0	True	True	False	False		
Meldepuffer[409]	Byte	1736.0	16#0	True	True	False	False		
Meldepuffer[410]	Byte	1737.0	16#0	True	True	False	False		
Meldepuffer[411]	Byte	1738.0	16#0	True	True	False	False		
Meldepuffer[412]	Byte	1739.0	16#0	True	True	False	False		
Meldepuffer[413]	Byte	1740.0	16#0	True	True	False	False		
Meldepuffer[414]	Byte	1741.0	16#0	True	True	False	False		
Meldepuffer[415]	Byte	1742.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[416]	Byte	1743.0	16#0	True	True	False	False		
Meldepuffer[417]	Byte	1744.0	16#0	True	True	False	False		
Meldepuffer[418]	Byte	1745.0	16#0	True	True	False	False		
Meldepuffer[419]	Byte	1746.0	16#0	True	True	False	False		
Meldepuffer[420]	Byte	1747.0	16#0	True	True	False	False		
Meldepuffer[421]	Byte	1748.0	16#0	True	True	False	False		
Meldepuffer[422]	Byte	1749.0	16#0	True	True	False	False		
Meldepuffer[423]	Byte	1750.0	16#0	True	True	False	False		
Meldepuffer[424]	Byte	1751.0	16#0	True	True	False	False		
Meldepuffer[425]	Byte	1752.0	16#0	True	True	False	False		
Meldepuffer[426]	Byte	1753.0	16#0	True	True	False	False		
Meldepuffer[427]	Byte	1754.0	16#0	True	True	False	False		
Meldepuffer[428]	Byte	1755.0	16#0	True	True	False	False		
Meldepuffer[429]	Byte	1756.0	16#0	True	True	False	False		
Meldepuffer[430]	Byte	1757.0	16#0	True	True	False	False		
Meldepuffer[431]	Byte	1758.0	16#0	True	True	False	False		
Meldepuffer[432]	Byte	1759.0	16#0	True	True	False	False		
Meldepuffer[433]	Byte	1760.0	16#0	True	True	False	False		
Meldepuffer[434]	Byte	1761.0	16#0	True	True	False	False		
Meldepuffer[435]	Byte	1762.0	16#0	True	True	False	False		
Meldepuffer[436]	Byte	1763.0	16#0	True	True	False	False		
Meldepuffer[437]	Byte	1764.0	16#0	True	True	False	False		
Meldepuffer[438]	Byte	1765.0	16#0	True	True	False	False		
Meldepuffer[439]	Byte	1766.0	16#0	True	True	False	False		
Meldepuffer[440]	Byte	1767.0	16#0	True	True	False	False		
Meldepuffer[441]	Byte	1768.0	16#0	True	True	False	False		
Meldepuffer[442]	Byte	1769.0	16#0	True	True	False	False		
Meldepuffer[443]	Byte	1770.0	16#0	True	True	False	False		
Meldepuffer[444]	Byte	1771.0	16#0	True	True	False	False		
Meldepuffer[445]	Byte	1772.0	16#0	True	True	False	False		
Meldepuffer[446]	Byte	1773.0	16#0	True	True	False	False		
Meldepuffer[447]	Byte	1774.0	16#0	True	True	False	False		
Meldepuffer[448]	Byte	1775.0	16#0	True	True	False	False		
Meldepuffer[449]	Byte	1776.0	16#0	True	True	False	False		
Meldepuffer[450]	Byte	1777.0	16#0	True	True	False	False		
Meldepuffer[451]	Byte	1778.0	16#0	True	True	False	False		
Meldepuffer[452]	Byte	1779.0	16#0	True	True	False	False		
Meldepuffer[453]	Byte	1780.0	16#0	True	True	False	False		
Meldepuffer[454]	Byte	1781.0	16#0	True	True	False	False		
Meldepuffer[455]	Byte	1782.0	16#0	True	True	False	False		
Meldepuffer[456]	Byte	1783.0	16#0	True	True	False	False		
Meldepuffer[457]	Byte	1784.0	16#0	True	True	False	False		
Meldepuffer[458]	Byte	1785.0	16#0	True	True	False	False		
Meldepuffer[459]	Byte	1786.0	16#0	True	True	False	False		
Meldepuffer[460]	Byte	1787.0	16#0	True	True	False	False		
Meldepuffer[461]	Byte	1788.0	16#0	True	True	False	False		
Meldepuffer[462]	Byte	1789.0	16#0	True	True	False	False		
Meldepuffer[463]	Byte	1790.0	16#0	True	True	False	False		
Meldepuffer[464]	Byte	1791.0	16#0	True	True	False	False		
Meldepuffer[465]	Byte	1792.0	16#0	True	True	False	False		
Meldepuffer[466]	Byte	1793.0	16#0	True	True	False	False		
Meldepuffer[467]	Byte	1794.0	16#0	True	True	False	False		
Meldepuffer[468]	Byte	1795.0	16#0	True	True	False	False		
Meldepuffer[469]	Byte	1796.0	16#0	True	True	False	False		
Meldepuffer[470]	Byte	1797.0	16#0	True	True	False	False		
Meldepuffer[471]	Byte	1798.0	16#0	True	True	False	False		
Meldepuffer[472]	Byte	1799.0	16#0	True	True	False	False		
Meldepuffer[473]	Byte	1800.0	16#0	True	True	False	False		
Meldepuffer[474]	Byte	1801.0	16#0	True	True	False	False		
Meldepuffer[475]	Byte	1802.0	16#0	True	True	False	False		
Meldepuffer[476]	Byte	1803.0	16#0	True	True	False	False		
Meldepuffer[477]	Byte	1804.0	16#0	True	True	False	False		
Meldepuffer[478]	Byte	1805.0	16#0	True	True	False	False		
Meldepuffer[479]	Byte	1806.0	16#0	True	True	False	False		
Meldepuffer[480]	Byte	1807.0	16#0	True	True	False	False		
Meldepuffer[481]	Byte	1808.0	16#0	True	True	False	False		
Meldepuffer[482]	Byte	1809.0	16#0	True	True	False	False		
Meldepuffer[483]	Byte	1810.0	16#0	True	True	False	False		
Meldepuffer[484]	Byte	1811.0	16#0	True	True	False	False		
Meldepuffer[485]	Byte	1812.0	16#0	True	True	False	False		
Meldepuffer[486]	Byte	1813.0	16#0	True	True	False	False		
Meldepuffer[487]	Byte	1814.0	16#0	True	True	False	False		
Meldepuffer[488]	Byte	1815.0	16#0	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
Meldepuffer[489]	Byte	1816.0	16#0	True	True	False	False		
Meldepuffer[490]	Byte	1817.0	16#0	True	True	False	False		
Meldepuffer[491]	Byte	1818.0	16#0	True	True	False	False		
Meldepuffer[492]	Byte	1819.0	16#0	True	True	False	False		
Meldepuffer[493]	Byte	1820.0	16#0	True	True	False	False		
Meldepuffer[494]	Byte	1821.0	16#0	True	True	False	False		
Meldepuffer[495]	Byte	1822.0	16#0	True	True	False	False		
Meldepuffer[496]	Byte	1823.0	16#0	True	True	False	False		
Meldepuffer[497]	Byte	1824.0	16#0	True	True	False	False		
Meldepuffer[498]	Byte	1825.0	16#0	True	True	False	False		
Meldepuffer[499]	Byte	1826.0	16#0	True	True	False	False		
Meldepuffer[500]	Byte	1827.0	16#0	True	True	False	False		
Meldepuffer[501]	Byte	1828.0	16#0	True	True	False	False		
Meldepuffer[502]	Byte	1829.0	16#0	True	True	False	False		
Meldepuffer[503]	Byte	1830.0	16#0	True	True	False	False		
Meldepuffer[504]	Byte	1831.0	16#0	True	True	False	False		
Meldepuffer[505]	Byte	1832.0	16#0	True	True	False	False		
Meldepuffer[506]	Byte	1833.0	16#0	True	True	False	False		
Meldepuffer[507]	Byte	1834.0	16#0	True	True	False	False		
Meldepuffer[508]	Byte	1835.0	16#0	True	True	False	False		
Meldepuffer[509]	Byte	1836.0	16#0	True	True	False	False		
Meldepuffer[510]	Byte	1837.0	16#0	True	True	False	False		
Meldung_01	Word	1838.0	WORD#16#0000	True	True	False	False		
MeldungBandBreite_01	Byte	1840.0	BYTE#16#0A	True	True	False	False		
MeldungNaechsterBand_01	Byte	1841.0	16#0	True	True	False	False		
Meldung_02	Word	1842.0	WORD#16#1B5C	True	True	False	False		
MeldungBandBreite_02	Byte	1844.0	BYTE#16#0A	True	True	False	False		
MeldungNaechsterBand_02	Byte	1845.0	16#0	True	True	False	False		
Meldung_03	Word	1846.0	WORD#16#1B5B	True	True	False	False		
MeldungBandBreite_03	Byte	1848.0	BYTE#16#14	True	True	False	False		
MeldungNaechsterBand_03	Byte	1849.0	16#0	True	True	False	False		
Meldung_04	Word	1850.0	WORD#16#1C70	True	True	False	False		
MeldungBandBreite_04	Byte	1852.0	BYTE#16#18	True	True	False	False		
MeldungNaechsterBand_04	Byte	1853.0	16#0	True	True	False	False		
Meldung_05	Word	1854.0	WORD#16#1C73	True	True	False	False		
MeldungBandBreite_05	Byte	1856.0	BYTE#16#18	True	True	False	False		
MeldungNaechsterBand_05	Byte	1857.0	16#0	True	True	False	False		
Meldung_06	Word	1858.0	WORD#16#1C6D	True	True	False	False		
MeldungBandBreite_06	Byte	1860.0	BYTE#16#10	True	True	False	False		
MeldungNaechsterBand_06	Byte	1861.0	16#0	True	True	False	False		
Meldung_07	Word	1862.0	WORD#16#1BCE	True	True	False	False		
MeldungBandBreite_07	Byte	1864.0	BYTE#16#10	True	True	False	False		
MeldungNaechsterBand_07	Byte	1865.0	16#0	True	True	False	False		
Meldung_08	Word	1866.0	WORD#16#1B59	True	True	False	False		
MeldungBandBreite_08	Byte	1868.0	BYTE#16#10	True	True	False	False		
MeldungNaechsterBand_08	Byte	1869.0	16#0	True	True	False	False		
wMeldeWerte	Array[1..6] of Word	1870.0		True	True	False	False		
wMeldeWerte[1]	Word	1870.0	16#0	True	True	False	False		
wMeldeWerte[2]	Word	1872.0	16#0	True	True	False	False		
wMeldeWerte[3]	Word	1874.0	16#0	True	True	False	False		
wMeldeWerte[4]	Word	1876.0	16#0	True	True	False	False		
wMeldeWerte[5]	Word	1878.0	16#0	True	True	False	False		
wMeldeWerte[6]	Word	1880.0	16#0	True	True	False	False		
xSubOrdinateStateOld	Word	1882.0	WORD#16#FFFF	True	True	False	False		
xASVeraltet	Word	1884.0	WORD#16#FFFF	True	True	False	False		
xAnlaufMilde	Bool	1886.0	TRUE	True	True	False	False		
iAnlaufSchleife_a	Byte	1887.0	16#0	True	True	False	False		
iAnlaufSchleife_b	Byte	1888.0	16#0	True	True	False	False		
pAnlaufMapDaten_a	Byte	1889.0	16#0	True	True	False	False		
pAnlaufMapDaten_b	Byte	1890.0	16#0	True	True	False	False		
pAnlaufMapDaten_c	Byte	1891.0	16#0	True	True	False	False		
pAnlaufMapDaten_d	Byte	1892.0	16#0	True	True	False	False		
iSFB54RestLaenge_a	Byte	1893.0	16#0	True	True	False	False		
iSFB54RestLaenge_b	Byte	1894.0	16#0	True	True	False	False		
iSFB54BlocLaenge_a	Byte	1895.0	16#0	True	True	False	False		
iSFB54BlocLaenge_b	Byte	1896.0	16#0	True	True	False	False		
dAktAINFOPos_a	Byte	1897.0	16#0	True	True	False	False		
dAktAINFOPos_b	Byte	1898.0	16#0	True	True	False	False		
dAktAINFOPos_c	Byte	1899.0	16#0	True	True	False	False		
dAktAINFOPos_d	Byte	1900.0	16#0	True	True	False	False		
bPNKDKanalLaenge	Byte	1901.0	16#0	True	True	False	False		
xSFB54Multiple	Bool	1902.0	false	True	True	False	False		

Name	Data type	Offset	Default value	Accessible from HMI/OPC UA/Web API	Writable from HMI/OPC UA/Web API	Visible in HMI engineering	Setpoint	Supervision	Comment
dDiagSlotSubSlotNummer_a	Byte	1903.0	16#0	True	True	False	False		
dDiagSlotSubSlotNummer_b	Byte	1904.0	16#0	True	True	False	False		
dDiagSlotSubSlotNummer_c	Byte	1905.0	16#0	True	True	False	False		
dDiagSlotSubSlotNummer_d	Byte	1906.0	16#0	True	True	False	False		
wBlockLengtKopf_a	Byte	1907.0	16#0	True	True	False	False		
wBlockLengtKopf_b	Byte	1908.0	16#0	True	True	False	False		
▼ TempDaten	Array[1..58] of Byte	1910.0		True	True	False	False		
TempDaten[1]	Byte	1910.0	16#0	True	True	False	False		
TempDaten[2]	Byte	1911.0	16#0	True	True	False	False		
TempDaten[3]	Byte	1912.0	16#0	True	True	False	False		
TempDaten[4]	Byte	1913.0	16#0	True	True	False	False		
TempDaten[5]	Byte	1914.0	16#0	True	True	False	False		
TempDaten[6]	Byte	1915.0	16#0	True	True	False	False		
TempDaten[7]	Byte	1916.0	16#0	True	True	False	False		
TempDaten[8]	Byte	1917.0	16#0	True	True	False	False		
TempDaten[9]	Byte	1918.0	16#0	True	True	False	False		
TempDaten[10]	Byte	1919.0	16#0	True	True	False	False		
TempDaten[11]	Byte	1920.0	16#0	True	True	False	False		
TempDaten[12]	Byte	1921.0	16#0	True	True	False	False		
TempDaten[13]	Byte	1922.0	16#0	True	True	False	False		
TempDaten[14]	Byte	1923.0	16#0	True	True	False	False		
TempDaten[15]	Byte	1924.0	16#0	True	True	False	False		
TempDaten[16]	Byte	1925.0	16#0	True	True	False	False		
TempDaten[17]	Byte	1926.0	16#0	True	True	False	False		
TempDaten[18]	Byte	1927.0	16#0	True	True	False	False		
TempDaten[19]	Byte	1928.0	16#0	True	True	False	False		
TempDaten[20]	Byte	1929.0	16#0	True	True	False	False		
TempDaten[21]	Byte	1930.0	16#0	True	True	False	False		
TempDaten[22]	Byte	1931.0	16#0	True	True	False	False		
TempDaten[23]	Byte	1932.0	16#0	True	True	False	False		
TempDaten[24]	Byte	1933.0	16#0	True	True	False	False		
TempDaten[25]	Byte	1934.0	16#0	True	True	False	False		
TempDaten[26]	Byte	1935.0	16#0	True	True	False	False		
TempDaten[27]	Byte	1936.0	16#0	True	True	False	False		
TempDaten[28]	Byte	1937.0	16#0	True	True	False	False		
TempDaten[29]	Byte	1938.0	16#0	True	True	False	False		
TempDaten[30]	Byte	1939.0	16#0	True	True	False	False		
TempDaten[31]	Byte	1940.0	16#0	True	True	False	False		
TempDaten[32]	Byte	1941.0	16#0	True	True	False	False		
TempDaten[33]	Byte	1942.0	16#0	True	True	False	False		
TempDaten[34]	Byte	1943.0	16#0	True	True	False	False		
TempDaten[35]	Byte	1944.0	16#0	True	True	False	False		
TempDaten[36]	Byte	1945.0	16#0	True	True	False	False		
TempDaten[37]	Byte	1946.0	16#0	True	True	False	False		
TempDaten[38]	Byte	1947.0	16#0	True	True	False	False		
TempDaten[39]	Byte	1948.0	16#0	True	True	False	False		
TempDaten[40]	Byte	1949.0	16#0	True	True	False	False		
TempDaten[41]	Byte	1950.0	16#0	True	True	False	False		
TempDaten[42]	Byte	1951.0	16#0	True	True	False	False		
TempDaten[43]	Byte	1952.0	16#0	True	True	False	False		
TempDaten[44]	Byte	1953.0	16#0	True	True	False	False		
TempDaten[45]	Byte	1954.0	16#0	True	True	False	False		
TempDaten[46]	Byte	1955.0	16#0	True	True	False	False		
TempDaten[47]	Byte	1956.0	16#0	True	True	False	False		
TempDaten[48]	Byte	1957.0	16#0	True	True	False	False		
TempDaten[49]	Byte	1958.0	16#0	True	True	False	False		
TempDaten[50]	Byte	1959.0	16#0	True	True	False	False		
TempDaten[51]	Byte	1960.0	16#0	True	True	False	False		
TempDaten[52]	Byte	1961.0	16#0	True	True	False	False		
TempDaten[53]	Byte	1962.0	16#0	True	True	False	False		
TempDaten[54]	Byte	1963.0	16#0	True	True	False	False		
TempDaten[55]	Byte	1964.0	16#0	True	True	False	False		
TempDaten[56]	Byte	1965.0	16#0	True	True	False	False		
TempDaten[57]	Byte	1966.0	16#0	True	True	False	False		
TempDaten[58]	Byte	1967.0	16#0	True	True	False	False		

Flow / PLC_1 [CPU 314C-2 PN/DP] / Program blocks / System blocks / System diagnostics

RSE_FC [FC49]

RSE_FC Properties							
General							
Name	RSE_FC	**Number**	49	**Type**	FC	**Language**	RSE
Numbering	Manual						
Information							
Title	Report System Error Diagnostic Block	**Author**	SIMATIC	**Comment**	This FC contains the code generated from STEP 7 Report System Errors.	**Family**	RSEDIAG
Version	1.0	**User-defined ID**	RSE				

Name	Data type	Offset	Default value	Comment
Input				
Output				
InOut				
▼ Return				
RSE_FC	Void			

Flow / PLC_1 [CPU 314C-2 PN/DP]

Technology objects

This folder is empty.

Flow / PLC_1 [CPU 314C-2 PN/DP]

PLC tags

PLC tags						
Icon	**Name**	**Data type**	**Address**	**Visible in HMI engineering**	**Accessible from HMI/OPC UA/Web API**	**Comment**
	AUTO	Bool	%M66.0	True	True	
	CV	Real	%MD3	True	True	
	Lamp	Bool	%Q136.0	True	True	
	MV	Real	%MD7	True	True	
	MV_Man	Bool	%M2.0	True	True	
	Pump_Off	Bool	%M1.0	True	True	
	Pump_On	Bool	%M0.5	True	True	
	R_Pump	Bool	%M0.6	True	True	
	R_Pump_off	Bool	%M0.7	True	True	
	SP	Real	%MD11	True	True	
	Tag_1	Bool	%M0.0	True	True	
	Tag_2	Bool	%M100.0	True	True	
	Tag_3	Bool	%Q0.0	True	True	
	Tag_4	Bool	%M0.1	True	True	
	Tag_5	Int	%IW158	True	True	
	Tag_6	Int	%IW258	True	True	
	Tag_7	Bool	%M1.1	True	True	
	Tag_9	Int	%QW274	True	True	
	Tag_10	Bool	%M0.2	True	True	
	Tag_12	Bool	%M200.0	True	True	
	Tag_13	Bool	%Q0.1	True	True	
	Tag_16	Real	%MD54	True	True	
	Tag_18	Bool	%M0.3	True	True	
	Tag_19	Real	%MD15	True	True	
	Tag_20	Real	%MD50	True	True	
	Tag_21	Bool	%M0.4	True	True	
	Tag_22	Real	%MD60	True	True	
	Tag_23	Real	%MD56	True	True	
	Tag_24	Real	%MD64	True	True	
	V1	Bool	%Q136.2	True	True	
	V3	Bool	%Q136.1	True	True	

Flow / PLC_1 [CPU 314C-2 PN/DP] / PLC tags

Default tag table [31]

PLC tags						
Icon	Name	Data type	Address	Visible in HMI engineering	Accessible from HMI/OPC UA/Web API	Comment
	AUTO	Bool	%M66.0	True	True	
	CV	Real	%MD3	True	True	
	Lamp	Bool	%Q136.0	True	True	
	MV	Real	%MD7	True	True	
	MV_Man	Bool	%M2.0	True	True	
	Pump_Off	Bool	%M1.0	True	True	
	Pump_On	Bool	%M0.5	True	True	
	R_Pump	Bool	%M0.6	True	True	
	R_Pump_off	Bool	%M0.7	True	True	
	SP	Real	%MD11	True	True	
	Tag_1	Bool	%M0.0	True	True	
	Tag_2	Bool	%M100.0	True	True	
	Tag_3	Bool	%Q0.0	True	True	
	Tag_4	Bool	%M0.1	True	True	
	Tag_5	Int	%IW158	True	True	
	Tag_6	Int	%IW258	True	True	
	Tag_7	Bool	%M1.1	True	True	
	Tag_9	Int	%QW274	True	True	
	Tag_10	Bool	%M0.2	True	True	
	Tag_12	Bool	%M200.0	True	True	
	Tag_13	Bool	%Q0.1	True	True	
	Tag_16	Real	%MD54	True	True	
	Tag_18	Bool	%M0.3	True	True	
	Tag_19	Real	%MD15	True	True	
	Tag_20	Real	%MD50	True	True	
	Tag_21	Bool	%M0.4	True	True	
	Tag_22	Real	%MD60	True	True	
	Tag_23	Real	%MD56	True	True	
	Tag_24	Real	%MD64	True	True	
	V1	Bool	%Q136.2	True	True	
	V3	Bool	%Q136.1	True	True	

Flow / PLC_1 [CPU 314C-2 PN/DP]

PLC data types

This folder is empty.

Flow / PLC_1 [CPU 314C-2 PN/DP] / Watch and force tables

Force table

Name	Address	Display format	Force value	Comment

Flow / PLC_1 [CPU 314C-2 PN/DP] / Watch and force tables

Watch table_1

Name	Address	Display format	Modify value	Comment
"SP"	%MD11	Floating-point number	2.0	
"MV"	%MD7	Floating-point number	0.0	
"CV"	%MD3	Floating-point number		
"Data_block_1".kc	%DB1.DBD0	Floating-point number	2.575	
"Data_block_1".taui	%DB1.DBD4	Floating-point number	0.01	
"Data_block_1".taud	%DB1.DBD8	Floating-point number	0.23	
"Data_block_1".deltat	%DB1.DBD32	Floating-point number	0.5	
"Data_block_1".mvk	%DB1.DBD12	Floating-point number		
"Pump_On"	%M0.5	Bool	TRUE	
"AUTO"	%M66.0	Bool	FALSE	

Flow / PLC_1 [CPU 314C-2 PN/DP] / PLC supervisions & alarms

PLC alarms

PLC alarms
No entries

Flow / PLC_1 [CPU 314C-2 PN/DP] / PLC supervisions & alarms

User diagnostics alarms

User diagnostics alarms
No entries

Flow / PLC_1 [CPU 314C-2 PN/DP] / PLC supervisions & alarms

System alarms

System alarms			
Name	SDIAG_ALCAT_CH_MSG_0001	**Type**	Alarm_s
ID	1610619737	**Range**	16
Location	PLC_1	**Alarm text**	Fault: @1W%t#1K@ - @5W%t#1K@ on @2W%1d@ |> Component: @6W%t#276K@ / @6W%t#257K@ / @6W %t#258K@.@6W%t#259K@ |> Path: @6W%t#266K@ / @6W%t#267K@ / @6W%t#268K@.@6W%t#269K@ / @2W %1d@ HW_ID= @6W%t#264K@ @6W%t#262K@ @6W %t#263K@
Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			
Name	SDIAG_ALCAT_MODUL_MSG_0003	**Type**	Alarm_s
ID	1610619739	**Range**	20
Location	PLC_1	**Alarm text**	Fault: @1W%t#1K@ - @5W%t#1K@ S7300/ET200M station_1 |> Component: @6W%t#276K@ / @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ |> Path: @6W %t#266K@ / @6W%t#267K@ / @6W%t#268K@.@6W %t#269K@ HW_ID= @6W%t#264K@ @6W%t#262K@ @6W%t#263K@
Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			
Name	SDIAG_ALCAT_RACK_MSG_0004	**Type**	Alarm_s
ID	1610619740	**Range**	1
Location	PLC_1	**Alarm text**	Fault: @1W%t#1K@ - @5W%t#1K@ S7300/ET200M station_1 |> Component: @6W%t#276K@ / @6W%t#257K@ |> Path: @6W%t#266K@ / @6W%t#267K@ HW_ID= @6W %t#264K@ @6W%t#262K@ @6W%t#263K@
Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			
Name	Module_message_0076	**Type**	Alarm_s
ID	1610619854	**Range**	16
Location	PLC_1	**Alarm text**	Fault: @1W%t#6K@ - @5W%t#6K@ S7300/ET200M station_1 |> Component: @6W%t#276K@ / @6W%t#257K@ / @6W%t#258K@.@6W%t#259K@ |> Path: @6W %t#266K@ / @6W%t#267K@ / @6W%t#268K@.@6W %t#269K@ HW_ID= @6W%t#264K@ @6W%t#262K@ @6W%t#263K@
Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			
Name	SDIAG_ALCAT_CH_ERR_MSG_0115	**Type**	Alarm_s
ID	1610620013	**Range**	16
Location	PLC_1	**Alarm text**	Fault: @1W%t#6K@ - @5W%t#6K@ on @2W%1d@ |> Component: @6W%t#276K@ / @6W%t#257K@ / @6W %t#258K@.@6W%t#259K@ |> Path: @6W%t#266K@ / @6W%t#267K@ / @6W%t#268K@.@6W%t#269K@ / @2W %1d@ HW_ID= @6W%t#264K@ @6W%t#262K@ @6W %t#263K@

Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			

Name	SDIAG_ALCAT_CH_MD_MSG_0118	**Type**	Alarm_s
ID	1610620016	**Range**	16
Location	PLC_1	**Alarm text**	Maintenance demanded: @1W%t#6K@ - @5W%t#6K@ S7300/ET200M station_1 |> Component: @6W %t#276K@ / @6W%t#257K@ / @6W%t#258K@.@6W %t#259K@ |> Path: @6W%t#266K@ / @6W%t#267K@ / @6W%t#268K@.@6W%t#269K@ HW_ID= @6W %t#264K@ @6W%t#262K@ @6W%t#263K@
Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			

Name	SDIAG_ALCAT_CH_MR_MSG_011B	**Type**	Alarm_s
ID	1610620019	**Range**	16
Location	PLC_1	**Alarm text**	Maintenance required: @1W%t#6K@ - @5W%t#6K@ S7300/ET200M station_1 |> Component: @6W %t#276K@ / @6W%t#257K@ / @6W%t#258K@.@6W %t#259K@ |> Path: @6W%t#266K@ / @6W%t#267K@ / @6W%t#268K@.@6W%t#269K@ HW_ID= @6W %t#264K@ @6W%t#262K@ @6W%t#263K@
Info text	Short name: @6W%t#260K@ Order number: @6W %t#265K@	**Alarm class**	No Acknowledgement
Acknowledgment	False	**Priority**	0
Display class	0	**Report**	False
Created by	Report system errors	**Date created**	10/14/2022 3:59 PM
Last change	10/16/2022 11:35 AM	**Group ID**	0
Additional text 1		**Additional text 2**	
Additional text 3		**Additional text 4**	
Additional text 5		**Additional text 6**	
Additional text 7		**Additional text 8**	
Additional text 9			

Flow / PLC_1 [CPU 314C-2 PN/DP]

PLC alarm text lists

This folder is empty.

Flow / PLC_1 [CPU 314C-2 PN/DP] / Local modules

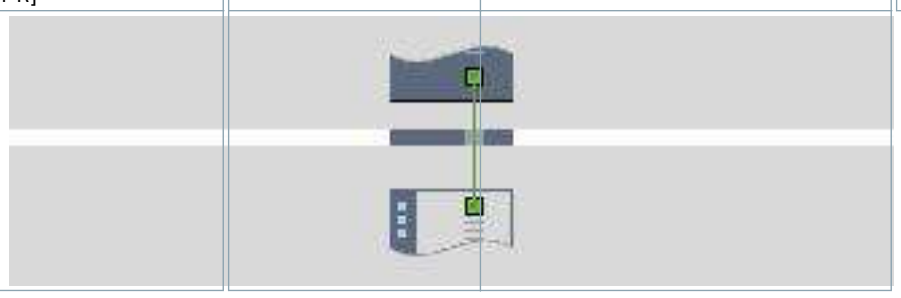
PS 307 2A_1

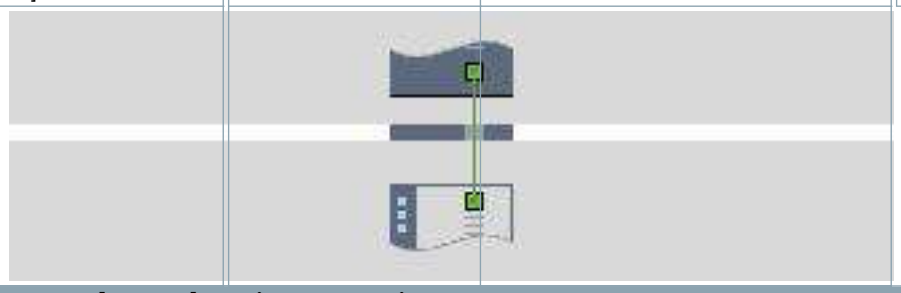
This folder is empty.

Flow / PLC_1 [CPU 314C-2 PN/DP] / Local modules

PLC_1 [CPU 314C-2 PN/DP]

PLC_1					
General					
Name	PLC_1	**Author**	home	**Comment**	
Rack	0	**Slot**	2		
General\Catalog information					
Short designation	CPU 314C-2 PN/DP	**Description**	Work memory 192KB; 0.6ms/1000 instructions; DI24/DO16; AI5/AO2 integrated; 4 pulse outputs (2.5kHz); 4 channels counting and measuring with 24 V (60kHz) incremental encoders; integrated positioning function; PROFINET interface and 2 Ports; MRP; PROFINET CBA; PROFINET CBA Proxy; TCP/IP transport protocol; combined MPI/DP interface (MPI or DP master or DP slave); multi-tier configuration up to 31 modules; capable of sending and receiving in direct data exchange; constant bus cycle time; routing; firmware V3.3	**Article number**	6ES7 314-6EH04-0AB0
Firmware version	V3.3				
General\Identification & Maintenance					
Plant designation		**Location identifier**			
MPI/DP interface [X1]\General					
Name	MPI/DP interface_1	**Comment**			
MPI/DP interface [X1]\MPI address\Interface networked with					
Subnet:	Not networked				
MPI/DP interface [X1]\MPI address\Parameters					
Interface type:	Mpi	**Address:**	2	**Highest address:**	
Transmission speed:					
MPI/DP interface [X1]\Time-of-day synchronization\SIMATIC mode					
Type of synchronization	None	**Time interval**	None		
MPI/DP interface [X1]\Diagnostics addresses\Diagnostics addresses					
Start address	2047				
PROFINET interface [X2]\General					
Name	PROFINET interface_1	**Comment**			
PROFINET interface [X2]\Ethernet addresses\Interface networked with					
Subnet:	Not connected				
PROFINET interface [X2]\Ethernet addresses\IP protocol					
IP configuration	Set IP address in the project	**IP address:**	192.168.0.1	**Subnet mask:**	255.255.255.0
Use router	False				
PROFINET interface [X2]\Ethernet addresses\PROFINET					
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True	**PROFINET device name:**	plc_1
Converted name:	plcxb1d0ed	**Device number:**	0		
PROFINET interface [X2]\Time-of-day synchronization\NTP mode					
Enable time synchronization via NTP server	False		IP addresses	**Server 1**	0.0.0.0
Server 2	0.0.0.0	**Server 3**	0.0.0.0	**Server 4**	0.0.0.0
Update interval	10s				
PROFINET interface [X2]\Operating mode					
IO controller	True	**IO system**		**Device number**	0
IO device	False				
PROFINET interface [X2]\Advanced options\Interface options					
Call the user program if communication errors occur	False	**Support device replacement without exchangeable medium**	True	**Use IEC V2.2 LLDP mode**	True
Keep-Alive connection monitoring:	30s				
PROFINET interface [X2]\Advanced options\Real time settings\IO communication					
Send clock:	1.000ms				
PROFINET interface [X2]\Advanced options\Real time settings\Synchronization					
RT class:	RT,IRT				
PROFINET interface [X2]\Advanced options\Real time settings\Real time options					
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%		
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\General					
Name	Port_1	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X2]\Port_1 [X2 P1 R]	**Medium:**	Copper	**Cable name:**	---



PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P1 R]\Diagnostics addresses\Diagnostics addresses					
Start address	2045				
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\General					
Name	Port_2	**Comment**			
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port interconnection\Local port:					
Local port:	PLC_1\PROFINET interface_1 [X2]\Port_2 [X2 P2 R]	**Medium:**	Copper	**Cable name:**	---
					
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port interconnection\Partner port:					
	Monitoring of partner port is not possible	**Partner port:**	Any partner		
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port options\Activate					
Activate this port for use	True				
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port options\Connection					
Transmission rate / duplex:	Automatic	**Monitor**	False	**Enable autonegotiation**	True
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Port options\Boundaries					
End of detection of accessible devices	False	**End of topology discovery**	False	**End of the sync domain**	False
PROFINET interface [X2]\Advanced options\Port [X2 P2 R]\Diagnostics addresses\Diagnostics addresses					
Start address	2044				
PROFINET interface [X2]\Diagnostics addresses\Diagnostics addresses					
Start address	2046				
DI 24/DO 16\General					
Name	DI 24/DO 16_1	**Comment**			
DI 24/DO 16\General\Catalog information					
Short designation	DI 24/DO 16	**Description**	Digital input/output DI24 + DO16		
DI 24/DO 16\Inputs\Channel group 0 - 3					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 0\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 1\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 2\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 0 - 3\Hardware interrupt channel 3\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7					
Input delay	3ms				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 4\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 5\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Rising (positive) edge					
Rising (positive) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 6\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Rising (positive) edge					
Rising (positive) edge	False				

DI 24/DO 16\Inputs\Channel group 4 - 7\Hardware interrupt channel 7\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 8\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 9\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 10\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 8 - 11\Hardware interrupt channel 11\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 12\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 13\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 14\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 12 - 15\Hardware interrupt channel 15\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 16\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 17\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 18\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 16 - 19\Hardware interrupt channel 19\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23	
Input delay	3ms
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 20\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 21\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Rising (positive) edge	
Rising (positive) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 22\Falling (negative) edge	
Falling (negative) edge	False
DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Rising (positive) edge	
Rising (positive) edge	False

DI 24/DO 16\Inputs\Channel group 20 - 23\Hardware interrupt channel 23\Falling (negative) edge					
Falling (negative) edge	False				
DI 24/DO 16\I/O addresses\Input addresses					
Start address	136.0	**End address**	138.7	**Process image**	OB1-PI
Interrupt OB number	40				
DI 24/DO 16\I/O addresses\Output addresses					
Start address	136.0	**End address**	137.7	**Process image**	OB1-PI
AI 5/AO 2\General					
Name	AI 5/AO 2_1	**Comment**			
AI 5/AO 2\General\Catalog information					
Short designation	AI 5/AO 2	**Description**	Analog I/O AI5 + AO2		
AI 5/AO 2\Inputs					
Temperature unit	Degrees Celsius				
AI 5/AO 2\Inputs\Channel 0					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 1					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 2					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 3					
Measurement type	Voltage	**Measuring range**	+/- 10V	**Interference frequency suppression**	50Hz
Integration time	20ms				
AI 5/AO 2\Inputs\Channel 4					
Measurement type	Resistor (2-wire)	**Measuring range**	600 ohmsOhm		
AI 5/AO 2\Outputs\Output 0					
Output type	Voltage	**Output range**	+/- 10V		
AI 5/AO 2\Outputs\Output 1					
Output type	Voltage	**Output range**	+/- 10V		
AI 5/AO 2\I/O addresses\Input addresses					
Start address	800	**End address**	809	**Process image**	None
Interrupt OB number	40				
AI 5/AO 2\I/O addresses\Output addresses					
Start address	800	**End address**	803	**Process image**	None
Count\General					
Name	Count_1	**Comment**			
Count\General\Catalog information					
Short designation	Count	**Description**	4 channels; counting and frequency measurement at 60 kHz, pulse width modulation at 2.5 kHz switching frequency		
Count\Interrupt selection					
Interrupt selection	None				
Count\Channel 0					
Operating mode	Not configured				
Count\Channel 1					
Operating mode	Not configured				
Count\Channel 2					
Operating mode	Not configured				
Count\Channel 3					
Operating mode	Not configured				
Count\I/O addresses\Input addresses					
Start address	816	**End address**	831	**Process image**	None
Interrupt OB number	40				
Count\I/O addresses\Output addresses					
Start address	816	**End address**	831	**Process image**	None
Positioning\General					
Name	Positioning_1	**Comment**			
Positioning\General\Catalog information					
Short designation	Positioning	**Description**	1 channel; positioning with analog and digital outputs, counting frequency		
Positioning\Interrupt selection					
Interrupt selection	None				
Positioning\Channel 0					
Operating mode	None				
Positioning\I/O addresses\Input addresses					
Start address	832	**End address**	847	**Process image**	None
Interrupt OB number	40				
Positioning\I/O addresses\Output addresses					
Start address	832	**End address**	847	**Process image**	None
Startup					
Startup if preset configuration does not match actual configuration	True	**Startup after POWER ON**	Warm restart		
Startup\Monitoring time for					
Ready message from modules	650x 100 ms	**Parameter transfer to modules**	100x 100 ms		

Cycle					
Cycle monitoring time	150ms	**Cycle load due to communication**	20%	**Size of the process image input:**	256
Size of the process image output:	256	**OB85 call if I/O access error occurs**	No OB85 call		
Clock memory					
Clock memory	False	**Memory byte**	0		

Interrupts\Isochronous mode interrupts

OB number	Priority	Distributed I/O	Process image partition(s)	Delay time (ms)	Automatic setting
OB 61	25	0		0.000	True

Interrupts\Isochronous mode interrupts\OB 61

Application cycle:	0ms	**Delay time:**	0.000ms	**Automatic setting**	True
Distributed I/O:	0				

Interrupts\Isochronous mode interrupts\OB 61\Process image partition

PIP:	

Interrupts\Time-of-day interrupts

OB number	Priority	Active	Execution	Start time
OB 10	2	False	None	1994-01-01 00:00:00.000

Interrupts\Time-delay interrupts

OB number	Priority	Process image partition(s)
OB 20	3	None
OB 21	4	None

Interrupts\Cyclic interrupts

OB number	Priority	Execution	Phase offset	Unit
OB 32	9	1000	0	ms
OB 33	10	500	0	ms
OB 34	11	200	0	ms
OB 35	12	500	0	ms

Interrupts\Hardware interrupts

OB number	Priority
OB 40	16

Interrupts\Interrupts for DPV1

OB number	Priority
OB 55	2
OB 56	2
OB 57	2

Interrupts\Asynchronous error interrupts

OB number	Priority
OB 82	26
OB 83	26
OB 85	26
OB 86	26
OB 87	26

Retentive memory					
Number of memory bytes starting at MB 0	16	**Number of S7 timers starting at T 0**	0	**Number of S7 counters starting at C 0**	8
Protection					
Password		**Confirm password**			
Protection					
Level of protection	No protection				
Protection\ \Can be canceled with password					
Can be canceled with password	False				
Diagnostics system					
Report cause of STOP	True	**Number of alarms in the diagnostics buffer**	10		
System diagnostics\General					
Activate system diagnostics for this device	True				
System diagnostics\Diagnostic support					
Query for status "activated/deactivated" after startup	False	**Send alarm if status changes from/to activated or deactivated**	False		

Additional blocks for diagnostic data	Create	Block name	Block number
Diagnostic status DB:	True	RSE_DIAGNOSTIC_STATUS_DB	127

System diagnostics\System diagnostic blocks

System diagnostic blocks	Block name	Block number
FB:	RSE_FB	49
DB:	RSE_DB	49
Global DB:	RSE_GLOBAL_DB	50
FC:	RSE_FC	49

Time of day					
Correction factor	0ms				
Time of day\Synchronization on PLC					
Type of synchronization	None	**Time interval**	None		
Time of day\Synchronization on MPI					
Type of synchronization	None	**Time interval**	None		
Web server\General					
Activate web server on this module	False	**Permit access only with HTTPS**	False		
Web server\Automatic update					
Enable automatic update	False	**Update interval**	0s		

Web server\Languages		
Active	**Web server language**	**Assign project language**
False	German	None
False	English	None
False	French	None
False	Spanish	None
False	Italian	None
False	Japanese	None
False	Chinese (simplified)	None

Web server\User management	
User name	**User rights**
Everybody	

Web server\User-defined web pages					
Application name	**HTML source path**	**Default HTML page**	**Files with dynamic content**	**Web DB number**	**Fragment DB number**
		index.htm	.htm;.html	333	334

Web server\Text_Display_classes_of_messages	
Display class	**Active**
0	True
1	True
2	True
3	True
4	True
5	True
6	True
7	True
8	True
9	True
10	True
11	True
12	True
13	True
14	True
15	True
16	True

Connection resources					
PG communication:	1	**OP communication:**	1	**S7 basic communication:**	0
S7 communication:	0	**Maximum number of S7 connection resources:**	12		

Overview of addresses\Overview of addresses\Overview of addresses					
Inputs	True	**Outputs**	True	**Address gaps**	False
Slot	True				

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I*	2047	2047	MPI/DP interface_1	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X1
I*	2046	2046	PROFINET interface_1	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X2
I*	2045	2045	Port_1	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X2 P1 R
I*	2044	2044	Port_2	---	PLC_1 [CPU 314C-2 PN/DP]	-	0 Bits	-	0	2 X2 P2 R
I	136	138	DI 24/DO 16_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	3 Bytes	-	0	2 5
O	136	137	DI 24/DO 16_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	2 Bytes	-	0	2 5
I	800	809	AI 5/AO 2_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	10 Bytes	-	0	2 6
O	800	803	AI 5/AO 2_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	4 Bytes	-	0	2 6
I	816	831	Count_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 7
O	816	831	Count_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 7
I	832	847	Positioning_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 8
O	832	847	Positioning_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	16 Bytes	-	0	2 8
I	256	259	AI 2x12BIT_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	4 Bytes	-	0	4
O	272	275	AO 2x12BIT_1	OB1-PI	PLC_1 [CPU 314C-2 PN/DP]	-	4 Bytes	-	0	5

Flow / PLC_1 [CPU 314C-2 PN/DP] / Local modules

AI 2x12BIT_1

AI 2x12BIT_1					
General					
Name	AI 2x12BIT_1	**Author**	home	**Comment**	
Rack	0	**Slot**	4		
General\Catalog information					
Short designation	AI 2x12BIT	**Description**	Analog input module AI2 x U/I/R/RTD/TC; 14 bits of resolution; accuracy appr. 1%; grouping 2; common mode voltage appr. 2.3VDC; configurable diagnostics; hardware interrupts; 20-pin front connector	**Article number**	6ES7 331-7KB02-0AB0
Firmware version					
Inputs\General\Diagnostics					
Diagnostics interrupt	Deactivated				
Inputs\General\Hardware interrupts					
Hardware interrupt when limit violated	Deactivated	**RidPrefixHwInterrupt**	49152	**Event name:**	0
Hardware interrupt:	0	**Hardware interrupt**	Hardware interrupt	**HardwareInterruptChannelForModule**	32768
HardwareInterruptEventIdNull	0				
Inputs\Channel 0 - 1\Diagnostics					
Group diagnostics	Deactivated	**Check for wire break**	Deactivated		
Inputs\Channel 0 - 1\Measuring					
Measurement type	Voltage	**Measuring range**	+/- 10 V	**Position of measuring range selection module**	[B]
Interference frequency suppression	50Hz	**Integration time**	20ms		
Inputs\Channel 0 - 1\Trigger for hardware interrupt\Channel 0					
High limit		**Low limit**			
I/O addresses\Input addresses					
Start address	256	**End address**	259	**Process image**	None
Interrupt OB number	40				

Flow / PLC_1 [CPU 314C-2 PN/DP] / Local modules

AO 2x12BIT_1

AO 2x12BIT_1					
General					
Name	AO 2x12BIT_1	**Author**	home	**Comment**	
Rack	0	**Slot**	5		
General\Catalog information					
Short designation	AO 2x12BIT	**Description**	Analog output module AO2 x U/I 12bits of resolution; accuracy appr. 0.6%; grouping 2; common mode voltage appr. 3VDC; configurable diagnostics; configurable substitute value for output; 20-pin front connector	**Article number**	6ES7 332-5HB01-0AB0
Firmware version					
Outputs\Enable					
Diagnostics interrupt	Deactivated				
Outputs\Channel 0\Diagnostics					
Group diagnostics	Deactivated				
Outputs\Channel 0\Output					
Output type	Current	**Output range**	4 to 20 mA	**Reaction to CPU STOP**	Output has no current or voltage
Substitute value					
Outputs\Channel 1\Diagnostics					
Group diagnostics	Deactivated				
Outputs\Channel 1\Output					
Output type	Current	**Output range**	4 to 20 mA	**Reaction to CPU STOP**	Output has no current or voltage
Substitute value					
I/O addresses\Output addresses					
Start address	272	**End address**	275	**Process image**	None

Flow

Ungrouped devices

This folder is empty.

Flow

Security settings

This folder is empty.

Flow / Cross-device functions / Project traces

Measurements

This folder is empty.

Flow / Common data

Alarm classes

Alarm classes			
Name	**Display name**	**Acknowledgment**	**Priority**
Acknowledgement	A	True	0
No Acknowledgement	NA	False	0

Flow / Common data

Logs

This folder is empty.

Flow / Languages & resources

Project languages

Languages
Reference language
English (United States)

Editing language
English (United States)

Other project languages
Empty

Flow / Languages & resources / Project texts

Project texts

Project texts		
English (United States)	**Category**	**Reference**
	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\Network 2\Comment
	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\Network 3\Comment
	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\Network 4\Comment
	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\Network 5\Comment
	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\Network 1\Comment
This DB contains the generated data from STEP 7 Report System Errors.	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_GLOBAL_DB [DB50]\Data block comment
This DB contains the generated data from STEP 7 Report System Errors.	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DB [DB49]\Data block comment
This DB is part of report system error and will be updated by the RSE FB. It is an interface for support of system diagnosis via the CPU web server.	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Data block comment
This FB contains the code generated from STEP 7 Report System Errors. It is called in the error OBs and in OB1 or a cyclic interrupt OB. The FB evaluates the system errors and displays them by means of an Alarm_S to the display device. While the FB executes, all interrupts are delayed.	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_FB [FB49]\Block comment
This FC contains the code generated from STEP 7 Report System Errors.	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_FC [FC49]\Block comment
"Cyclic Interrupt"	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\CYC_INT5 [OB35]\Block title
"I/O Point Fault"	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\I/O_FLT1 [OB82]\Block title
"I/O Point Fault"	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\I/O_FLT2 [OB83]\Block title
"Loss Of Rack Fault"	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\RACK_FLT [OB86]\Block title
"Main Program Sweep (Cycle)"	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\Main [OB1]\Block title
"Organization Block (OB) Not Loaded Fault"	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\Block title
16#35 Event class 3	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_EV_CLASS
16#XX, Fault identification code	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_FLT_ID
85 (Organization block 85, OB85)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_OB_NUMBR
A	Alarm class text	Flow\Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
A	Alarm class text	Flow\Acknowledgement\ShortName
Addressing mode of the module	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Component.C_AddressMode
An underlying component is disrupted	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_SubFault
AS-iMaster gibt AS-iSlave Alarme zurück	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_SubComponentAlarm
Associated values of the message	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_AssociatedValue
At least one lower-level component is disabled	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_SubDeactivated
Byte offset to the start of the "Alarm" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pAlarm
Byte offset to the start of the "Component" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pComponent
Byte offset to the start of the "Error" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pError
Byte offset to the start of the "GlobalState" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pGlobalState
Byte offset to the start of the "Query" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pQuery
Byte offset to the start of the "State" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pState
Byte offset to the start of the "Subcomponent" section	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_pSubComponent
Call system diagnostics block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\I/O_FLT1 [OB82]\Network 1\Title
Call system diagnostics block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\I/O_FLT2 [OB83]\Network 1\Title
Call system diagnostics block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\RACK_FLT [OB86]\Network 1\Title
Call system diagnostics block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\Main [OB1]\Network 7\Title
Class of event causing error	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_ERR_EV_CLASS
Date and time OB85 started	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_DATE_TIME
Hardware ID of the component (internal)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Component.C_ComponentID
Hardware ID of the component (internal)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_ComponentID
ID of the client	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_ClientID_User
ID of the client (internal)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_ClientID_Intern
ID of the first error text	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_TextID1
ID of the first error text in the export file (HMI)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_MapTextID
ID of the first error text lexicon	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_TextLexikonID1
ID of the first help text in the export file (HMI)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_MapHelpTextID
ID of the first help text lexicon	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_HelpTextLexikonID1
ID of the last event (counter)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\GlobalState.G_EventCount

English (United States)	Category	Reference
ID of the second error text	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_TextID2
ID of the second error text in the export file (HMI)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_MapTextID2
ID of the second error text lexicon	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_TextLexikonID2
ID of the second help text in the export file (HMI)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_MapHelpTextID2
ID of the second help text lexicon	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_HelpTextLexikonID2
Index of the requested/actual error	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Error.E_ErrorNo
Internal error in query	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Error
List of channels involved; Valid only when Q_WithSubComponent is set	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_ChannelVector
List of status of subordinate components, 1 byte per component	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\SubComponent.U_SubComponentFault
Maintenance demand is indicated	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Maintenance2
Maintenance demand is indicated for an underlying component	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_SubMaintenance2
Maintenance required is indicated	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Maintenance1
Maintenance required is indicated for an underlying component	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_SubMaintenance1
Maintenance state of the component	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_TIAMS
Maintenance state: Configured channels	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_TIAMSChannelExist
Maintenance State: Disrupted channels	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_TIAMSChannelOK
Message number	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_AlarmID
NA	Alarm class text	Flow\No Acknowledgement\AlarmClassData_IDisplayNaming_DisplayName
NA	Alarm class text	Flow\No Acknowledgement\ShortName
Number of channels; Valid only when Q_WithSubComponent is set	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_ChannelCount
Number of event causing error	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_ERR_EV_NUM
Number of OB causing error	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_OB_NUM
Number of other occupied bytes	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Alarm.A_ValueCount
Number of underlying components	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\SubComponent.U_SubComponentCount
Priority of OB causing error	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_OB_PRIOR
Priority of OB Execution	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_PRIORITY
Report System Error Diagnostic Block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_FC [FC49]\Block title
Report System Error Diagnostic Block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_FB [FB49]\Block title
Report System Error Diagnostic Block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Data block title
Report System Error Diagnostic Block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_GLOBAL_DB [DB50]\Data block title
Report System Error Diagnostic Block	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DB [DB49]\Data block title
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Reserved7
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Reserved6
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Reserved5
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved5
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved4
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved6
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Reserved2
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved7
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved3
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved8
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Reserved2
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Component.C_Reserved1
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Periphery
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Hierarchy
Reserved	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Reserved4
Reserved for system	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_RESERVED_1
Reserved for system	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_RESERVED_3
Reserved for system	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\OBNL_FLT [OB85]\OB85_RESERVED_2

English (United States)	Category	Reference
Start query	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_Start
Startup evaluation running	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\GlobalState.G_StartReporting
The component does not exist	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_NotAvailable
The component is disrupted; the "Alarm" section is not empty	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Faulty
The component is not obtainable	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_SupFault
The component was deactivated	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_Deactivated
There are more errors than RSE can store.	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\State.S_MoreErrors
TRUE, if E_ErrorNo contains the indes of the last error, otherwise FALSE	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Error.E_LastError
Version that supports RSE	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Directory.D_Version
With/without status of the underlying components (slower)	Block comment	Flow\PLC_1 [CPU 314C-2 PN/DP]\Program blocks\System blocks\System diagnostics\RSE_DIAGNOSTIC_STATUS_DB [DB127]\Query.Q_WithSubComponent